International Rectifier

IRF540NPbF

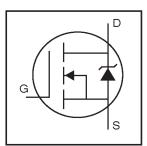
HEXFET® Power MOSFET

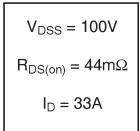
- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

Description

Advanced HEXFET® Power MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The TO-220 package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 watts. The low thermal resistance and low package cost of the TO-220 contribute to its wide acceptance throughout the industry.







Absolute Maximum Ratings

	Parameter	Max.	Units	
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V	33	33	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V	23	A	
I _{DM}	Pulsed Drain Current ①	110]	
P _D @T _C = 25°C	Power Dissipation	130	W	
	Linear Derating Factor	0.87	W/°C	
V _{GS}	Gate-to-Source Voltage	± 20	V	
I _{AR}	Avalanche Current①	16	A	
E _{AR}	Repetitive Avalanche Energy①	13	mJ	
dv/dt	Peak Diode Recovery dv/dt ③	7.0	V/ns	
TJ	Operating Junction and	-55 to + 175		
T _{STG}	Storage Temperature Range		_	
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)		
	Mounting torque, 6-32 or M3 srew	10 lbf•in (1.1N•m)		

Thermal Resistance

	Parameter	Тур.	Max.	Units
R _{eJC}	Junction-to-Case		1.15	
R _{θCS}	Case-to-Sink, Flat, Greased Surface	0.50		°C/W
R _{0JA}	Junction-to-Ambient		62	

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	100			٧	V _{GS} = 0V, I _D = 250μA
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient		0.12		V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance			44	mΩ	V _{GS} = 10V, I _D = 16A ④
V _{GS(th)}	Gate Threshold Voltage	2.0		4.0	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
9fs	Forward Transconductance	21			S	V _{DS} = 50V, I _D = 16A@
I	Drain-to-Source Leakage Current			25	μΑ	V _{DS} = 100V, V _{GS} = 0V
I _{DSS}	Brain to Gource Leakage Guiterit			250	"^	$V_{DS} = 80V, V_{GS} = 0V, T_{J} = 150$ °C
1	Gate-to-Source Forward Leakage			100	nA	V _{GS} = 20V
I _{GSS}	Gate-to-Source Reverse Leakage			-100	''^	V _{GS} = -20V
Qg	Total Gate Charge			71		I _D = 16A
Q _{gs}	Gate-to-Source Charge			14	nC	$V_{DS} = 80V$
Q _{gd}	Gate-to-Drain ("Miller") Charge			21		V_{GS} = 10V, See Fig. 6 and 13
t _{d(on)}	Turn-On Delay Time		11			$V_{DD} = 50V$
t _r	Rise Time		35		ns	$I_D = 16A$
t _{d(off)}	Turn-Off Delay Time		39		115	$R_G = 5.1\Omega$
t _f	Fall Time		35			V _{GS} = 10V, See Fig. 10 ⊕
1	Internal Drain Inductance		4.5			Between lead,
L _D	internal Diam inductance		4.5		nH	6mm (0.25in.)
,	Intomal Course Industrias		7.5			from package
L _S	Internal Source Inductance		7.5	_		and center of die contact
C _{iss}	Input Capacitance		1960			V _{GS} = 0V
Coss	Output Capacitance		250			$V_{DS} = 25V$
C _{rss}	Reverse Transfer Capacitance		40		pF	f = 1.0MHz, See Fig. 5
E _{AS}	Single Pulse Avalanche Energy®		700®	185©	mJ	I _{AS} = 16A, L = 1.5mH

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			33		MOSFET symbol
	(Body Diode)		33	A	showing the	
I _{SM}	Pulsed Source Current			110	110	integral reverse
	(Body Diode)①		110		p-n junction diode.	
V _{SD}	Diode Forward Voltage			1.2	V	$T_J = 25^{\circ}C$, $I_S = 16A$, $V_{GS} = 0V$ ④
t _{rr}	Reverse Recovery Time		115	170	ns	T _J = 25°C, I _F = 16A
Q _{rr}	Reverse Recovery Charge		505	760	nC	di/dt = 100A/µs ⊕
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L _S +L _D)				

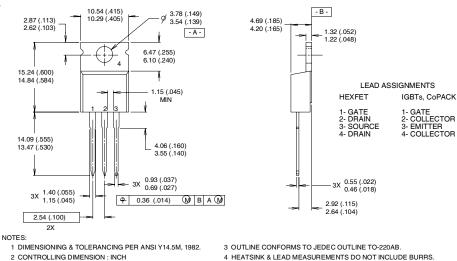
Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)
- $\begin{tabular}{ll} \hline \& Starting $T_J=25^\circ$C, $L=1.5mH$\\ \hline $R_G=25\Omega$, $I_{AS}=16A$. (See Figure 12) \\ \hline \end{tabular}$
- 3 I $_{SD}$ \leq 16A, di/dt \leq 340A/ μ s, V_{DD} \leq $V_{(BR)DSS}$, T_{J} \leq 175°C
- ⑤ This is a typical value at device destruction and represents operation outside rated limits.
- 6 This is a calculated value limited to $T_J = 175^{\circ}\text{C}$.

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TO-220AB Package Outline

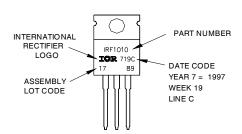
Dimensions are shown in millimeters (inches)



2 CONTROLLING DIMENSION : INCH

TO-220AB Part Marking Information

EXAMPLE: THIS IS AN IRF1010 LOT CODE 1789 ASSEMBLED ON WW 19, 1997 IN THE ASSEMBLY LINE "C" Note: "P" in assembly line position indicates "Lead-Free"



Data and specifications subject to change without notice. This product has been designed and qualified for the industrial market.

