

LM79XX Series

3-Terminal Negative Regulators

General Description

The LM79XX series of 3-terminal regulators is available with fixed output voltages of –5V, –12V, and –15V. These devices need only one external component—a compensation capacitor at the output. The LM79XX series is packaged in the TO-220 power package and is capable of supplying 1.5A of output current.

These regulators employ internal current limiting safe area protection and thermal shutdown for protection against virtually all overload conditions.

Low ground pin current of the LM79XX series allows output voltage to be easily boosted above the preset value with a

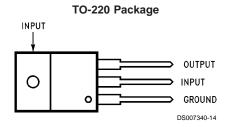
resistor divider. The low quiescent current drain of these devices with a specified maximum change with line and load ensures good regulation in the voltage boosted mode.

For applications requiring other voltages, see LM137 datasheet.

Features

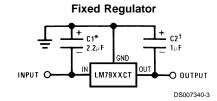
- Thermal, short circuit and safe area protection
- High ripple rejection
- 1.5A output current
- 4% tolerance on preset output voltage

Connection Diagrams



Front View
Order Number LM7905CT, LM7912CT or LM7915CT
See NS Package Number TO3B

Typical Applications



*Required if regulator is separated from filter capacitor by more than 3". For value given, capacitor must be solid tantalum. 25µF aluminum electrolytic may be substituted.

†Required for stability. For value given, capacitor must be solid tantalum. 25µF aluminum electrolytic may be substituted. Values given may be increased without limit.

For output capacitance in excess of $100\mu F$, a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Input Voltage

 $(V_o = -5V)$ $(V_o = -12V \text{ and } -15V)$ Input-Output Differential

 $(V_o = -5V)$

 $(V_o = -12V \text{ and } -15V)$

Internally Limited

Power Dissipation (Note 2)
Operating Junction Temperature Range

0°C to +125°C

25V

30V

Storage Temperature Range Lead Temperature (Soldering, 10 sec.) -65°C to +150°C 230°C

Electrical Characteristics

Conditions unless otherwise noted: I_{OUT} = 500mA, C_{IN} = 2.2 μ F, C_{OUT} = 1 μ F, 0°C \leq T $_{J}$ \leq +125°C, Power Dissipation \leq 1.5W.

-25V

-35V

Part Number				LM7905C				
	Output	Voltage		-5V				
	Input Voltage (unless	otherwise specified)	ed) –10V					
Symbol	Parameter	Conditions	Min	Тур	Max			
Vo	Output Voltage	$T_J = 25^{\circ}C$	-4.8	-5.0	-5.2	V		
		$5mA \le I_{OUT} \le 1A$,	-4.75		-5.25	V		
		P ≤ 15W		$(-20 \le V_{IN} \le -7)$	7)	V		
ΔV_{O}	Line Regulation	$T_J = 25^{\circ}C$, (Note 3)		8	50	mV		
				$(-25 \le V_{IN} \le -7)$				
				2	15	mV		
				$(-12 \le V_{IN} \le -8)$				
ΔV_{O}	Load Regulation	$T_J = 25^{\circ}C$, (Note 3)						
		5mA ≤ I _{OUT} ≤ 1.5A		15	100	mV		
		250mA ≤ I _{OUT} ≤ 750mA		5	50	mV		
I _Q	Quiescent Current	$T_J = 25^{\circ}C$		1	2	mA		
ΔI_Q	Quiescent Current	With Line			0.5	mA		
	Change			$(-25 \le V_{IN} \le -7)$	7)	V		
		With Load, 5mA ≤ I _{OUT} ≤ 1A			0.5	mA		
V _n	Output Noise Voltage	$T_A = 25^{\circ}C, 10Hz \le f \le 100Hz$		125		μV		
	Ripple Rejection	f = 120Hz	54	66		dB		
				$(-18 \le V_{IN} \le -8)$	3)	V		
	Dropout Voltage	$T_J = 25^{\circ}C$, $I_{OUT} = 1A$		1.1		V		
I _{OMAX}	Peak Output Current	$T_J = 25^{\circ}C$		2.2		А		
	Average Temperature	I _{OUT} = 5mA,		0.4		mV/°C		
	Coefficient of	$0 \text{ C} \leq \text{T}_{\text{J}} \leq 100^{\circ}\text{C}$						
	Output Voltage							

Electrical Characteristics

 $Conditions \ unless \ otherwise \ noted: \ I_{OUT} = 500mA, \ C_{IN} = 2.2 \mu F, \ C_{OUT} = 1 \mu F, \ 0^{\circ}C \leq T_{J} \leq +125^{\circ}C, \ Power \ Dissipation \leq 1.5W.$

Part Number Output Voltage			LM7912C -12V			LM7915C -15V			Units
Symbol	Parameter	Conditions	Min	Тур	Max	Min	Тур	Max	
Vo	Output Voltage	$T_J = 25^{\circ}C$	-11.5	-12.0	-12.5	-14.4	-15.0	-15.6	V
		5mA ≤ I _{OUT} ≤ 1A,	-11.4		-12.6	-14.25		-15.75	V
		P ≤ 15W	(-27	≤ V _{IN} ≤	–14.5)	(-30	≤ V _{IN} ≤	–17.5)	V
ΔV_{O}	Line Regulation	$T_J = 25^{\circ}C$, (Note 3)		5	80		5	100	mV
			(-30	≤ V _{IN} ≤	–14.5)	(-30	≤ V _{IN} ≤ -	–17.5)	V
				3	30		3	50	mV
			(-22	≤ V _{IN} ≤	-16)	(-26	$S \leq V_{IN} \leq$	≤–20)	V
ΔV_{O}	Load Regulation	$T_J = 25^{\circ}C$, (Note 3)							
ΔV_{O}	Load Regulation	$T_J = 25^{\circ}C$, (Note 3)							

Electrical Characteristics (Continued)

Conditions unless otherwise noted: $I_{OUT} = 500 \text{mA}$, $C_{IN} = 2.2 \mu\text{F}$, $C_{OUT} = 1 \mu\text{F}$, $0^{\circ}\text{C} \le T_{J} \le +125^{\circ}\text{C}$, Power Dissipation $\le 1.5 \text{W}$.

Part Number Output Voltage Input Voltage (unless otherwise specified)			LM7912C -12V -19V			LM7915C -15V -23V			Units										
										Symbol	Parameter	Conditions	Min	Тур	Max	Min	Тур	Max	
												5mA ≤ I _{OUT} ≤ 1.5A		15	200		15	200	mV
		250mA ≤ I _{OUT} ≤ 750mA		5	75		5	75	mV										
I _Q	Quiescent Current	$T_J = 25^{\circ}C$		1.5	3		1.5	3	mA										
ΔI_Q	Quiescent Current	With Line			0.5			0.5	mA										
	Change		$(-30 \le V_{IN} \le -14.5)$		$(-30 \le V_{IN} \le -17.5)$		V												
		With Load, 5mA ≤ I _{OUT} ≤ 1A			0.5			0.5	mA										
V _n	Output Noise Voltage	$T_A = 25^{\circ}C, 10Hz \le f \le 100Hz$		300			375		μV										
	Ripple Rejection	f = 120 Hz	54	70		54	70		dB										
			(-25	≤ V _{IN} ≤	-15)	(-30	$\leq V_{IN} \leq -$	-17.5)	V										
	Dropout Voltage	$T_{J} = 25^{\circ}C, I_{OUT} = 1A$		1.1			1.1		V										
I _{OMAX}	Peak Output Current	$T_J = 25^{\circ}C$	2.2			2.2		А											
	Average Temperature	$I_{OUT} = 5mA,$		-0.8			-1.0		mV/°C										
	Coefficient of	0 C ≤ T _J ≤ 100°C																	
	Output Voltage																		

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee Specific Performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics.

Note 2: Refer to Typical Performance Characteristics and Design Considerations for details.

Note 3: Regulation is measured at a constant junction temperature by pulse testing with a low duty cycle. Changes in output voltage due to heating effects must be taken into account.

Design Considerations

The LM79XX fixed voltage regulator series has thermal overload protection from excessive power dissipation, internal short circuit protection which limits the circuit's maximum current, and output transistor safe-area compensation for reducing the output current as the voltage across the pass transistor is increased.

Although the internal power dissipation is limited, the junction temperature must be kept below the maximum specified temperature (125°C) in order to meet data sheet specifications. To calculate the maximum junction temperature or heat sink required, the following thermal resistance values should be used:

	Тур	Max	Тур	Max
Package	θJC	θ _{JC}	θ_{JA}	θ_{JA}
	°C/W	°C/W	°C/W	°C/W
TO-220	3.0	5.0	60	40

$${\rm P_{D\,MAX}} \; = \; \frac{{\rm T_{J\,Max}} - {\rm T_{A}}}{\theta_{\rm JC}} \; {\rm or} \; \frac{{\rm T_{J\,Max}} \; {\rm T_{A}}}{\theta_{\rm JA}}$$

$$\theta_{\text{CA}} = \theta_{\text{CS}} + \theta_{\text{SA}}$$
 (without heat sink)

Solving for T_{.1}:

$$T_J = T_A + P_D (\theta_{JC} + \theta_{CA})$$
 or
= $T_A + P_D \theta_{JA}$ (without heat sink)

Where:

 T_J = Junction Temperature T_A = Ambient Temperature P_D = Power Dissipation θ_{JA} = Junction-to-Ambient Thermal Resistance

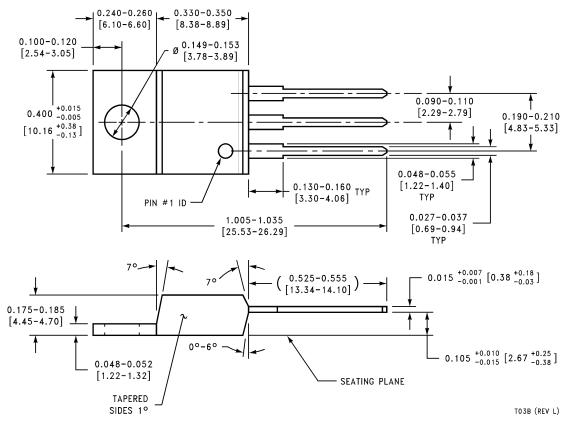
 θ_{JC} = Junction-to-Case Thermal Resistance

 θ_{CA} = Case-to-Ambient Thermal Resistance

 θ_{CS} = Case-to-Heat Sink Thermal Resistance

 θ_{SA} = Heat Sink-to-Ambient Thermal Resistance

Physical Dimensions inches (millimeters) unless otherwise noted



TO-220 Outline Package (T)
Order Number LM7905CT, LM7912CT or LM7915CT
NS Package Number T03B

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