

MICROCHIP TC1054/TC1055/TC1186

50 mA, 100 mA and 150 mA CMOS LDOs with Shutdown and ERROR Output

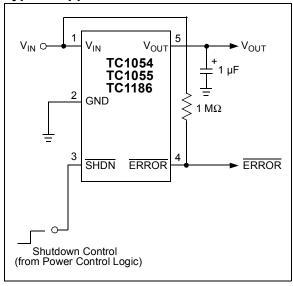
Features

- · Low Ground Current for Longer Battery Life
- · Low Dropout Voltage
- Choice of 50 mA (TC1054), 100 mA (TC1055) and 150 mA (TC1186) Output
- · High Output Voltage Accuracy
- · Standard or Custom Output Voltages
- · Power-Saving Shutdown Mode
- ERROR Output Can Be Used as a Low Battery Detector or Microcontroller Reset Generator
- · Over-Current and Over-Temperature Protection
- 5-Pin SOT-23A Package
- · Pin Compatible Upgrades for Bipolar Regulators

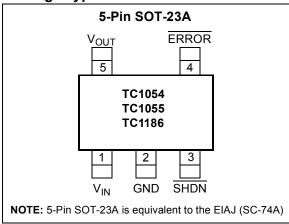
Applications

- · Battery Operated Systems
- · Portable Computers
- · Medical Instruments
- Instrumentation
- · Cellular/GSM/PHS Phones
- · Linear Post-Regulators for SMPS
- · Pagers

Typical Application



Package Type



General Description

The TC1054, TC1055 and TC1186 are high accuracy (typically $\pm 0.5\%$) CMOS upgrades for older (bipolar) low dropout regulators. Designed specifically for battery-operated systems, the devices' CMOS construction minimizes ground current, extending battery life. Total supply current is typically 50 μ A at full load (20 to 60 times lower than in bipolar regulators).

The devices' key features include low noise operation, low dropout voltage — typically 85 mV (TC1054), 180 mV (TC1055) and 270 mV (TC1186) at full load — and fast response to step changes in load. An error output (ERROR) is asserted when the devices are out-of-regulation (due to a low input voltage or excessive output current). ERROR can be used as a low battery warning or as a processor RESET signal (with the addition of an external RC network). Supply current is reduced to 0.5 μA (max), with both V_{OUT} and ERROR disabled when the shutdown input is low. The devices incorporate both over-temperature and over-current protection.

The TC1054, TC1055 and TC1186 are stable with an output capacitor of only 1 μ F and have a maximum output current of 50 mA, 100 mA and 150 mA, respectively. For higher output current regulators, please refer to the TC1173 (I_{OUT} = 300 mA) data sheet (DS21632).

1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

Input Voltage	6.5V
Output Voltage	(-0.3V) to (V _{IN} + 0.3V)
Power Dissipation	Internally Limited (Note 6)
Maximum Voltage on Any Pin	V _{IN} +0.3V to -0.3V
Operating Junction Temperature F	Range40°C < T _J < 125°C
Storage Temperature	65°C to +150°C

† Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of the specifications is not implied. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.

DC CHARACTERISTICS

Electrical Specifications: Unless otherwise noted, $V_{IN} = V_{OUT} + 1V$, $I_L = 100 \mu A$, $C_L = 3.3 \mu F$, $\overline{SHDN} > V_{IH}$, $T_A = +25^{\circ}C$.

Boldface type specifications apply for junction temperatures of -40°C to +125°C.						
Parameters	Sym	Min	Тур	Max	Units	Conditions
Input Operating Voltage	V _{IN}	2.7	_	6.0	V	Note 8
Maximum Output Current	I _{OUTMAX}	50	_	_	mA	TC1054
	331	100	_	_		TC1055
		150	_			TC1186
Output Voltage	V_{OUT}	$V_R - 2.5\%$	V _R ±0.5%	V _R + 2.5%	V	Note 1
V _{OUT} Temperature Coefficient	TCV _{OUT}	1	20	_	ppm/°C	Note 2
		_	40	_		
Line Regulation	$\Delta V_{OUT}/\Delta V_{IN}$	_	0.05	0.35	%	$(V_R + 1V) \le V_{IN} \le 6V$
Load Regulation:	$\Delta V_{OUT}/V_{OUT}$					(Note 3)
TC1054; TC1055		_	0.5	2	%	$I_L = 0.1 \text{ mA to } I_{OUTMAX}$
TC1186		1	0.5	3		$I_L = 0.1 \text{ mA to } I_{OUTMAX}$
Dropout Voltage:	V_{IN} - V_{OUT}	-	2	-	mV	I _L = 100 μA
		_	65	_		$I_L = 20 \text{ mA}$
		_	85	120		$I_L = 50 \text{ mA}$
TC1055; TC1186		_	180	250		$I_L = 100 \text{ mA}$
TC1186		_	270	400		I _L = 150 mA (Note 4)
Supply Current	I _{IN}	_	50	80	μΑ	$\overline{SHDN} = V_{IH}, I_L = 0 \mu A $ (Note 9)
Shutdown Supply Current	I _{INSD}	-	0.05	0.5	μΑ	SHDN = 0V
Power Supply Rejection Ratio	PSRR		64	_	dB	f≤1 kHz
Output Short Circuit Current	I _{OUTsc}	1	300	450	mA	V _{OUT} = 0V
Thermal Regulation	$\Delta V_{OUT}/\Delta P_{D}$	_	0.04	_	V/W	Notes 5, 6
Thermal Shutdown Die Temperature	T _{SD}	_	160	_	°C	
Thermal Shutdown Hysteresis	ΔT_{SD}		10		°C	

Note 1: V_R is the regulator output voltage setting. For example: V_R = 1.8V, 2.5V, 2.7V, 2.85V, 3.0V, 3.3V, 3.6V, 4.0V, 5.0V.

- 2: TC V_{OUT} = $\frac{(V_{OUTMAX} V_{OUTMIN})x \cdot 10^{6}}{V_{OUT} x \Delta T}$
- 3: Regulation is measured at a constant junction temperature using low duty cycle pulse testing. Load regulation is tested over a load range from 0.1 mA to the maximum specified output current. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 4: Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value.
- 5: Thermal Regulation is defined as the change in output voltage at a time T after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a current pulse equal to I_{LMAX} at V_{IN} = 6V for T = 10 msec.
- 6: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction-to-air (i.e., T_A, T_J, θ_{JA}). Exceeding the maximum allowable power dissipation causes the device to initiate thermal shutdown. Please see Section 5.0, "Thermal Considerations", for more details.
- 7: Hysteresis voltage is referenced by V_R.
- 8: The minimum V_{IN} has to justify the conditions: $V_{IN} \ge V_R + V_{DROPOUT}$ and $V_{IN} \ge 2.7V$ for $I_L = 0.1$ mA to $I_{OUT_{MAX}}$.
- 9: Apply for junction temperatures of -40C to +85C.

DC CHARACTERISTICS (CONTINUED)

Electrical Specifications: Unless otherwise noted, $V_{IN} = V_{OUT} + 1V$, $I_L = 100 \mu A$, $C_L = 3.3 \mu F$, $\overline{SHDN} > V_{IH}$, $T_A = +25^{\circ}C$. **Boldface** type specifications apply for junction temperatures of -40°C to +125°C.

Parameters	Sym	Min	Тур	Max	Units	Conditions
Output Noise	eN	_	260	_	nV/√Hz	I _L = I _{OUTMAX}
SHDN Input						
SHDN Input High Threshold	V_{IH}	45	_	_	%V _{IN}	V _{IN} = 2.5V to 6.5V
SHDN Input Low Threshold	V_{IL}	1	_	15	%V _{IN}	V _{IN} = 2.5V to 6.5V
ERROR Output						
Minimum VIN Operating Voltage	V_{INMIN}	1.0	_	_	V	
Output Logic Low Voltage	V_{OL}		_	400	mV	1 mA Flows to ERROR
ERROR Threshold Voltage	V_{TH}	_	0.95 x V _R	_	V	See Figure 4-2
ERROR Positive Hysteresis	V _{HYS}	_	50	_	mV	Note 7

Note 1: V_R is the regulator output voltage setting. For example: $V_R = 1.8V$, 2.5V, 2.7V, 2.85V, 3.0V, 3.3V, 3.6V, 4.0V, 5.0V.

2: TC
$$V_{OUT} = \frac{(V_{OUTMAX} - V_{OUTMIN})x \cdot 10^6}{V_{OUT} \times \Delta T}$$

- 3: Regulation is measured at a constant junction temperature using low duty cycle pulse testing. Load regulation is tested over a load range from 0.1 mA to the maximum specified output current. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 4: Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value.
- 5: Thermal Regulation is defined as the change in output voltage at a time T after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a current pulse equal to I_{LMAX} at V_{IN} = 6V for T = 10 msec.
- 6: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction-to-air (i.e., T_A, T_J, θ_{JA}). Exceeding the maximum allowable power dissipation causes the device to initiate thermal shutdown. Please see Section 5.0, "Thermal Considerations", for more details.
- **7:** Hysteresis voltage is referenced by V_R .
- 8: The minimum V_{IN} has to justify the conditions: $V_{IN} \ge V_R + V_{DROPOUT}$ and $V_{IN} \ge 2.7V$ for $I_L = 0.1$ mA to I_{OUTMAX} .
- 9: Apply for junction temperatures of -40C to +85C.

2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

Note: Unless otherwise indicated, $V_{IN} = V_{OUT} + 1V$, $I_L = 100 \mu A$, $C_L = 3.3 \mu F$, $\overline{SHDN} > V_{IH}$, $T_A = +25^{\circ}C$.

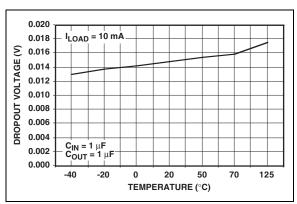


FIGURE 2-1: Dropout Voltage vs. Temperature ($I_{LOAD} = 10 \text{ mA}$).

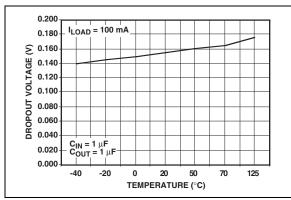


FIGURE 2-2: Dropout Voltage vs. Temperature ($I_{LOAD} = 100 \text{ mA}$).

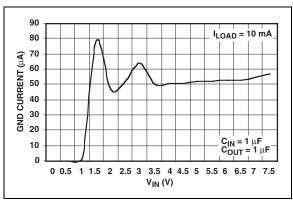


FIGURE 2-3: Ground Current vs. V_{IN} ($I_{LOAD} = 10 \text{ mA}$).

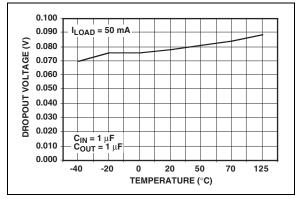


FIGURE 2-4: Dropout Voltage vs. Temperature ($I_{LOAD} = 50 \text{ mA}$).

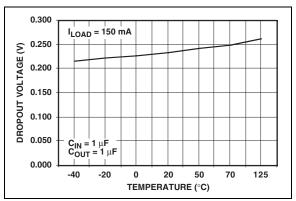


FIGURE 2-5: Dropout Voltage vs. Temperature ($I_{LOAD} = 150 \text{ mA}$).

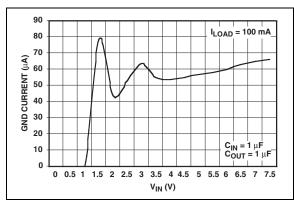


FIGURE 2-6: Ground Current vs. V_{IN} ($I_{LOAD} = 100 \text{ mA}$).

Note: Unless otherwise indicated, $V_{IN} = V_{OUT} + 1V$, $I_L = 100 \mu A$, $C_L = 3.3 \mu F$, $\overline{SHDN} > V_{IH}$, $T_A = +25^{\circ}C$.

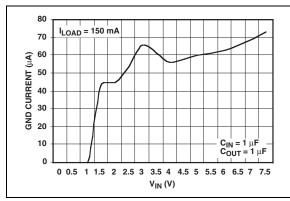


FIGURE 2-7: Ground Current vs. V_{IN} ($I_{LOAD} = 150 \text{ mA}$).

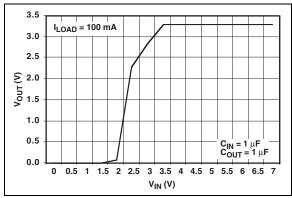


FIGURE 2-8: V_{OUT} vs. V_{IN} (I_{LOAD} = 100 mA).

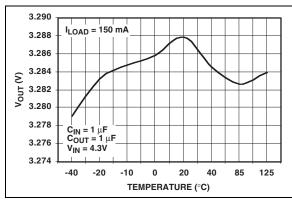


FIGURE 2-9: V_{OUT} vs. V_{IN} $(I_{LOAD} = 150 \text{ mA}).$

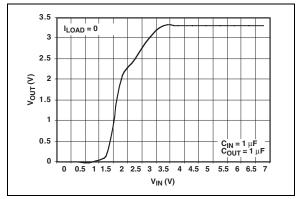


FIGURE 2-10: V_{OUT} vs. V_{IN} $(I_{LOAD} = 0 \text{ mA}).$

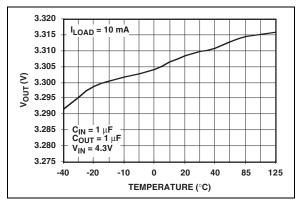


FIGURE 2-11: Output Voltage (3.3V) vs. Temperature ($I_{LOAD} = 10 \text{ mA}$).

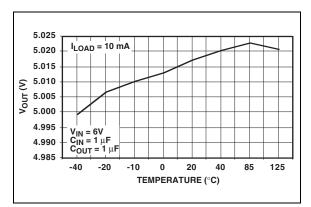


FIGURE 2-12: Output Voltage (5V) vs. Temperature (I_{LOAD} = 10 mA).

Note: Unless otherwise indicated, $V_{IN} = V_{OUT} + 1V$, $I_L = 100 \mu A$, $C_L = 3.3 \mu F$, $\overline{SHDN} > V_{IH}$, $T_A = +25^{\circ}C$.

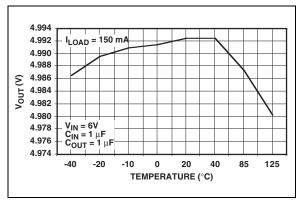


FIGURE 2-13: Output Voltage (5V) vs. Temperature ($I_{LOAD} = 10 \text{ mA}$).

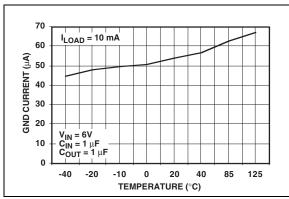


FIGURE 2-14: GND Current vs. Temperature ($I_{LOAD} = 10 \text{ mA}$).

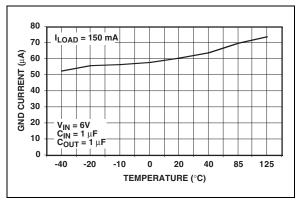


FIGURE 2-15: GND Current vs. Temperature ($I_{LOAD} = 150 \text{ mA}$).

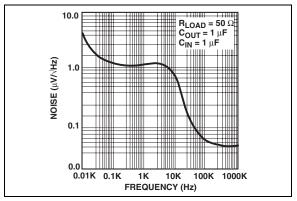


FIGURE 2-16: Output Noise vs. Frequency.

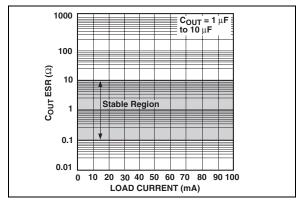


FIGURE 2-17: Stability Region vs. Load Current.

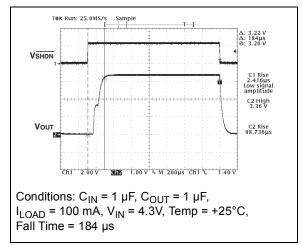


FIGURE 2-18: Measure Rise Time of 3.3V LDO.

Note: Unless otherwise indicated, $V_{IN} = V_{OUT} + 1V$, $I_L = 100 \mu A$, $C_L = 3.3 \mu F$, $\overline{SHDN} > V_{IH}$, $T_A = +25^{\circ}C$.

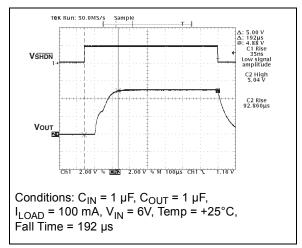
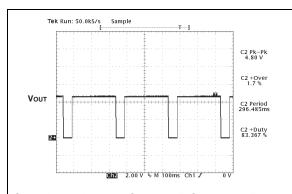


FIGURE 2-19: Measure Rise Time of 5.0V LDO.



Conditions: V_{IN} = 6V, C_{IN} = 0 μ F, C_{OUT} = 1 μ F

 I_{LOAD} was increased until temperature of die reached about 160°C, at which time integrated thermal protection circuitry shuts the regulator off when die temperature exceeds approximately 160°C. The regulator remains off until die temperature drops to approximately 150°C.

FIGURE 2-20: Thermal Shutdown Response of 5.0V LDO.

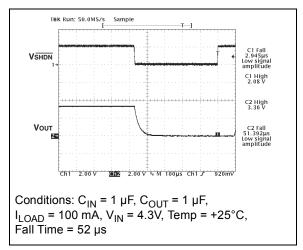


FIGURE 2-21: Measure Fall Time of 3.3V LDO.

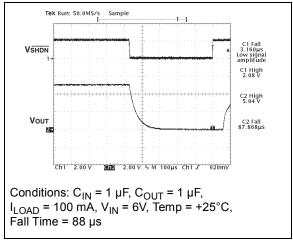


FIGURE 2-22: Measure Fall Time of 5.0V LDO.

3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1.

TABLE 3-1: PIN FUNCTION TABLE

Pin No.	Symbol	Description	
1	V _{IN}	Unregulated supply input	
2	GND	Ground terminal	
3	SHDN	Shutdown control input	
4	ERROR	Out-of-Regulation Flag (Open-drain output)	
5	V _{OUT}	Regulated voltage output	

3.1 Unregulated Supply Input (V_{IN})

Connect unregulated input supply to the V_{IN} pin. If there is a large distance between the input supply and the LDO regulator, some input capacitance is necessary for proper operation. A 1 μF capacitor connected from V_{IN} to ground is recommended for most applications.

3.2 Ground Terminal (GND)

Connect the unregulated input supply ground return to GND. Also connect the negative side of the 1 μF typical input decoupling capacitor close to GND and the negative side of the output capacitor C_{OUT} to GND.

3.3 Shutdown Control Input (SHDN)

The regulator is fully enabled when a logic-high is applied to SHDN. The regulator enters shutdown when a logic-low is applied to \underline{SHDN} . During shutdown, output voltage falls to zero, \underline{ERROR} is open-circuited and supply current is reduced to 0.5 μ A (max).

3.4 Out Of Regulation Flag (ERROR)

 $\overline{\text{ERROR}}$ goes low when V_{OUT} is out-of-tolerance by approximately -5%.

3.5 Regulated Voltage Output (V_{OUT})

Connect the output load to V_{OUT} of the LDO. Also connect the positive side of the LDO output capacitor as close as possible to the V_{OUT} pin.

4.0 DETAILED DESCRIPTION

The TC1054, TC1055 and TC1186 are precision fixed output voltage regulators (If an adjustable version is desired, please see the TC1070/TC1071/TC1187 data sheet (DS21353)). Unlike bipolar regulators, the TC1054, TC1055 and TC1186 supply current does not increase with load current.

Figure 4-1 shows a typical application circuit, where the regulator is enabled any time the shutdown input (SHDN) is at or above V_{IH} , and shutdown (disabled) when SHDN is at or below V_{IL} . \overline{SHDN} may be controlled by a CMOS logic gate or I/O port of a microcontroller. If the SHDN input is not required, it should be connected directly to the input supply. While in shutdown, supply current decreases to 0.05 μA (typical), V_{OUT} falls to zero volts, and \overline{ERROR} is opencircuited.

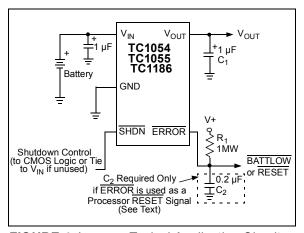


FIGURE 4-1: Typical Application Circuit.

4.1 ERROR Open-Drain Output

ERROR is driven low whenever V_{OUT} falls out of regulation by more than -5% (typical). This condition may be caused by low input voltage, output current limiting or thermal limiting. The ERROR threshold is 5% below rated V_{OUT} , regardless of the programmed output voltage value (e.g. ERROR = V_{OL} at 4.75V (typ.) for a 5.0V regulator and 2.85V (typ.) for a 3.0V regulator). ERROR output operation is shown in Figure 4-2.

Note that $\overline{\text{ERROR}}$ is active when V_{OUT} falls to V_{TH} and inactive when V_{OUT} rises above V_{TH} by V_{HYS} .

As shown in Figure 4-1, \overline{ERROR} can be used either as a battery low flag or as a processor \overline{RESET} signal (with the addition of timing capacitor C_2). $R_1 \times C_2$ should be chosen to maintain \overline{ERROR} below V_{IH} of the processor \overline{RESET} input for at least 200 msec to allow time for the system to stabilize. Pull-up resistor R_1 can be tied to V_{OUT} , V_{IN} or any other voltage less than $(V_{IN} + 0.3V)$.

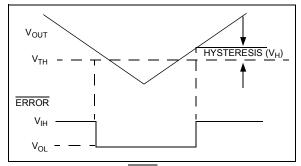


FIGURE 4-2: Error Output Operation.

4.2 Output Capacitor

A $1 \mu F$ (min) capacitor from V_{OUT} to ground is recommended. The output capacitor should have an effective series resistance greater than 0.1Ω and less than 5.0 Ω , with a resonant frequency above 1 MHz. A 1 μ F capacitor should be connected from V_{IN} to GND if there is more than 10 inches of wire between the regulator and the AC filter capacitor or if a battery is used as the power source. Aluminum electrolytic or tantalum capacitor types can be used (Since many aluminum electrolytic capacitors freeze at approximately -30°C, solid tantalums are recommended for applications operating below -25°C.). When operating from sources other than batteries, supply-noise rejection and transient response can be improved by increasing the value of the input and output capacitors and employing passive filtering techniques.

5.0 THERMAL CONSIDERATIONS

5.1 Thermal Shutdown

Integrated thermal protection circuitry shuts the regulator off when die temperature exceeds 160°C. The regulator remains off until the die temperature drops to approximately 150°C.

5.2 Power Dissipation

The amount of power the regulator dissipates is primarily a function of input voltage, output voltage and output current. The following equation is used to calculate worst case actual power dissipation:

EQUATION 5-1:

 $P_D \approx (V_{INMAX} - V_{OUTMIN})I_{LOADMAX}$

Where:

P_D = Worst case actual power dissipation

 V_{INMAX} = Maximum voltage on V_{IN}

 $V_{OUT_{MIN}}$ = Minimum regulator output voltage

I_{LOADMAX} = Maximum output (load) current

The maximum allowable power dissipation (Equation 5-2) is a function of the maximum ambient temperature (T_{AMAX}), the maximum allowable die temperature (T_{JMAX}) and the thermal resistance from junction-to-air (θ_{JA}). The 5-Pin SOT-23A package has a θ_{JA} of approximately 220°C/Watt.

EQUATION 5-2:

$$P_{DMAX} = \frac{(T_{JMAX} - T_{AMAX})}{\theta_{JA}}$$

Where all terms are previously defined.

Equation 5-1 can be used in conjunction with Equation 5-2 to ensure regulator thermal operation is within limits.

For example:

Given:

 V_{INMAX} = 3.0V +5% V_{OUTMIN} = 2.7V - 2.5%

 $I_{LOADMAX}$ = 40 mA T_{JMAX} = +125°C T_{AMAX} = +55°C

Find: 1. Actual power dissipation

2. Maximum allowable dissipation

Actual power dissipation:

$$P_D \approx (V_{INMAX} - V_{OUTMIN})I_{LOADMAX}$$

= $[(3.0 \times 1.05) - (2.7 \times 0.975)]40 \times 10^{-3}$
= $20.7mW$

Maximum allowable power dissipation:

$$\begin{split} P_{DMAX} &= \frac{(T_{JMAX} - T_{AMAX})}{\theta_{JA}} \\ &= \frac{(125 - 55)}{220} \\ &= 318 mW \end{split}$$

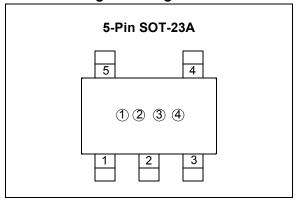
In this example, the TC1054 dissipates a maximum of 20.7 mW; below the allowable limit of 318 mW. In a similar manner, Equation 5-1 and Equation 5-2 can be used to calculate maximum current and/or input voltage limits.

5.3 Layout Considerations

The primary path of heat conduction out of the package is via the package leads. Therefore, layouts having a ground plane, wide traces at the pads and wide power supply bus lines combine to lower θ_{JA} and, therefore, increase the maximum allowable power dissipation limit.

6.0 PACKAGING INFORMATION

6.1 Package Marking Information



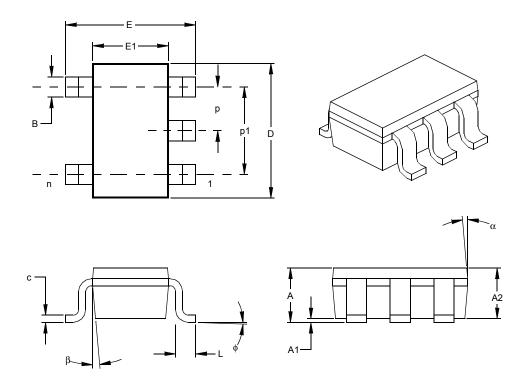
①&② represents part number code + temperature range and voltage

(V)	TC1054 Code	TC1055 Code	TC1186 Code
1.8	CY	DY	PY
2.5	C1	D1	P1
2.7	C2	D2	P2
2.8	CZ	DZ	PZ
2.85	C8	D8	P8
3.0	C3	D3	P3
3.3	C4	D4	P5
3.6	C9	D9	P9
4.0	C0	D0	P0
5.0	C6	D6	P7

③ represents year and quarter code

④ represents lot ID number

5-Lead Plastic Small Outline Transistor (OT) (SOT23)



	INCHES*			MILLIMETERS			
Dimension	n Limits	MIN	NOM	MAX	MIN	NOM	MAX
Number of Pins	n		5			5	
Pitch	р		.038			0.95	
Outside lead pitch (basic)	p1		.075			1.90	
Overall Height	Α	.035	.046	.057	0.90	1.18	1.45
Molded Package Thickness	A2	.035	.043	.051	0.90	1.10	1.30
Standoff §	A1	.000	.003	.006	0.00	0.08	0.15
Overall Width	Е	.102	.110	.118	2.60	2.80	3.00
Molded Package Width	E1	.059	.064	.069	1.50	1.63	1.75
Overall Length	D	.110	.116	.122	2.80	2.95	3.10
Foot Length	L	.014	.018	.022	0.35	0.45	0.55
Foot Angle	ф	0	5	10	0	5	10
Lead Thickness	С	.004	.006	.008	0.09	0.15	0.20
Lead Width	В	.014	.017	.020	0.35	0.43	0.50
Mold Draft Angle Top	α	0	5	10	0	5	10
Mold Draft Angle Bottom	β	0	5	10	0	5	10

Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed .010" (0.254mm) per side.

JEDEC Equivalent: MO-178

Drawing No. C04-091

^{*} Controlling Parameter § Significant Characteristic

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

PART NO. X /XX	Examples:
Device Temperature Package	a) TC1054-1.8VCT713: 50 mA LDO, 1.8V
Range	b) TC1054-2.8VCT713: 50 mA LDO, 2.8V
	c) TC1054-3.3VCT713: 50 mA LDO, 3.3V
Device: TC1054: 50 mA LDO with Shutdown & /Error output TC1055: 100 mA LDO with Shutdown & /Error output	d) TC1054-5.0VCT713: 50 mA LDO, 5.0V
TC1186: 150 mA LDO with Shutdown & /Error output	a) TC1055-2.5VCT713: 100 mA LDO, 2.5V
	b) TC1055-2.85VCT713: 100 mA LDO, 2.85V
Temperature Range: V = -40°C to +125°C	c) TC1055-3.6VCT713: 100 mA LDO, 3.6V
	d) TC1055-5.0VCT713: 100 mA LDO, 5.0V
Package: CT713 = 5L SOT-23A, Tape and Reel	
	a) TC1186-2.7VCT713: 150 mA LDO, 2.7V
	b) TC1186-2.85VCT713: 150 mA LDO, 2.85V
	c) TC1186-4.0VCT713: 150 mA LDO, 4.0V
	d) TC1186-5.0VCT713: 150 mA LDO, 5.0V

Sales and Support

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Please specify which device, revision of silicon and Data Sheet (include Literature #) you are using.

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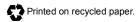
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WORLDWIDE SALES AND SERVICE

AMERICAS

Corporate Office

2355 West Chandler Blvd. Chandler, AZ 85224-6199 Tel: 480-792-7200 Fax: 480-792-7277 Technical Support: 480-792-7627 Web Address: http://www.microchip.com

Atlanta

3780 Mansell Road, Suite 130 Alpharetta, GA 30022 Tel: 770-640-0034 Fax: 770-640-0307

Boston

2 Lan Drive, Suite 120

Westford, MA 01886 Tel: 978-692-3848 Fax: 978-692-3821

Chicago

333 Pierce Road, Suite 180 Itasca, IL 60143

Tel: 630-285-0071 Fax: 630-285-0075

Dallas

4570 Westgrove Drive, Suite 160 Addison, TX 75001

Tel: 972-818-7423 Fax: 972-818-2924

Tri-Atria Office Building 32255 Northwestern Highway, Suite 190 Farmington Hills, MI 48334 Tel: 248-538-2250 Fax: 248-538-2260

Kokomo

2767 S. Albright Road Kokomo, Indiana 46902 Tel: 765-864-8360 Fax: 765-864-8387

Los Angeles

18201 Von Karman, Suite 1090 Irvine, CA 92612

Tel: 949-263-1888 Fax: 949-263-1338

Phoenix

2355 West Chandler Blvd. Chandler, AZ 85224-6199 Tel: 480-792-7966 Fax: 480-792-4338

San Jose

Microchip Technology Inc. 2107 North First Street, Suite 590 San Jose, CA 95131 Tel: 408-436-7950 Fax: 408-436-7955

Toronto

6285 Northam Drive, Suite 108 Mississauga, Ontario L4V 1X5, Canada Tel: 905-673-0699 Fax: 905-673-6509

ASIA/PACIFIC

Australia

Microchip Technology Australia Pty Ltd Marketing Support Division Suite 22, 41 Rawson Street Epping 2121, NSW Australia

Tel: 61-2-9868-6733 Fax: 61-2-9868-6755

China - Beijing

Microchip Technology Consulting (Shanghai)
Co., Ltd., Beijing Liaison Office Unit 915

Bei Hai Wan Tai Bldg. No. 6 Chaoyangmen Beidajie Beijing, 100027, No. China Tel: 86-10-85282100 Fax: 86-10-85282104

China - Chengdu

Microchip Technology Consulting (Shanghai) Co., Ltd., Chengdu Liaison Office Rm. 2401-2402, 24th Floor, Ming Xing Financial Tower No. 88 TIDU Street Chengdu 610016, China

Tel: 86-28-86766200 Fax: 86-28-86766599 China - Fuzhou

Microchip Technology Consulting (Shanghai) Co., Ltd., Fuzhou Liaison Office Unit 28F, World Trade Plaza No. 71 Wusi Road Fuzhou 350001, China Tel: 86-591-7503506 Fax: 86-591-7503521

China - Hong Kong SAR

Microchip Technology Hongkong Ltd. Unit 901-6, Tower 2, Metroplaza 223 Hing Fong Road Kwai Fong, N.T., Hong Kong Tel: 852-2401-1200 Fax: 852-2401-3431

China - Shanghai

Microchip Technology Consulting (Shanghai) Co., Ltd.

Room 701, Bldg. B Far East International Plaza No. 317 Xian Xia Road Shanghai, 200051

Tel: 86-21-6275-5700 Fax: 86-21-6275-5060

China - Shenzhen

Microchip Technology Consulting (Shanghai)
Co., Ltd., Shenzhen Liaison Office
Rm. 1812, 18/F, Building A, United Plaza No. 5022 Binhe Road, Futian District Shenzhen 518033, China Tel: 86-755-82901380 Fax: 86-755-82966626

China - Qingdao

Rm. B505A, Fullhope Plaza, No. 12 Hong Kong Central Rd. Qingdao 266071, China Tel: 86-532-5027355 Fax: 86-532-5027205

India

Microchip Technology Inc. India Liaison Office Marketing Support Division Divyasree Chambers 1 Floor, Wing A (A3/A4) No. 11, O'Shaugnessey Road Bangalore, 560 025, India Tel: 91-80-2290061 Fax: 91-80-2290062

Japan

Microchip Technology Japan K.K. Benex S-1 6F 3-18-20, Shinyokohama Kohoku-Ku, Yokohama-shi Kanagawa, 222-0033, Japan Tel: 81-45-471-6166 Fax: 81-45-471-6122

Korea

Microchip Technology Korea 168-1, Youngbo Bldg. 3 Floor Samsung-Dong, Kangnam-Ku

Seoul, Korea 135-882 Tel: 82-2-554-7200 Fax: 82-2-558-5934

Singapore

Microchip Technology Singapore Pte Ltd. 200 Middle Road #07-02 Prime Centre Singapore, 188980 Tel: 65-6334-8870 Fax: 65-6334-8850

Taiwan

Microchip Technology (Barbados) Inc., Taiwan Branch 11F-3, No. 207 Tung Hua North Road Taipei, 105, Taiwan Tel: 886-2-2717-7175 Fax: 886-2-2545-0139

EUROPE

Austria

Microchip Technology Austria GmbH Durisolstrasse 2 A-4600 Wels Austria Tel: 43-7242-2244-399

Fax: 43-7242-2244-393

Denmark

Microchip Technology Nordic ApS Regus Business Centre Lautrup hoj 1-3 Ballerup DK-2750 Denmark Tel: 45 4420 9895 Fax: 45 4420 9910

France

Microchip Technology SARL Parc d'Activite du Moulin de Massy 43 Rue du Saule Trapu Batiment A - ler Etage 91300 Massy, France Tel: 33-1-69-53-63-20 Fax: 33-1-69-30-90-79

Germany

Microchip Technology GmbH Steinheilstrasse 10 D-85737 Ismaning, Germany Tel: 49-89-627-144-0 Fax: 49-89-627-144-44

Italy

Microchip Technology SRL Via Quasimodo, 12 20025 Legnano (MI)

Milan, Italy Tel: 39-0331-742611 Fax: 39-0331-466781

United Kingdom

Microchip Ltd 505 Eskdale Road Winnersh Triangle Wokingham Berkshire, England RG41 5TU

Tel: 44 118 921 5869 Fax: 44-118 921-5820

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