Compact PLC series

The All-in-One Controller



Combining the processing power and data capacity of the CJ1M series and the built-in digital I/O functionality of the CPM2A series in a compact PLC outline, the CP1H CPU series sets new standards.

With 4 high-speed encoder inputs up to 1 MHz (single phase) and 4 pulse outputs up to 1 MHz (line driver), CP1H CPUs are ideal for positioning and speed control.

Their optional 4 analogue inputs and 2 analogue outputs plus advanced PID control with auto-tuning also make them ideal for continuous control applications.

What's more, expandable with CPM1A I/O units (up to 320 I/O points) and up to two CJ1 Special I/O units or CPU bus units, CP1H CPUs offer a wide range of communication interfaces and advanced I/O units.

Equipped with a USB interface as standard for programming and monitoring, the new CPUs allows up to two serial ports to be plugged in for communication with HMI or field devices. And, of course, they provide 'Smart Platform' communication routing over multiple network layers.

Using CX-One, programs can be created that enable the user to build, configure and program networks, PLCs, HMIs, motion-control systems, drives, temperature controllers and sensors.

The CP1H CPU series has the same architecture as the CS/CJ PLC series, which means programs are compatible for memory allocations and instructions and also support Function Blocks and Structured Text.

rogrammable Controllers

High-speed counter / encoder input

Four axes Counter Function (single phase or differential phase) CP1H-X(A) CPU Units: Four axes, single-phase at 100 kHz or differential phases at 500 kHz CP1H-Y CPU Units: Two axes, single phase at 1 MHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100 kHz or differential phases at 500 kHz Bingle phase at 100

Eight Interrupt Inputs

Eight inputs be used as:

- 50 µs pulse catch inputs
- interrupt inputs
- simple counter inputs (<5 kHz)

Program execution speed

Fast I/O requires fast response, the CJ1M core provides classleading program execution speed.





4 Pulse outputs for precise positioning



Analogue I/O

Analogue Control without Using Expansion Units CP1H-XA CPU Units have four analogue inputs and two analogue outputs built in.







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Serial communications

Two Option Boards can be mounted for RS-232C or RS-422A/485 communications making it easy to simultaneously connect to a PT, and other devices such as Inverters, Temperature controllers, Smart Sensors or Serial PLC link. The standard USB port is used for connection to a personal computer.



Modbus-RTU Easy Master

The Modbus-RTU Easy Master makes it easy to control Modbus slaves (such as Inverters). Serial communications can be executed independently of the program simply by setting a Modbus command in a fixed memory area and turning ON software switches.



Serial PLC Links

Up to 10 Words/Unit of data can be exchanged between up to nine CP1H (or CJ1M) CPU units.



NS/NT-series PTs can also be incorporated as slaves (1:N NT Link connections) to exchange data using the NT Links with only the master CP1H. Each is treated as one slave node.

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Reduce development time with efficient tools

• Plug-and-play USB Connection

Just install the CX-Programmer (Ver. 6.1 or higher) and connect the USB cable to the CP1H. The driver will be installed automatically.



Standard A-type male to B-type male USB cables can be used.

Modular PLCs CJ Series



Rack-based PLCs CS Series

A Wealth of Instructions

Personal computer CX-One

• PID Instruction with Autotuning

PID constants can be automatically tuned for the PID instruction. The limit cycle method is used for tuning, allowing tuning to be completed quickly

• Floating-point Decimal Instructions, Trigonometric Instructions, and More.

Just like the CS/CJ-series PLCs, the CP1H has approximately 400 instructions for ladder programming.

The Structured Text (ST) language makes arithmetic operations even easier.

In addition to ladder programming, function block logic can be written in ST language, which conforms to IEC 61131-3. Arithmetic processing is also possible with ST, including processing of absolute values, square roots, logarithms, and trigonometric functions (SIN, COS, and TAN). Processing that is difficult to write in ladder programming becomes easy using structured text.





Communications programs are provided by the Function Block library.

OMRONs Function Block Libraries drastically reduce the amount of programming needed to communicate with field devices. Just drag and drop a pre-tested function block in your program and set the parameters. You'll be up and running within one minute.

• A FB Library for Pulse Outputs.

Function blocks are also provided for pulse outputs to make it easy to write programs for positioning in addition to communications function blocks. These function blocks will reduce the time required for developing programs for applications such as for OMRON's Smartstep Servo System.



Security

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Programs can be protected by setting a password from the CX-Programmer (with the PLC online).

Password setting: Up to 8 alphanumeric characters (A-Z, a–z, 0-9)

One software, one connection, one minute

CX-Cne

CX-One is a single programming and configuration environment that enables the user to build, configure and program networks, PLCs, HMIs, Motion Control systems, Drives, Temperature Controllers and Sensors. The result of a single software is to reduce complexity of the configuration, allowing automation systems to be programmed or configured with minimal training.

| 1 Network Software | CX-Integrator CX-Protocol CX-FLnet |
|---------------------------------------|--|
| 2 PLC Software | CX-Programmer CX-Simulator SwitchBox |
| 3 HMI Software | CX-Designer |
| 4 Motion Controller Software | CX-Motion CX-Motion-NCF CX-Motion-MCH CX-Position CX-Drive |
| PLC-based Process Control Software | CX-Process Tool NS-series Face Plate Auto-Builder |
| 6 Component Software | CX-Thermo |
| CX-Simulator | |

Online CPU Unit operations, such as program monitoring, I/O memory manipulation, PV monitoring, forced setting/resetting memory bits, differential monitoring, data tracing, and online editing, can be executed without the actual PLC.



• CX-Thermo

The Support Software for Temperature Controllers (CX-Thermo) can be started from the CX-Integrator's Serial Communications (CompoWay/F) network.

Parameters can be created, edited, and transferred at the computer. The time required to make settings can be reduced when setting the same parameters in multiple devices.



CX-Integrator

Settings and configurations for devices can be made from any PLC in the network.

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• CX-Designer

The CX-Designer can be started from the CX-Integrator. Settings such as the PLC and Unit information are passed to the CXDesigner, so you can start developing screens immediately after CX-Designer starts.



Handy built-in functions make maintenance easier



Analogue Inputs Are Made Simple

An analogue control setting and an analogue input are provided.

Analogue setting

The analogue control setting has a resolution of 256 steps. When the value is changed it is displayed (hexadecimal) for three seconds on the 7-segment display.



Analogue Input

This input has a resolution of 256 steps and is used for an analogue input set of 0 to 10 V. Each CP1H CPU Unit has one of these connectors built in. (The built-in analogue I/O for CP1HXA CPU Units is separate.) A device, such as a potentiometer, can be connected to enable direct manual operation and control from a control panel. The maximum cable length is 3 meters. A connecting cable (1 m) is included with the CPU Unit.

Memory Cassette

- Data, such as programs and initial memory values, can be stored on a Memory Cassette (optional) and copied to other systems.
- The Memory Cassette can also be used when installing new versions of application programs.



7-segment Status Display

- The 7-segment Display provides two display digits.
- In addition to displaying error codes for errors detected by the PLC, codes can be displayed on the display from the ladder program.
- The 7-segment display is useful for maintenance as well, allowing problems that arise during system operation to be grasped without using any Support Software.

4 Battery-free Operation

- The values in the DM Area (32 Kwords) are saved in the CPU Unit's built-in flash memory as initial values, and can be read at startup.
- Battery-free operation is also possible when saving production data and machine parameters in the DM Area, turning OFF the power, and using the same data again for the next production run.



A battery is required for the clock function and to retain the status of HR Area bits and counter values.A battery is provided as a standard feature with the CPU Unit. The user program (ladder program) is stored in builtflash memory, so no battery is required to back it up.

Expansion I/O units Expand as needed



CPU unit overview

CP1H-XA40D - Built-in Analogue I/O



CP1H-XA40DR-A AC power supply, 24 DC inputs, 16 relay outputs, 4 analogue inputs, 2 analogue outputs



CP1H-XA40DT-D DC power supply, 24 DC inputs, 16 transistor (sinking) outputs, 4 analogue inputs, 2 analogue outputs

CP1H-XA40DT1-D

DC power supply, 24 DC inputs, 16 transistor (sourcing) outputs, 4 analogue inputs, 2 analogue outputs

CP1H-X40D Basic Model



CP1H-X40DR-A AC power supply, 24 DC inputs, 16 relay outputs



CP1H-X40DT-D DC power supply, 24 DC inputs, 16 transistor (sinking) outputs

CP1H-X40DT1-D DC power supply, 24 DC inputs, 16 transistor (sourcing) outputs



CP1H-Y20DT-D DC power supply, 12 DC inputs, 8 transistor (sinking) outputs

Two 1-MHz line-driver inputs (phases A, B, and Z) and two 1-MHz line-driver outputs (CW and CCW) are provided separately.

| | CP1H-XA CPU Units | CP1H-X CPU Units | CP1H-Y CPU Unit | | | |
|--|---|--------------------------------------|---|--|--|--|
| I/O capacity | 24 inputs, 16 outputs | | 12 inputs, 8 outputs Line-driver inputs: Phases A, B, and Z for 2 axes Line-driver outputs: CW and CCW for 2 axes | | | |
| High-speed counter | 100 kHz (single phase), 50 kHz (di | fferential phases), 4 axes | 1 MHz (single phase), 500 kHz (differential phases) for 2 axes (line-driver input), 100 kHz (single phase), 50 kHz (differential phas- es) for 2 axes (4 axes total) | | | |
| Pulse output function (Models with Transistor Outputs only) | 100 kHz for 2 axes and 30 kHz for 2 axes (4 axes total) | | 1 MHz for 2 axes (line-driver output), 100 kHz for 2 axes (4 axes total) | | | |
| Serial communications | USB port (peripheral port) and 2 op | otional serial ports (either RS-2320 | C or RS-422A/485 Option Boards) | | | |
| Analogue I/O | 4 analogue inputs and 2 analogue outputs | - | - | | | |
| Interrupt inputs Quick-response inputs (50-ms width min.) | 8 inputs | | 6 inputs | | | |
| User program capacity | 20 kstep | | | | | |
| DM capacity | 32 kwords | 32 kwords | | | | |
| Maximum number of CPM1A Expansion I/O Units | 7 (Refer to page16 for Unit restrictions.) | | | | | |
| Maximum number of CJ-series Units | 2 (CJ-series Special I/O Units and | CPU Bus Units only. Refer to pag | e 14 for information on Units that can be used.) | | | |

• Options



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CP-series expansion units

• Expansion I/O Units

CPM1A-8ED Input points: 8 DC input CPM1A-8ER Output points: 8 Relay output CPM1A-8ET

CPM1A-8ET1

Output points: 8 Transistor output (sinking)

CPM1A-20EDT Input points: 12 DC inputs Output points: 8, transistor outputs (sinking)

CPM1A-20EDT1 Input points: 12 DC inputs

CPM1A-20EDR1

Input points: 12 DC inputs

Output points: 8 relay outputs

CPM1A-40EDR

Input points: 24 DC inputs Output points: 16 relay outputs CPM1A-40EDT Input points: 24 DC inputs Output points: 16 transistor outputs (sinking)

CPM1A-40EDT1 Input points: 24 DC inputs

Output points: 8 Transistor output (sourcing) Output points: 8, transistor outputs (sourcing) Output points: 16 transistor outputs (sourcing)

Analogue Units





CPM1A-DA041

Analogue outputs: 4

(resolution: 6,000)

Analogue Output Unit

Analogue Input Unit CPM1A-AD041 Analogue inputs: 4 (resolution: 6,000)

Temperature Sensor Units

• CompoBus/S - I/O Link Unit

CPM1A-TS001 Thermocouple inputs: 2 CPM1A-TS002 Thermocouple inputs: 4

CPM1A-SRT21

Input points: 8

Output points: 8

CJ Unit Adapter

CP1W-EXT01

CPM1A-TS101 Platinum resistance thermometer inputs: 2 CPM1A-TS102 Platinum resistance thermometer inputs: 4

CJ-series Special I/O Units and CPU Bus Units

Two CJ-series Special I/O Units or CPU Bus Units can be connected by using a CJ Unit Adapter.

Analogue Input Unit

CJ1W-AD

CJ1W-DA

CJ1W-MAD42

Analogue I/O Unit

CJ-series Special I/O Units

Analogue Output Unit CJ1W-PDC15



CPM1A-TS101-DA

Platinum resistance

Analogue output: 1

(resolution: 256)

DeviceNet I/O Link Unit

Process Input Unit

Temperature Control Un

CompoBus/S Master Unit

PROFIBUS-DP Slave Unit

CJ1W-PTS

CJ1W-TC

CJ1W-SRM21

CJ1W-PRT21

CPM1A-DRT21

Input points: 32

Output points: 32

thermometer inputs: 2

CPM1A- MAD11 Analogue inputs: 2 (resolution: 6,000) Analogue outputs: 1 (resolution: 6,000)



Analogue I/O Unit CPM1A- MAD01 Analogue inputs: 2 (resolution: 256) Analogue outputs: 1 (resolution: 256)





CP1W-CN811 80 cm

PROFIBUS-DP I/O Link Unit

CPM1A-PRT21 Input points: 16 Output points: 16





Serial Communications Unit **DeviceNet Unit** CJ1W-DRM21

PROFIBUS-DP Master Unit CJ1W-PRM21 CAN unit

CJ1W-CORT21

System configuration

A maximum of seven CPM1A Expansion I/O Units can be connected.



Group A

| | Unit type | Model |
|---------------------------|---|----------------|
| Expansion I/O Units | 40 I/O points | CPM1A-40EDR |
| | | CPM1A-40EDT |
| | | CPM1A-40EDT1 |
| | 20 I/O points | CPM1A-20EDR1 |
| | | CPM1A-20EDT |
| | | CPM1A-20EDT1 |
| | 8 inputs | CPM1A-8ED |
| | 8 outputs | CPM1A-8ER |
| | | CPM1A-8ET |
| | | CPM1A-8ET1 |
| Analogue Unit | 2 analogue inputs, 1 analogue output | CPM1A-MAD01 |
| | | CPM1A-MAD11 |
| Temperature Sensor Units | 2 thermocouple inputs | CPM1A-TS001 |
| | 2 platinum resistance thermometer inputs | CPM1A-TS101 |
| | 2 platinum resistance thermometer inputs, 1 analogue output | CPM1A-TS101-DA |
| CompoBus/S I/O Link Unit | 8 inputs, 8 outputs | CPM1A-SRT21 |
| DeviceNet I/O Link Unit | 32 inputs, 32 outputs | CPM1A-DRT21 |
| PROFIBUS-DP I/O Link Unit | 16 inputs, 16 outputs | CPM1A-PRT21 |

Group B Units that each count as two units

| | Model | |
|--------------------------|--|-------------|
| Analogue Units | 4 analogue inputs | CPM1A-AD041 |
| | 4 analogue outputs | CPM1A-DA041 |
| Temperature Sensor Units | 4 thermocouple inputs | CPM1A-TS002 |
| | 4 platinum resistance thermometer inputs | CPM1A-TS102 |

CJ-series Special I/O Units and CPU Bus Units

A maximum of two CJ-series Special I/O Units or CPU Bus Units can be connected by using a CP1W-EXT01 CJ Unit Adapter.

| | CJ-series S | CJ-series CPU | Bus Units | | | |
|-----------------------|---------------|---------------------------|------------|-----------------------------|---------------|--|
| Unit name Model | | Unit name | Model | Unit name | Model | |
| Analogue Input Units | CJ1W-AD081-V1 | Process Input Units | CJ1W-PDC15 | Serial Communications Units | CJ1W-SCU41-V1 | |
| | CJ1W-AD041-V1 | Temperature Control Units | CJ1W-TC001 | 1 | CJ1W-SCU21-V1 | |
| Analogue Output Units | CJ1W-DA08V | _ | CJ1W-TC002 | Ethernet Unit | CJ1W-ETN21 | |
| | CJ1W-DA08C | _ | CJ1W-TC003 | DeviceNet Unit | CJ1W-DRM21 | |
| | CJ1W-DA041 | _ | CJ1W-TC004 | Controller Link Unit | CJ1W-CLK21-V1 | |
| | CJ1W-DA021 | _ | CJ1W-TC101 | PROFIBUS-DP Master Unit | CJ1W-PRM21 | |
| Analogue I/O Unit | CJ1W-MAD42 | | CJ1W-TC102 | CAN Unit | CJ1W-CORT21 | |
| Process Input Units | CJ1W-PTS51 | | CJ1W-TC103 | | | |
| | CJ1W-PTS52 | | CJ1W-TC104 | | | |
| | CJ1W-PTS15 | CompoBus/S Master Unit | CJ1W-SRM21 | | | |
| | CJ1W-PTS16 | PROFIBUS-DP Slave Unit | CJ1W-PRT21 | | | |

Specifications

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CPU Unit Specifications

| | Item | AC power supply models: CP1H-DD-A | DC power supply models: CP1H-DD-D | | |
|---|---|--|---|--|--|
| Power S | upply | 100 to 240 VAC 50/60 Hz | 24 VDC | | |
| Operating voltage range | | 85 to 264 VAC | 20.4 to 26.4 VDC | | |
| 1 0 0 0 | | | (21.6 to 26.4 VDC with four or more Expansion Units.) | | |
| Power c | onsumption | Can be used for backing up programs or auto-booting | 50 W max | | |
| Inrush c | urrent | 100 to 120 VAC inpute: 20 A may 8 me may /200 to 240 VAC in- | 30 A max 20 ms max | | |
| initu Shi Ci | anent | nuts: 40 A max 8 ms max | 00 A max. 20 m3 max. | | |
| Extornal | power supply | 200 mA at 24 VDC | Nono | | |
| LAternal | | 300 mA at 24 VDC | 00 MO min. (at 500 V(DC) between the systemal DC terminals and | | |
| Insulatio | n resistance | 20 M22min. (at 500 VDC) between the external AC terminals and | 20 M2/min. (at 500 VDC) between the external DC terminals and | | |
| Dielestri | | | an terminals | | |
| Dielectri | c strength | 2,300 VAC at 50/60 Hz for 1 min between the external AC and | CD terminale lookage surrent: 5 m 4 may | | |
| Nuclear tra | | | GH terminais, leakage current. 5 mA max. | | |
| Noise in | imunity | Conforming to IEC 61000-4-4. 2 kV (power supply line) | | | |
| Vibration | n resistance | 10 to 57 Hz, 0.075-mm amplitude, 57 to 150 Hz, acceleration: 9. | 8 m/s2 in X, Y, and Z directions for 80 minutes each | | |
| <u>.</u> | | (Sweep time: 8 minutes x 10 sweeps = total time 80 minutes) | | | |
| Shock resistance 147 m/s2, three times each in X, Y, and Z directions | | | | | |
| Ambient | operating temperature | 0 to 55°C | | | |
| Ambient | humidity | 10% to 90% (with no condensation) | | | |
| Ambient | operating environment | No corrosive gas | | | |
| Ambient | storage temperature | -20 to 75°C (Excluding battery.) | | | |
| Power h | olding time | 10 ms min. | 2 ms min. | | |
| Dimensi | ons | 150 x 90 x 85 mm (W x H x D) | | | |
| Weight | | 740 g max. | 590 g max. | | |
| | | • | | | |
| | Item | XA CPU Units: CP1H-XA | Y CPU Units: CP1H-Y | | |
| Control | nethod | Stored program method | | | |
| I/O contr | rol method | Cyclic scap with immediate refreshing | | | |
| Program | | Ladder diagram | | | |
| Eupotion | hlooko | Maximum number of function block dofi nitions: 129 Maximum n | imbor of instances: 256 Languages usable in function block | | |
| FUNCTION | DIUCKS | definitions: Ladder diagrams, structured text (ST) | amper of instances. 200 Languages usable in function block | | |
| Instructio | on length | 1 to 7 stops per instruction | | | |
| Instructio | | Approx. 400 (function codes: 2 digita) | | | |
| Instructio | | Approx. 400 (iunction codes. 5 digits) | | | |
| Instructio | on execution time | Basic Instructions: 0.10 is min. Special Instructions: 0.15 is min. | | | |
| Commor | n processing time | 0.7 ms | | | |
| Program | capacity | 20 Ksteps | | | |
| Number | of tasks | 288 (32 cyclic tasks and 256 interrupt tasks) Scheduled interrupt | tasks: 1 (interrupt task No. 2, fi xed) Input interrupt tasks: 8 (inter- | | |
| | | rupt task No. 140 to 147, fi xed), 6 for Y CPU Units High-speed of | ounter interrupt tasks: 256 (interrupt task No. 0 to 255) | | |
| Maximu | m subroutine number | 256 | | | |
| Maximu | n jump number | 256 | | | |
| I/O | Input bits | 1,600 bits (100 words): CIO 0.00 to CIO 99.15 | | | |
| areas | | (The 24 built-in inputs are allocated in CIO 0.00 to CIO 0.11 and | CIO 1.00 to CIO 1.11.) | | |
| | Output bits | 1,600 bits (100 words): CIO 100.00 to CIO 199.15 | | | |
| | (The 16 built-in outputs are allocated in CIO 100 00 to CIO 100 07 and CIO 101 00 to CIO 101 07) | | | | |
| | | Built in Analog Inguist. CIO 200 to CIO 2003 | | | |
| | Built-in Analog Inputs | CIO 200 to CIO 203 | | | |
| | Built-in Analog Inputs Built-in Analog Outputs | CIO 200 to CIO 203 CIO 210 to CIO 211 | | | |
| | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to | , CIO 3189) | | |
| Work bit | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 | 0 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 | | |
| Work bit | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) | 0 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 | | |
| Work bit TR Area | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 | CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 | | |
| Work bit TR Area Holding | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) | CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 | | |
| Work bit TR Area Holding AR Area | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 | 2 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) | | |
| Work bit TR Area Holding AR Area | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area S | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to | 2 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) 4959) | | |
| Work bit TR Area Holding AR Area Timers | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 | 0 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) A959) | | |
| Work bit TR Area Holding AR Area Timers Counters | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 | 2 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) 9 A959) | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 | 2 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) A959) | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Re | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area a (See note.) ojster Area | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 | 2 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) A959) | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Re Index Re | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area (See note.) gister Area | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): B0 to IB15 | 9 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) A959) | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Re Index Re Task Fla | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area Area a (See note.) gister Area agister Area | CIO 200 to CIO 203 CIO 210 to CIO 211 1.440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): K0000 to TK0031 | 9 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) 9 A959) | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Re Index Re Task Fla Task Fla | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area Area a (See note.) gister Area agister Area g Area | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 scamples for the trace data maximum of 31 bits | 2 CIO 3189) 04 bits (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) 9 A959) | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Re Index Re Task Fla Trace M | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area (See note.) gister Area egister Area g Area emory Consette | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits 4 organized Marror: Consette (CD1W (MCGN)) can be around of | and 6 words.) | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area DM Area DM Area DM Area Trace M Memory | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area a (See note.) gister Area egister Area g Area emory Cassette | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N | and 6 words.) and 6 words.) this can be used for program backups and auto-booting. | | |
| Work bit TR Area Holding AR Area Counters DM Area Data Re Index Re Task Fla Trace M Memory Clock fu | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area S Area (see note.) gister Area egister Area gister Area emory Cassette nction | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a 1.5 min to +1.5 min (ambient temperature: 28°C). | and 6 words.) and 6 words.) tic CIO 3189) and 6 words.) tic Can be used for program backups and auto-booting. mbient temperature: 55°C), min (ambient temperature: 0°C) | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Re Index Re Trace M Memory Clock fu | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area (See note.) gister Area gister Area gg Area emory Cassette nction | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): TR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 for the comparison of the temperature: 25°C). | and 6 words.) ote: Can be used for program backups and auto-booting. mbient temperature: 55°C), in (ambient temperature: 0°C) | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Re Index Re Task Fla Trace M Memory Clock fu | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area (See note.) gister Area gister Area gister Area emory Cassette nction hications functions | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 m One built-in peripheral port (USB1.1): For connecting Support So | and 6 words.) ote: Can be used for program backups and auto-booting. mbient temperature: 55°C), in (ambient temperature: 0°C) iftware only. | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Re Index Re Task Fla Trace M Memory Clock fu Commun | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area Area a (See note.) gister Area gg Area emory Cassette nnction nications functions backup | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 m One built-in peripheral port (USB1.1): For connecting Support Sc A maximum of two Serial Communications Option Boards can be Elash memory. Ilese programe parameters (when as the PI-C Serial Content of the serial Communications Option Boards can be Elash memory. Ilese programe parameters (when as the PI-C Serial Content of two Serial Communications Option Boards can be | and 6 words.) ote: Can be used for program backups and auto-booting. mbient temperature: 55°C), in (ambient temperature: 0°C) ftware only. e mounted. b) comment data, and the entire DM Area can be caued to ff ech. | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Re Index Re Task Fla Trace M Memory Clock fu Commun | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area Area (See note.) gister Area gister Area gg Area emory Cassette nnction hications functions backup | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 n One built-in peripheral port (USB1.1): For connecting Support SC A maximum of two Serial Communications Option Boards can be Flash memory: User programs, parameters (such as the PLC Set memory as initial values. Battery backury: The Holding Area. DM | and 6 words.) and 6 words.) and 6 words.) and 6 words.) bit: (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) (A959) and 6 words.) and 6 words.) ote: Can be used for program backups and auto-booting. mbient temperature: 55°C), in (ambient temperature: 0°C) iftware only. e mounted. up), comment data, and the entire DM Area can be saved to fl ash Area. and counter values (fl ags. PV) are backed up by a battery. | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area DAta Ree Index Ref Task Fla Trace M Memory Clock fu Commun Memory Batters | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area S Area (See note.) gister Area egister Area gister Area emory Cassette nction hications functions backup | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 n One built-in peripheral port (USB1.1): For connecting Support Sc A maximum of two Serial Communications Option Boards can be Flash memory: User programs, parameters (such as the PLC Set memory as initial values. Battery backup: The Holding Area, DM | and 6 words.) and 6 words.) and 6 words.) de bits (2,344 words): CIO 3800.00 to CIO 6143.15 47.15 (A0 to A447) A959) and 6 words.) and 6 words.) ote: Can be used for program backups and auto-booting. mbient temperature: 55°C), ini (ambient temperature: 0°C) oftware only. e mounted. up), comment data, and the entire DM Area can be saved to fl ash Area, and counter values (fl ags, PV) are backed up by a battery. of manufacture) | | |
| Work bit TR Area Holding AR Area Dimers Counters DM Area Data Re Index Re Task Fla Trace M Memory Clock fu Commun Memory Battery s | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area (See note.) gister Area gister Area g Area emory Cassette nction nications functions backup service life | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 m One built-in peripheral port (USB1.1): For connecting Support Sc A maximum of two Serial Communications Option Boards can be Flash memory: User programs, parameters (such as the PLC Set memory as initial values. Battery backup: The Holding Area, DM 5 years at 25°C. (Use the replacement battery within two years of 40 (24 insurts 16 output) | and 6 words.) and 6 words.) and 6 words.) and 6 words.) and 6 words.) bit: Can be used for program backups and auto-booting. mbient temperature: 55°C), nin (ambient temperature: 0°C) iffware only. e mounted. up), comment data, and the entire DM Area can be saved to fl ash Area, and counter values (fl ags, PV) are backed up by a battery. of manufacture.) 120 (12 inpute 9 outpute) | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Re Index Re Trace M Memory Clock fu Commun Memory Battery s Built-in i | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area (See note.) gister Area agister Area gaster Area garea emory Cassette nction nications functions backup service life nput terminals | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 n One built-in peripheral port (USB1.1): For connecting Support Sc A maximum of two Serial Communications Option Boards can be Flash memory: User programs, parameters (such as the PLC Set memory as initial values. Battery backup: The Holding Area, DM 5 years at 25 °C. (Use the replacement battery within two years of 40 (24 inputs, 16 outputs) | and 6 words.) and 6 words.) and 6 words.) and 6 words.) ter: Can be used for program backups and auto-booting. mbient temperature: 55°C), in (ambient temperature: 0°C) offware only. a mounted. up), comment data, and the entire DM Area can be saved to fl ash Area, and counter values (fl ags, PV) are backed up by a battery. of manufacture.) 20 (12 inputs, 8 outputs) Line-driver inputs: Two ayas for phases A. B. and Z. | | |
| Work bit TR Area Holding AR Area Timers Counter: DM Area Data Re Index Re Task Fla Trace M Memory Clock fu Commun Memory Battery s Built-in in | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area (See note.) gister Area gister Area gister Area emory Cassette nction nications functions backup service life nput terminals | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 m One built-in peripheral port (USB1.1): For connecting Support Sc A maximum of two Serial Communications Option Boards can be Flash memory: User programs, parameters (such as the PLC Set memory as initial values. Battery backup: The Holding Area, DM 5 years at 25 °C. (Use the replacement battery within two years of 40 (24 inputs, 16 outputs) | and 6 words.) ote: Can be used for program backups and auto-booting. mbient temperature: 55°C), in (ambient temperature: 0°C) ftware only. e mounted. up), comment data, and the entire DM Area can be saved to fl ash Area, and counter values (fl ags, PV) are backed up by a battery. of manufacture.) 20 (12 inputs, 8 outputs) Line-driver inputs: Two axes for phases A, B, and Z Line-driver inputs: Two axes for CW and CCW | | |
| Work bit TR Area Holding AR Area Timers Counters Data Re Index Re Task Fla Trace M Memory Clock fu Commun Memory Battery s Built-in in | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area Area (See note.) gister Area gaster Area gaster Area emory Cassette nnction nications functions backup service life nput terminals | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 n One built-in peripheral port (USB1.1): For connecting Support Sc A maximum of two Serial Communications Option Boards can be Flash memory: User programs, parameters (such as the PLC Set memory as initial values. Battery backup: The Holding Area, DM 5 years at 25 °C. (Use the replacement battery within two years of 40 (24 inputs, 16 outputs) CPM1A Expansion I/O Units: 7 max · C-I-series Special I/O Units; 7 max · C-I-series Spe | and 6 words.) and 2 | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Ree Index Ref Task Fla Trace M Memory Clock fu Commun Memory Battery s Built-in in | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area Area (See note.) gister Area gister Area gg Area emory Cassette nnction nications functions backup service life nput terminals | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 n One built-in peripheral port (USB1.1): For connecting Support Sc A maximum of two Serial Communications Option Boards can be Flash memory: User programs, parameters (such as the PLC Set memory as initial values. Battery backup: The Holding Area, DM 5 years at 25 °C. (Use the replacement battery within two years of 40 (24 inputs, 16 outputs) CPM1A Expansion I/O Units: 7 max.; CJ-series Special I/O Units | and 6 words.) and 6 words.) bient temperature: 55°C), in (ambient temperature: 0°C) offware only. e mounted. up), comment data, and the entire DM Area can be saved to fl ash Area, and counter values (fl ags, PV) are backed up by a battery. of manufacture.) 20 (12 inputs, 8 outputs) Line-driver inputs: Two axes for phases A, B, and Z Line-driver outputs: Two axes for CW and CCW a or CPU Bus Units: 2 max. | | |
| Work bit TR Area Holding AR Area Timers Counters DM Area Data Ree Index Ref Task Fla Trace M Memory Clock fu Commun Memory Battery s Built-in in Number Expansia Max. nu | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area S Area Area (See note.) gister Area gister Area gister Area emory Cassette nction nications functions backup service life nput terminals of connectable on (I/O) Units mber of I/O points | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: T0 to T4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 n One built-in peripheral port (USB1.1): For connecting Support Sc A maximum of two Serial Communications Option Boards can be Flash memory: User programs, parameters (such as the PLC Set memory as initial values. Battery backup: The Holding Area, DM 5 years at 25 °C. (Use the replacement battery within two years of 40 (24 inputs, 16 outputs) CPM1A Expansion I/O Units: 7 max.; CJ-series Special I/O Units 320 (40 built in + 40 per Expansion (I/O) Unit x 7 Units) | and 6 words.) and 6 words.) and 6 words.) and 6 words.) and 6 words.) ote: Can be used for program backups and auto-booting. mbient temperature: 55°C), in (ambient temperature: 0°C) oftware only. a mounted. up), comment data, and the entire DM Area can be saved to fl ash Area, and counter values (fl ags, PV) are backed up by a battery. of manufacture.) 20 (12 inputs, 8 outputs) Line-driver inputs: Two axes for phases A, B, and Z Line-driver outputs: Two axes for CW and CCW or CPU Bus Units: 2 max. 300 | | |
| Work bit TR Area Holding AR Area DM Area DM Area DM Area Data Ree Index Ref Task Fla Trace M Memory Clock fu Commun Memory Battery s Built-in in Number Expansie Max. nu | Built-in Analog Inputs Built-in Analog Outputs Serial PLC Link Area s Area (See note.) gister Area gaster Area gaster Area gaster Area gaster Area backup cassette notion nications functions backup service life nput terminals of connectable on (I/O) Units mber of I/O points | CIO 200 to CIO 203 CIO 210 to CIO 211 1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to 8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) 37,5 (CIO 3800 to CIO 6143) 16 bits: TR0 to TR15 8,192 bits (512 words): H0.00 to H511.15 (H0 to H511) Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A4 Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to 4,096 bits: T0 to T4095 4,096 bits: C0 to C4095 32 Kwords: D0 to D32767 16 registers (16 bits): DR0 to DR15 6 registers (16 bits): IR0 to IR15 32 flags (32 bits): TK0000 to TK0031 4,000 words (500 samples for the trace data maximum of 31 bits A special Memory Cassette (CP1W-ME05M) can be mounted. N Supported. Accuracy (monthly deviation): -3.5 min to -0.5 min (a -1.5 min to +1.5 min (ambient temperature: 25°C), -3 min to +1 One built-in peripheral port (USB1.1): For connecting Support Sc A maximum of two Serial Communications Option Boards can be Flash memory: User programs, parameters (such as the PLC Set memory as initial values. Battery backup: The Holding Area, DM 5 years at 25 °C. (Use the replacement battery within two years of 40 (24 inputs, 16 outputs) CPM1A Expansion I/O Units: 7 max.; CJ-series Special I/O Units 320 (40 built in + 40 per Expansion (I/O) Unit x 7 Units) | and 6 words.) and 6 words.) and 6 words.) and 6 words.) te: Can be used for program backups and auto-booting. mbient temperature: 55°C), in (ambient temperature: 0°C) oftware only. e mounted. up), comment data, and the entire DM Area can be saved to fl ash Area, and counter values (fl ags, PV) are backed up by a battery. of manufacture.) 20 (12 inputs, 8 outputs) Line-driver inputs: Two axes for phases A, B, and Z Line-driver outputs: Two axes for CW and CCW is or CPU Bus Units: 2 max. 300 (20 built in + 40 per Expansion (I/O) Unit x 7 Units) | | |

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| Item | XA CPU Units: CP1H-XA | X CPU Units: CP1H-X | Y CPU Units: CP1H-Y | | |
|---|--|--|---|--|---|
| Interrupt inputs | 8 inputs (Shared by the external interrupt inputs (counter mode) and the quick-response inputs.) | | 6 inputs (Shared by the external interrupt inputs (counter mode) and the quick-response inputs.) | | |
| Interrupt inputs counter mode | 8 inputs (Response frequency: 5 kHz max. for all interrupt inputs), 16 bits | | 8 inputs (Response frequency: 5 kHz max. for all interrupt inputs), 16 bits | | 6 inputs (Response frequency: 5 kHz max. for all interrupt inputs), 16 bits |
| Quick-response inputs | 8 points (Min. input pulse width: 50 is | max.) | 6 points (Min. input pulse width: 50 is max.) | | |
| Scheduled interrupts | 1 | | | | |
| High-speed counters | 4 inputs: Differential phases (4x), 50 k single phase (pulse plus direction, up/ Value range: 32 bits, Linear mode or r Interrupts: Target value comparison o | Hz or (down, increment), ring mode r range comparison | 2 inputs: Differential phases (4x), 500 kHz or single phase, 1 MHz and 2 inputs: Differential phases (4x), 50 kHz or single phase (pulse plus direction, up/down, increment), 100 kHz Value range: 32 bits, Linear mode or ring mode Interrupts: Target value comparison or range com- parison | | |
| Pulse outputs (models with transistor outputs only) | Trapezoidal or S-curve acceleration and deceleration (Duty ratio: 50% fi xed) 2 outputs, 1 Hz to 100 kHz (CCW/CW or pulse plus direction) 2 outputs, 1 Hz to 30 kHz (CCW/CW or pulse plus direction) PWM outputs :(Duty ratio: 0.0% to 100.0% (Unit: 0.1%)) 2 outputs, 0.1 to 1 kHz (Accuracy: ±5% at 1 kHz) | | Trapezoidal or S-curve acceleration and deceleration (Duty ratio: 50% fi xed) 2 outputs, 1 Hz to 1 MHz (CCW/CW or pulse plus direction) 2 outputs, 1 Hz to 100 kHz (CCW/CW or pulse plus direction) PWM outputs :(Duty ratio: 0.0% to 100.0% (Unit: 0.1%)) 2 outputs, 0.1 to 1 kHz (Accuracy: ±5% at 1 kHz) | | |
| Built-in analog I/O terminals | 4 analogue inputs and 2 analogue outputs (Refer to separate detailed specifi cations.) | None | | | |
| Analogue control | 1 (Setting range: 0 to 255) | • | | | |
| External analogue input | 1 input (Resolution: 1/256, Input range | e: 0 to 10 V) | | | |

Serial Communications Specifications

| Item | Function | Interface |
|---------------------|--|--|
| Peripheral USB port | For connecting Peripheral Device. | Conforms to USB 1.1, B-type connector |
| Serial port 1 | Host Link, No-protocol, NT Link (1: N), Serial PLC Link (See note.), Serial Gateway (CompoWay/F master, Modbus-RTU master), Modbus-RTU easy master function | The CP1W-CIF01 RS-232C Option Board |
| Serial port 2 | Host Link, No-protocol, NT Link (1: N), Serial PLC Link (See note.), Serial Gateway (CompoWay/F master, Modbus-RTU master), Modbus-RTU easy master function | or the CP1W-CIF11 RS-422A/485 Option Board |
| | | |
| | | can be used with either port. |

Analogue I/O Specifications (CP1H-XA CPU Units Only)

| Item | | Voltage I/O | Current I/O | | | | |
|------------------|--|---|--|--|--|--|--|
| Analogue | Number of analog inputs | 4 | • | | | | |
| Input | Input signal range | 0 to 5 V, 1 to 5 V, 0 to 10 V, or -10 to 10 V | 0 to 20 mA or 4 to 20 mA | | | | |
| Section | Max. rated input | ±15 V | ±30 mA | | | | |
| | External input impedance | 1 MΩ min. | Approx. 250 | | | | |
| | Resolution | 1/6,000 or 1/12,000 (full scale) | 000 or 1/12,000 (full scale) | | | | |
| | Overall accuracy | 25 °C: ±0.3% full scale/0 to 55 °C: ±0.6% full scale | 25°C: ±0.4% full scale/0 to 55°C: ±0.8% full scale | | | | |
| | A/D conversion data | scale for -10 to 10 V: F448 (E890) to 0BB8 (1770) Hex scale for other ranges: 0000 to 1770 (2EE0) Hex | | | | | |
| | Averaging | upported (Set for individual inputs in the PLC Setup.) | | | | | |
| | Open-circuit detection | Supported (Value when disconnected: 8000 Hex) | pported (Value when disconnected: 8000 Hex) | | | | |
| Analogue | Number of outputs | 2 outputs | | | | | |
| Output | Output signal range | 0 to 5 V, 1 to 5 V, 0 to 10 V, or -10 to 10 V | 0 to 20 mA or 4 to 20 mA | | | | |
| Section | Allowable external output load resistance | 1 k Ω min. | 600 Ω max. | | | | |
| | External output impedance |).5 max. | | | | | |
| | Resolution | 1/6,000 or 1/12,000 (full scale) | | | | | |
| | Overall accuracy | 25 °C: ±0.4% full scale/0 to 55 °C: ±0.8% full scale | | | | | |
| | D/A conversion data | ull scale for -10 to 10 V: F448 (E890) to 0BB8 (1770) hex ull scale for other ranges: 0000 to 1770 (2EE0) hex | | | | | |
| Conversio | n time | 1 ms/point | | | | | |
| Isolation method | | Photocoupler isolation between analogue I/O terminals and internal circuits. No isolation between analogue I/O signals. | | | | | |

Dimensions CP1H CPU Units

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Ordering Information

CPU Units

| CPU Unit | | Specifications | | | Model | Standards |
|--|-----------------|-----------------------|----------------|----------------|------------------------|-----------|
| | Power Supply | Output method | Inputs | Outputs | | |
| CP1H-X CPU Units | AC | Relay | 24 | 16 | CP1H-X40DR-A | CE, N |
| Memory capacity: 20 Ksteps | DC | Transistor (sinking) | | | CP1H-X40DT-D | CE, N |
| High-speed counters: 100 kHz, 4 axes Pulse outputs: 100 kHz, 2 axes 30 kHz, 2 axes | | Transistor (sourcing) | | | CP1H-X40DT1-D | CE, N |
| CP1H-XA CPU Units | AC | Relay | 24 | 16 | CP1H-XA40DR-A | CE, N |
| Memory capacity: 20 Ksteps | DC | Transistor (sinking) | | | CP1H-XA40DT-D | CE, N |
| High-speed counters: 100 kHz, 4 axes Pulse outputs: 100 Hz, 2 axes 30 kHz, 2 axes Analogue inputs: 4 Analogue outputs: 2 | | Transistor(sourcing) | | | CP1H-XA40DT1-D | CE, N |
| CP1H-Y CPU Units | DC | Transistor (sinking) | 12+line-driver | 8 +line-driver | CP1H-Y20DT-D | - |
| Memory capacity: 20 Ksteps High-speed counters:1 MHz, 2 axes 100 kHz, 2 axes Pulse outputs: 1 MHz, 2 axes 30 kHz, 2 axes | | | input, 2 axes | input, 2 axes | (To be released soon.) | |

Options (for CPU Units)

| Name | Name Specifications | | Standards |
|--------------------------|--|------------|-----------|
| RS-232C Option Board | For CPU Unit option port. | CP1W-CIF01 | CE, N |
| RS-422A/485 Option Board | For CPU Unit option port. | CP1W-CIF11 | CE, N |
| Memory Cassette | Can be used for backing up programs or auto-booting. | CP1W-ME05M | CE, N |

Maintenance Products

| Name | Specifications | Model | Standards |
|-------------|---|------------|-----------|
| Battery Set | For CP1H CPU Units (Use batteries within two years of manufacture.) | CJ1W-BAT01 | CE |
| DIN Track | Length: 0.5 m; Height: 7.3 mm | PFP-50N | |
| | Length: 1 m; Height: 7.3 mm | PFP-100N | |
| | Length: 1 m; Height: 16 mm | PFP-100N2 | |
| End Plate | There are 2 stoppers provided with CPU Units and I/O Interface Units as standard accessories to secure the Units on the DIN Track. | PFP-M | |

I/O Connecting Cable

| Name | Specifications | Model | Standards |
|----------------------|-----------------------------------|------------|-----------|
| I/O Connecting Cable | 80 cm (for CPM1A Expansion Units) | CP1W-CN811 | CE, N |

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Programming Devices

| Name | Specifications | | Model | Standards |
|---|---|----------------|---------------|-----------|
| CX-One | CX-One is a package that integrates the Support Software for OMRON PLCs and components. CX-One runson the following OS. OS: Windows 98SE, Me, NT 4.0 (Service Pack 6a), 2000 (Service Pack 3 or higher), or XP CX-One Includes CX-Programmer Ver.6.® and CX-Simulator Ver.1.®.For details, refer to the CX-One catalog (Cat. No. R134). For CPU Unit option port. Can be used for backing up programs or auto-booting. | One license | CXONE-AL01C-E | - |
| FA Integrated Tool | | Three licenses | CXONE-AL03C-E | - |
| Package | | Ten licenses | CXONE-AL10C-E | - |
| Computer Connecting Cable for | D-Sub 9-pin (Length: 2.0 m) For anti-static | | XW2Z-200S-CV | - |
| CP1W-CIF01 RS-232C | D-Sub 9-pin (Length: 5.0 m) | connectors | XW2Z-500S-CV | - |
| Option Board (See note.) | D-Sub 9-pin (Length: 2.0 m) | XW2Z-200S-V | - | |
| | D-Sub 9-pin (Length: 5.0 m) | XW2Z-500S-V | - | |
| USB-Serial Conversion Cable ^{*1} | USB-RS-232C Conversion Cable (Length: 0.5 m) and PC Complies with USB Specifi cation 1.1 On personal computer side: USB (A plug connector, male) On PLC side: RS-232C (D-sub 9-pin, male) Driver: Supported by Windows 98, Me, 2000, and XP | | CS1W-CIF31 | - |

^{*1} Cannot be used with a peripheral USB port. To connect to a personal computer via a peripheral USB port, use commercially-available USB cable (A to B type, male).

Technical Documentation

| Name | Standards |
|----------------------------------|-----------|
| CP1H CPU Unit Operation Manual | W450-E1 |
| CP1H CPU Unit Programming Manual | W451-E1 |

Expansion Units

| Name | Output method | Input | Output | Model | Standard |
|---------------------------|---|--------------------|--------------------|----------------|-------------|
| Expansion I/O Units | Relay | 24 | 16 | CPM1A-40EDR | CE, N |
| | Transistor (sinking) | | | CPM1A-40EDT | CE, N |
| | Transistor output (sourcing) | | | CPM1A-40EDT1 | CE, N |
| | elay | 12 | 8 | CPM1A-20EDR1 | U, C, CE |
| | Transistor (sinking) | | | CPM1A-20EDT | U, C, N, CE |
| | Transistor output (sourcing) | | | CPM1A-20EDT1 | U, C, N, CE |
| | - | 8 | - | CPM1A-8ED | U, C, N, CE |
| | Relay | - | 8 | CPM1A-8ER | U, C, N, CE |
| | Transistor (sinking) | - | 8 | CPM1A-8ET | U, C, N, CE |
| | Transistor output (sourcing) | | | CPM1A-8ET1 | U, C, N, CE |
| Analogue Input Unit | Analogue (resolution: 1/6000) | 4 | - | CPM1A-AD041 | U, C, N, CE |
| Analogue Output Unit | Analogue (resolution: 1/6000) | - | 4 | CPM1A-DA041 | UC1, CE |
| Analogue I/O Units | Analogue (resolution: 1/256) | 2 | 1 | CPM1A-MAD01 | UC1, CE |
| | Analogue (resolution: 1/6000) | 2 | 1 | CPM1A-MAD11 | U, C, N, CE |
| DeviceNet I/O Link Unit | - | 32 (I/O link bits) | 32 (I/O link bits) | CPM1A-DRT21 | U, C, CE |
| CompoBus/S I/O Link Unit | - | 8 (I/O link bits) | 8 (I/O link bits) | CPM1A-SRT21 | U, C, N, CE |
| PROFIBUS-DP I/O Link Unit | | 16 (I/O link bits) | 16 (I/O link bits) | CPM1A-PRT21 | CE |
| Temperature Sensor Units | 2 thermocouple inputs | | | CPM1A-TS001 | U, C, N, CE |
| | 4 thermocouple inputs | | | CPM1A-TS002 | U, C, N, CE |
| | 2 platinum resistance thermometer inputs | | | CPM1A-TS101 | U, C, N, CE |
| | 4 platinum resistance thermometer inputs | | | CPM1A-TS102 | U, C, N, CE |
| | 2 platinum resistance thermometer inputs, 1 Analogue output (resolution: 256) | | | CPM1A-TS101-DA | U, C, L, CE |

CJ-series Special I/O Units and CPU Bus Units

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| Category | Name | Specifications | Model | Standard | | |
|-----------------------------------|---------------------------|---|---------------|---------------|--|--|
| CP1H CPU Unit options | CJ Unit Adapter | Adapter for connecting CJ-series Special I/O Units and CPU Bus Units (includes CJ-series End Cover) | CP1W-EXT01 | UC1, CE, N, L | | |
| CJ-series Special I/O Units | Analogue Input Units | 8 inputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/8,000; Conversion speed: 250 is/input max. (Can be set to 1/4,000 resolution and 1 ms/input.) | CJ1W-AD081-V1 | | | |
| | | 4 inputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/8,000; Conversion speed: 250 is/input max. (Can be set to 1/4,000 resolution and 1 ms/input.) | CJ1W-AD041-V1 | | | |
| | Analogue Output Units | 8 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V) Resolution: 1/4,000; Conversion speed: 1 ms/output max. (Can be set to 1/8000, 250 is/output) | CJ1W-DA08V | | | |
| | | 8 outputs (4 to 20 mA) Resolution: 1/4,000; Conversion speed: 1 ms/output max. (Can be set to 1/8,000, 250 is/ output) | CJ1W-DA08C | UC1, CE, N | | |
| | | 4 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/4,000, Conversion speed: 1 ms/point max. | CJ1W-DA041 | UC1, CE, N, L | | |
| | | 2 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/4,000; Conversion speed: 1 ms/output max. | CJ1W-DA021 | | | |
| | Analogue I/O Unit | 4 inputs, 2 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, -10 to 10 V, 4 to 20 mA) Resolution: 1/4000; Conversion speed: 1 ms/point max. (Can be set to 1/8,000, 250 is/point) | CJ1W-MAD42 | | | |
| | Process Input Units | 4 inputs, B, J, K, L, R, S, T; Conversion speed: 250 ms/4 inputs | CJ1W-PTS51 | UC1, CE | | |
| | | 4 inputs, Pt100 Ü (JIS, IEC), JPt100 Ü, Conversion speed: 250 ms/ 4 inputs | CJ1W-PTS52 | | | |
| | | 2 inputs, B, E, J, K, L, N, R, S, T, U, W, Re5-26, PL ±100 mV, Resolution: 1/64,000; Conversion speed: 10 ms/2 inputs | CJ1W-PTS15 | | | |
| | | 2 inputs, Pt100, JPt100, Pt50, Ni508.4; Resolution: 1/64,000; Conversion speed: 10 ms/2 inputs | CJ1W-PTS16 | | | |
| | | 2 inputs, 0 to 1.25 V, -1.25 to 1.25 V, 0 to 5 V, 1 to 5 V, -5 to 5 V, 0 to 10 V, -10 to 10V, ±10 V selectable range, 0 to 20 mA, 4 to 20 mA | CJ1W-PDC15 | | | |
| | Temperature Control Units | 4 loops, thermocouple input, NPN output | CJ1W-TC001 | UC1, CE, N, L | | |
| | | 4 loops, thermocouple input, PNP output | CJ1W-TC002 | | | |
| | | 2 loops, thermocouple input, NPN output, heater burnout detection function | CJ1W-TC003 | | | |
| | | 2 loops, thermocouple input, PNP output, heater burnout detection function | CJ1W-TC004 | | | |
| | | 4 loops, platinum resistance thermometer input, NPN output | CJ1W-TC101 | | | |
| | | 4 loops, platinum resistance thermometer input, PNP output | CJ1W-TC102 | | | |
| | | 22 loops, platinum resistance thermometer input, NPN output, heater burnout detection function | CJ1W-TC103 | | | |
| | | 2 loops, platinum resistance thermometer input, PNP output, heater burnout detection function | CJ1W-TC104 | | | |
| | CompoBus/S Master Unit | CompoBus/S remote I/O, 256 points max. | CJ1W-SRM21 | | | |
| | PROFIBUS-DP Slave Unit | Exchanges up to 180 words in any memory area with a PROFIBUS-DP Master Unit | CJ1W-PRT21 | UC, CE | | |
| CJ-series CPU | Controller Link Units | Wired (Shielded twisted-pair cable) | CJ1W-CLK21-V1 | UC1, CE, N, L | | |
| Bus Units | Serial Communications | 1 RS-232C port and 1 RS-422A/485 port | CJ1W-SCU41-V1 | | | |
| | Units | 2 RS-232C ports | CJ1W-SCU21-V1 | | | |
| | Ethernet Unit | 100Base-TX | CJ1W-ETN21 | | | |
| | DeviceNet Unit | Functions as master and/or slave; allows control of 32,000 points max. per master. | CJ1W-DRM21 | | | |
| | PROFIBUS-DP Master Unit | Controls up to 7000 words of remote I/O data over PROFIBUS-DP | CJ1W-PRM21 | UC, CE | | |
| | CAN Unit | Can send and/or receive any CAN-Message | CJ1W-CORT21 | CE | | |

Programmable Controllers •

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