Preferred Device

# **Low Noise Transistor**

## **NPN Silicon**

### Features

• Pb-Free Packages are Available\*

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CEO</sub>	45	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	45	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	6.5	Vdc
Collector Current – Continuous	Ι <sub>C</sub>	200	mAdc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	625 5.0	mW mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\thetaJA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

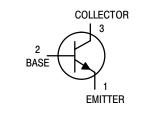
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1.  $R_{\theta JA}$  is measured with the device soldered into a typical printed circuit board.

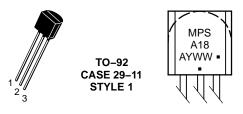


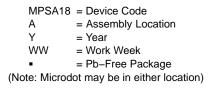
### **ON Semiconductor®**

#### http://onsemi.com









### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MPSA18	TO-92	5000 Units/Box
MPSA18G	TO-92 (Pb-Free)	5000 Units/Box
MPSA18RLRA	TO-92	2000/Tape & Reel
MPSA18RLRAG	TO-92 (Pb-Free)	2000/Tape & Reel
MPSA18RLRM	TO-92	2000/Ammo Pack
MPSA18RLRMG	TO-92 (Pb-Free)	2000/Ammo Pack
MPSA18RLRP	TO-92	2000/Ammo Pack
MPSA18RLRPG	TO–92 (Pb–Free)	2000/Ammo Pack

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use

and best overall value.

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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Publication Order Number: MPSA18/D

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = $25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (Note 2) ( $I_C = 10 \text{ mAdc}, I_B = 0$ )	V <sub>(BR)CEO</sub>	45	_	-	Vdc
Collector – Base Breakdown Voltage ( $I_C = 100 \ \mu Adc, I_E = 0$ )	V <sub>(BR)CBO</sub>	45	-	-	Vdc
Emitter–Base Breakdown Voltage $(I_E = 10 \ \mu Adc, I_C = 0)$	V <sub>(BR)EBO</sub>	6.5	-	-	Vdc
Collector Cutoff Current ( $V_{CB} = 30$ Vdc, $I_E = 0$ )	I <sub>CBO</sub>	-	1.0	50	nAdc
ON CHARACTERISTICS (Note 2)	•			•	•
$ \begin{array}{l} \text{DC Current Gain} \\ (I_C = 10 \ \mu \text{Adc}, \ V_{CE} = 5.0 \ \text{Vdc}) \\ (I_C = 100 \ \mu \text{Adc}, \ V_{CE} = 5.0 \ \text{Vdc}) \\ (I_C = 1.0 \ \text{mAdc}, \ V_{CE} = 5.0 \ \text{Vdc}) \\ (I_C = 10 \ \text{mAdc}, \ V_{CE} = 5.0 \ \text{Vdc}) \\ (I_C = 10 \ \text{mAdc}, \ V_{CE} = 5.0 \ \text{Vdc}) \end{array} $	h <sub>FE</sub>	400 500 500 500	580 850 1100 1150	- - - 1500	_
Collector – Emitter Saturation Voltage $(I_C = 10 \text{ mAdc}, I_B = 0.5 \text{ mAdc})$ $(I_C = 50 \text{ mAdc}, I_B = 5.0 \text{ mAdc})$	V <sub>CE(sat)</sub>		_ 0.08	0.2 0.3	Vdc
Base-Emitter On Voltage (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 5.0 Vdc)	V <sub>BE(on)</sub>	-	0.6	0.7	Vdc
SMALL-SIGNAL CHARACTERISTICS				1	1
Current–Gain – Bandwidth Product (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 5.0 Vdc, f = 100 MHz)	f <sub>T</sub>	100	160	-	MHz
Collector–Base Capacitance $(V_{CB} = 5.0 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C <sub>cb</sub>	-	1.7	3.0	pF
Emitter–Base Capacitance $(V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz})$	C <sub>eb</sub>	-	5.6	6.5	pF
Noise Figure (I <sub>C</sub> = 100 $\mu$ Adc, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 10 kΩ, f = 1.0 kHz) (I <sub>C</sub> = 100 $\mu$ Adc, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 1.0 kΩ, f = 100 Hz)	NF		0.5 4.0	1.5 -	dB
Equivalent Short Circuit Noise Voltage (I <sub>C</sub> = 100 $\mu$ Adc, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 1.0 kΩ, f = 100 Hz)	V <sub>T</sub>	-	6.5	-	nV/√Hz

2. Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%.

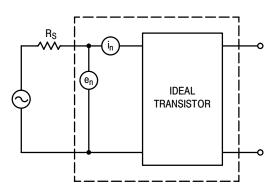
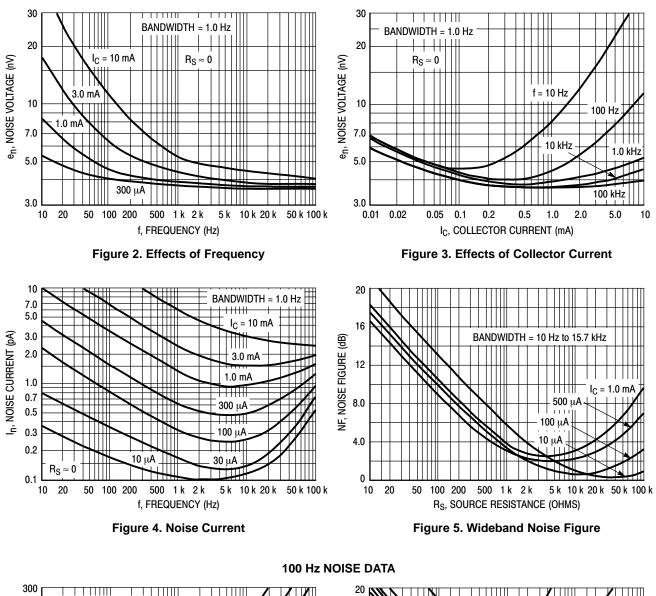


Figure 1. Transistor Noise Model

### **NOISE CHARACTERISTICS**

 $(V_{CE} = 5.0 \text{ Vdc}, T_A = 25^{\circ}C)$ 

#### NOISE VOLTAGE



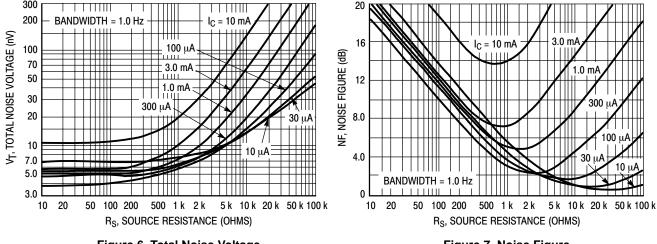


Figure 6. Total Noise Voltage

Figure 7. Noise Figure

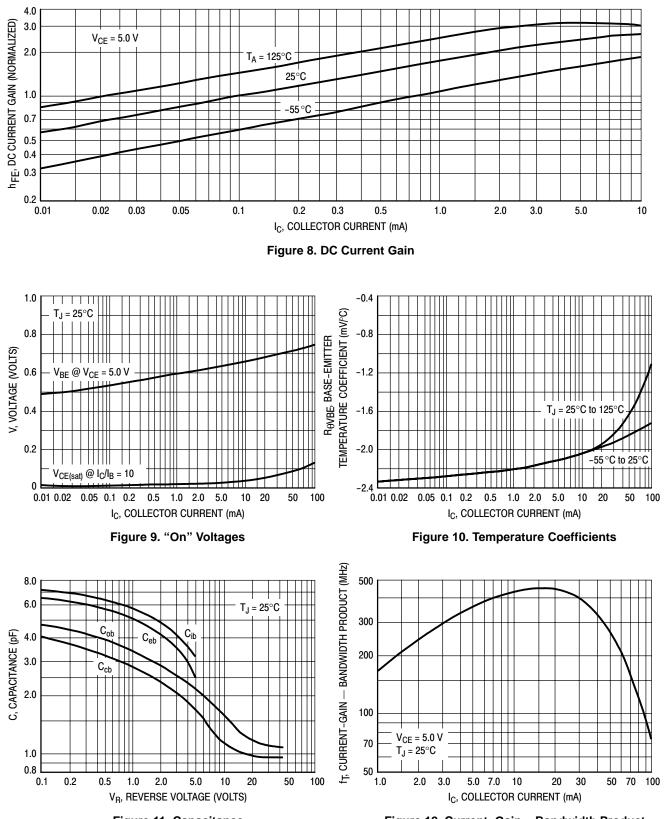
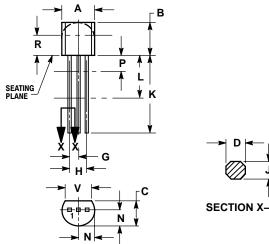


Figure 11. Capacitance

Figure 12. Current–Gain – Bandwidth Product

#### PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AL** 







NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI 1. Y14.5M, 1982.
- 2
- TI4-3M, 1962. CONTROLLING DIMENSION: INCH. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. LEAD DIMENSION IS UNCONTROLLED IN P AND 3.
- 4. BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Η	0.095	0.105	2.42	2.66
ſ	0.015	0.020	0.39	0.50
Κ	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.115		2.93	
۷	0.135		3.43	

STYLE 1: PIN 1. EMITTER

BASE 2. 3.

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