

# BUL3P5

## MEDIUM VOLTAGE FAST-SWITCHING PNP POWER TRANSISTOR

### Features

- MEDIUM VOLTAGE CAPABILITY
- LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- VERY HIGH SWITCHING SPEED

### **Applications**

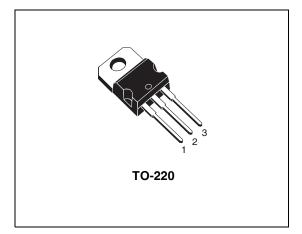
ELECTRONIC BALLASTS FOR FLUORESCENT LIGHTING

## Description

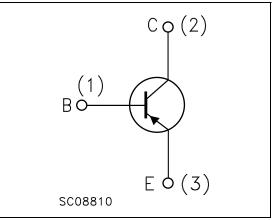
The BUL3P5 is manufactured using high voltage Multi-Epitaxial Planar technology for high switching speeds and medium voltage capability.

It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is expressly designed for a new solution to be used in compact fluorescent lamps, H.F. ballast voltage FED where it is coupled with the BUL3N7, its complementary NPN transistor.



## **Internal Schematic Diagram**



### **Order Codes**

Part Number	Marking	Package	Packing
BUL3P5	BUL3P5	TO-220	TUBE

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# 1 Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-Emitter Voltage (V <sub>BE</sub> = 0)	-500	V
V <sub>CEO</sub>	Collector-Emitter Voltage (I <sub>B</sub> = 0)	-400	V
V <sub>EBO</sub>	Emitter-Base Voltage ( $I_c = 0$ , $I_B = -0.75$ A, $t_p < 100$ ms, $T_j < 150$ °C)	V <sub>(BR)EBO</sub>	V
Ι <sub>C</sub>	Collector Current	-3	А
I <sub>CM</sub>	Collector Peak Current (t <sub>P</sub> < 5ms)	-6	А
Ι <sub>Β</sub>	Base Current	-1.5	А
I <sub>BM</sub>	Base Peak Current (t <sub>P</sub> < 5ms)	-3	А
P <sub>TOT</sub>	Total dissipation at $T_c = 25^{\circ}C$	60	W
T <sub>stg</sub>	Storage Temperature	-65 to 150	°C
Т <sub>Ј</sub>	Max. Operating Junction Temperature	150	°C

#### Table 1. Absolute Maximum Rating

#### Table 2. Thermal Data

Symbol	Parameter	Value	Unit
R <sub>thJ-case</sub>	Thermal Resistance Junction-Case Max	2.08	°C/W
R <sub>thJ-amb</sub>	Thermal Resistance Junction-Ambient Max	62.5	°C/W

## 2 Electrical Characteristics

Symbol	Parameter	Test C	onditions	Min.	Тур.	Max.	Unit
ICES	Collector Cut-off Current (V <sub>BE</sub> = 0)	V <sub>CE</sub> = -500 V V <sub>CE</sub> = -500 V	T <sub>C</sub> = 125°C			-0.1 -0.5	mA mA
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage $(I_{\rm C}=0)$	l <sub>E</sub> = -10 mA		-5		-10	V
V <sub>CEO(sus)</sub> Note: 1	Collector-Emitter Sustaining Voltage (I <sub>B</sub> = 0)	l <sub>C</sub> = 100 mA		-400			V
V <sub>CE(sat)</sub> Note: 1	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -0.7 A I <sub>C</sub> = -1 A	2			-0.5 -0.5	V V
V <sub>BE(sat)</sub> Note: 1	Base-Emitter Saturation Voltage	I <sub>C</sub> = -0.5A I <sub>C</sub> = -1A I <sub>C</sub> = -2A	I <sub>B</sub> = -0.2 A			-1.1 -1.2 -1.3	V V V
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -10 mA I <sub>C</sub> = -0.7A I <sub>C</sub> = -2A	V <sub>CE</sub> = -5 V	10 18 4		34	
t <sub>r</sub> t <sub>s</sub> t <sub>f</sub>	RESISTIVE LOAD Rise Time Storage Time Fall Time	I <sub>C</sub> = -0.7 A I <sub>B1</sub> = -0.14 A T <sub>p</sub> = 30 μs			100 2.4 80		ns μs ns
t <sub>s</sub> t <sub>f</sub>	INDUCTIVE LOAD Storage Time Fall Time	$I_{C} = -1 A$ $V_{BE(off)} = 5 V$ L = 1 mH			450 70		ns ns

Table 3.Electrical Characteristics ( $T_{CASE} = 25^{\circ}C$ ; unless otherwise specified)

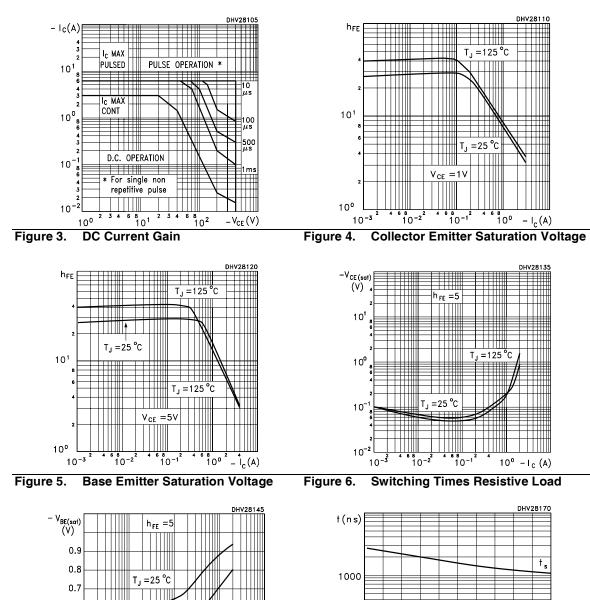
Note: 1 Pulsed duration =  $300 \ \mu s$ , duty cycle  $\leq 1.5\%$ .



### 2.1 Typical Characteristics



#### Figure 2. DC Current Gain



100

10

0.5

0.7

0.9

0.6 0.5

0.4

0.3

0.2

 $10^{-3}$ 

 $10^{-2}$ 

 $10^{-1}$ 

T<sub>J</sub> =125 °C

10<sup>0</sup>

– I<sub>c</sub> (A)

t<sub>f</sub>

 $V_{Clamp} = 250 V$ 

 $V_{BE(off)} = 5V$ 

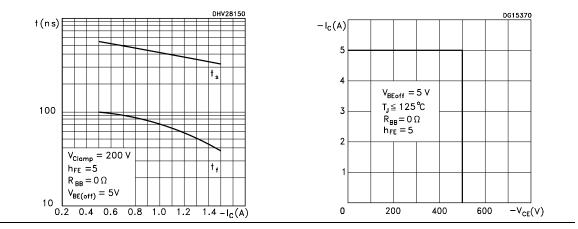
 $1.3 - I_{C}(A)$ 

 $h_{FE} = 5$  $I_{bon} = - I_{boff}$ 

1.1

#### Figure 7. Switching Times Inductive Load

#### Figure 8. Reverse Bised SOA





## 3 Test Circuits



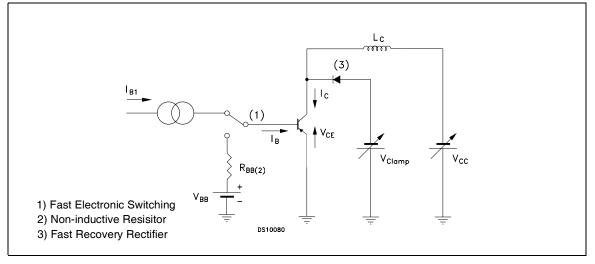
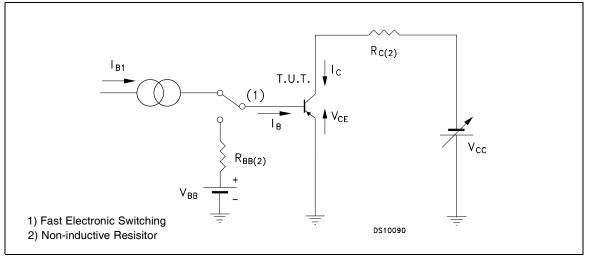


Figure 10. Resistive Load Switching Test Circuits



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## 4 Package Mechanical Data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



DIM. А b b1 С D Е е e1 F H1 J1 L L1 L20 L30

øΡ

Q

	mm.			inch		
MIN.	TYP	MAX.	MIN.	TYP.	MAX.	
4.40		4.60	0.173		0.181	
0.61		0.88	0.024		0.034	
1.15		1.70	0.045		0.066	
0.49		0.70	0.019		0.027	
15.25		15.75	0.60		0.620	
10		10.40	0.393		0.409	
2.40		2.70	0.094		0.106	
4.95		5.15	0.194		0.202	
1.23		1.32	0.048		0.052	
6.20		6.60	0.244		0.256	
2.40		2.72	0.094		0.107	
13		14	0.511		0.551	
3.50		3.93	0.137		0.154	
	16.40			0.645	1	

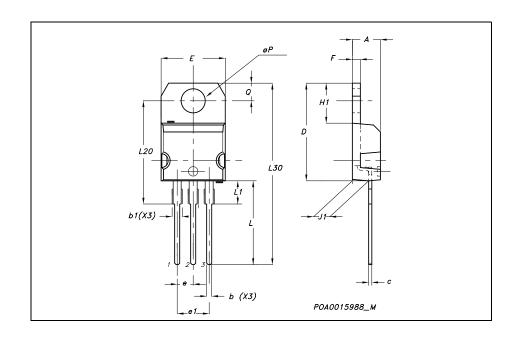
0.147

0.104

1.137

0.151

0.116



3.85

2.95

28.90

3.75

2.65



# 5 Revision History

Date	Revision	Changes
09-Dec-2005	2	Inserted curves



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