

STT13005D

High voltage fast-switching NPN power transistor

Features

- Integrated antiparallel collector-emitter diode
- High voltage capability
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed

Applications

- Electronic ballast for fluorescent lighting
- Flyback and forward single transistor low power converters

Description

The device is manufactured using high voltage multi-epitaxial planar technology for high switching speeds and medium voltage capability.

It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is designed for use in lighting applications and low cost switch-mode power supplies.

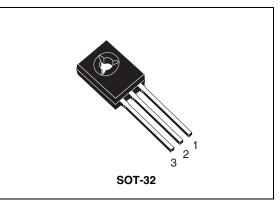
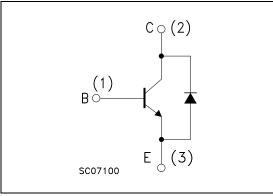


Figure 1. Internal schematic diagram



Order codes	Marking	Package	Packaging
STT13005D	T13005D	SOT-32	Tube
STT13005D-K	T13005D	SOT-32	Bag

November 2009

Doc ID 14897 Rev 2

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1 Electrical ratings

Table 2.	Absolute maximum ratings
	Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{BE} = 0)	700	V
V _{CEO}	Collector-emitter voltage ($I_B = 0$)	400	V
V _{EBO}	Emitter-base voltage (I _C = 0)	9	V
Ι _C	Collector current	2	А
I _{CM}	Collector peak current (t _P < 5 ms)	4	А
I _B	Base current	1	А
I _{BM}	Base peak current (t _P < 5 ms)	2	А
P _{TOT}	Total dissipation at $T_c = 25 \ ^{\circ}C$	45	W
T _{STG}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Thermal resistance junction-case Max	2.8	°C/W



2 Electrical characteristics

 T_{case} = 25 °C unless otherwise specified.

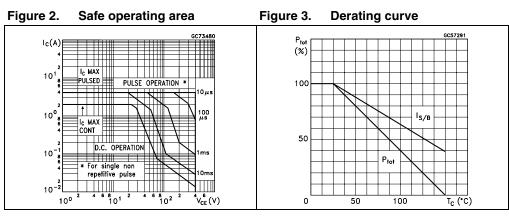
Symbol	mbol Parameter Test Conditions		Min.	Тур.	Max.	Unit
	Collector cut-off current	V _{CE} = 700 V			100	μA
ICES	(V _{BE} = 0)	$V_{CE} = 700 \text{ V} \text{ T}_{C} = 125 \text{ °C}$			500	μA
I _{CEO}	Collector cut-off current $(I_B = 0)$	V _{CE} = 400 V			250	μA
V _{EBO} Emitter-base voltage (I _C = 0)		I _E = 10 mA	9			v
$V_{CEO(sus)}^{(1)}$ Collector-emitter sustaining voltage $(I_B = 0)$		I _C = 10 mA	400			v
	Collector-emitter	$I_{\rm C} = 0.5 {\rm A}$ $I_{\rm B} = 125 {\rm mA}$			0.5	V
V _{CE(sat)} ⁽¹⁾	saturation voltage	$I_{\rm C} = 0.8 \text{ A}$ $I_{\rm B} = 0.2 \text{ A}$			1	V
		$I_{\rm C} = 1.6 {\rm A} \qquad I_{\rm B} = 0.4 {\rm A}$			1.5	V
	Deep emitter esturation	$I_{\rm C} = 0.5 {\rm A}$ $I_{\rm B} = 125 {\rm mA}$			1	V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = 0.8 A I _B = 0.2 A			1.3	V
	vonage	$I_{\rm C} = 1.6 \mbox{ A}$ $I_{\rm B} = 0.4 \mbox{ A}$			1.5	V
h _{FE} ⁽¹⁾	DC ourrent goin	$I_{C} = 0.5 A$ $V_{CE} = 5 V$	10		50	
^{II} FE ` ′	DC current gain	$I_{\rm C} = 2 \ {\rm A}$ $V_{\rm CE} = 5 \ {\rm V}$	8			
	Resistive load					
t _r	Rise time	$I_{\rm C} = 1 {\rm A}$ $V_{\rm CC} = 125 {\rm V}$		0.4	0.7	μs
t _s Storage time t _f Fall time		I _{B1} = -I _{B2} = 0.2 A		3.2	4.5	μs
				0.25	0.4	μs
	Inductive load	I _C = 1 A I _{B1} = 0.2 A				
t _s	Storage time	V _{BE(off)} = -5 V L= 50 mH		0.8		μs
t _f	Fall time	$V_{Clamp} = 300 V$		0.16		μs
V _F	Diode forward voltage	I _F = 1 A			2.5	V

Table 4. Electrical characteristics

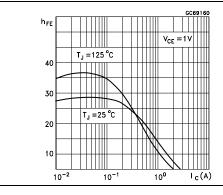
1. Pulse test: pulse duration \leq 300 $\mu s,$ duty cycle \leq 2 %

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2.1 Electrical characteristics (curves)







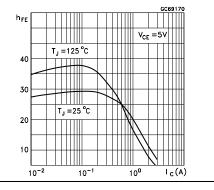
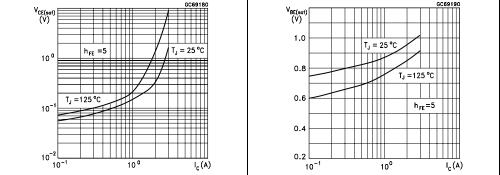
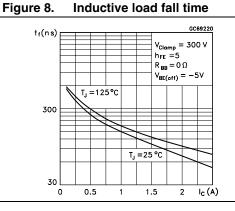
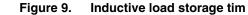


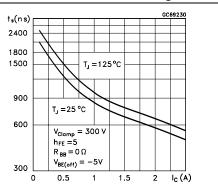
 Figure 6.
 Collector-emitter saturation voltage
 Figure 7.
 Base-emitter saturation voltage

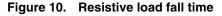


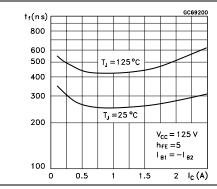


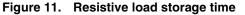












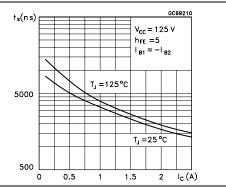
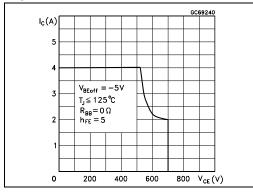


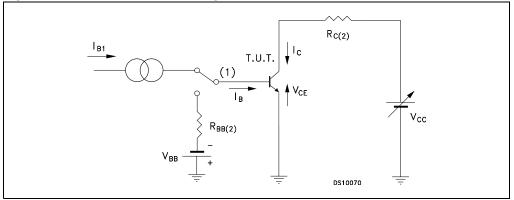
Figure 12. Reverse biased SOA



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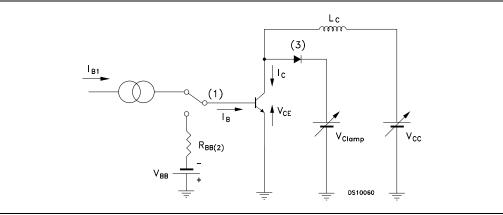
2.2 Test circuits

Figure 13. Resistive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor

Figure 14. Inductive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor
- 3. Fast recovery rectifier

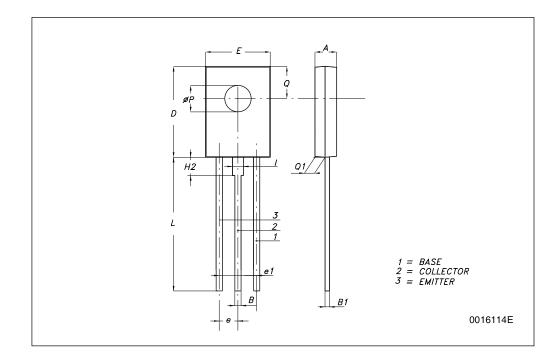


3 Package mechanical data

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IM.		mm.	
	MIN.	ТҮР	MAX.
A	2.4		2.9
В	0.64		0.88
B1	0.39		0.63
D	10.5		11.05
E	7.4		7.8
е	2.04	2.29	2.54
e1	4.07	4.58	5.08
L	15.3		16
Р	2.9		3.2
Q		3.8	
Q1	1		1.52
H2		2.15	
1		1.27	





4 Revision history

Table 5.Document revision history

Date	Revision	Changes	
10-Jul-2008	1	Initial release.	
03-Nov-2009	2	Added order code STT13005D-K Table 1 on page 1.	



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