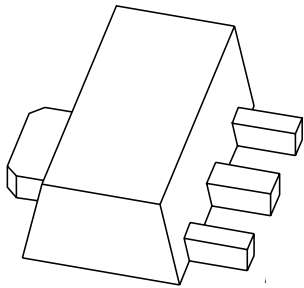


# DATA SHEET



## **PXTA92** PNP high-voltage transistor

Product data sheet  
Supersedes data of 1999 Apr 29

2004 Dec 09

# PNP high-voltage transistor

# PXTA92

### FEATURES

- Low current (max. 100 mA)
- High voltage (max. 300 V).

### APPLICATIONS

- Telephony and professional communication equipment.

### DESCRIPTION

PNP high-voltage transistor in a SOT89 plastic package.  
NPN complement: PXTA42.

### MARKING

TYPE NUMBER	MARKING CODE <sup>(1)</sup>
PXTA92	*1N

### Note

- \* = p: Made in Hong Kong.  
\* = t: Made in Malaysia.  
\* = W: Made in China.

### PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base

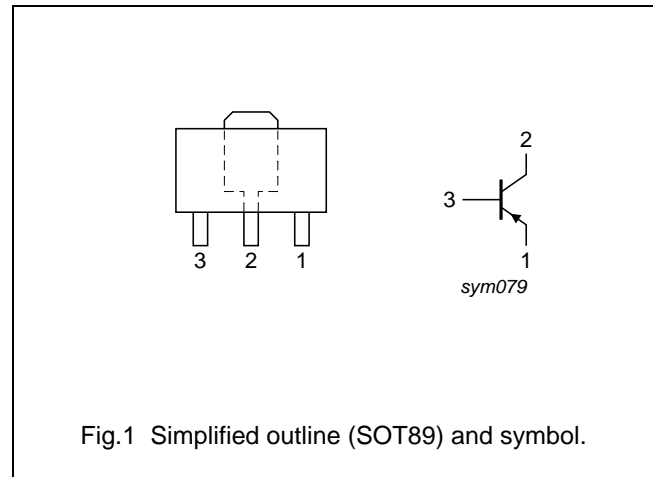


Fig.1 Simplified outline (SOT89) and symbol.

### ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
PXTA92	SC-62	plastic surface mounted package; collector pad for good heat transfer; 3 leads	SOT89

### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	–	–300	V
V <sub>CEO</sub>	collector-emitter voltage	open base	–	–300	V
V <sub>EBO</sub>	emitter-base voltage	open collector	–	–5	V
I <sub>C</sub>	collector current (DC)		–	–100	mA
I <sub>CM</sub>	peak collector current		–	–200	mA
I <sub>BM</sub>	peak base current		–	–100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	–	1.3	W
T <sub>stg</sub>	storage temperature		–65	+150	°C
T <sub>j</sub>	junction temperature		–	150	°C
T <sub>amb</sub>	ambient temperature		–65	+150	°C

### Note

- Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.  
For other mounting conditions, see “Thermal considerations for SOT89 in the General Part of associated Handbook”.

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## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	note 1	96	K/W
$R_{th(j-s)}$	thermal resistance from junction to soldering point		16	K/W

## Note

1. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.  
For other mounting conditions, see "Thermal considerations for SOT89 in the General Part of associated Handbook".

## CHARACTERISTICS

$T_{amb} = 25\text{ }^{\circ}\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$I_{CBO}$	collector-base cut-off current	$I_E = 0\text{ A}$ ; $V_{CB} = -200\text{ V}$	–	–250	nA
$I_{EBO}$	emitter-base cut-off current	$I_C = 0\text{ A}$ ; $V_{BE} = -3\text{ V}$	–	–100	nA
$h_{FE}$	DC current gain	$V_{CE} = -10\text{ V}$ ; note 1 $I_C = -1\text{ mA}$ $I_C = -10\text{ mA}$ $I_C = -30\text{ mA}$	25 40 25	– – –	
$V_{CEsat}$	collector-emitter saturation voltage	$I_C = -20\text{ mA}$ ; $I_B = -2\text{ mA}$	–	–500	mV
$V_{BEsat}$	base-emitter saturation voltage	$I_C = -20\text{ mA}$ ; $I_B = -2\text{ mA}$		–900	mV
$C_c$	collector capacitance	$I_E = i_e = 0\text{ A}$ ; $V_{CB} = -20\text{ V}$ ; $f = 1\text{ MHz}$	–	6	pF
$f_T$	transition frequency	$I_C = -10\text{ mA}$ ; $V_{CE} = -20\text{ V}$ ; $f = 100\text{ MHz}$	50	–	MHz

## Note

1. Pulse test:  $t_p \leq 300\text{ }\mu\text{s}$ ;  $\delta \leq 0.02$ .

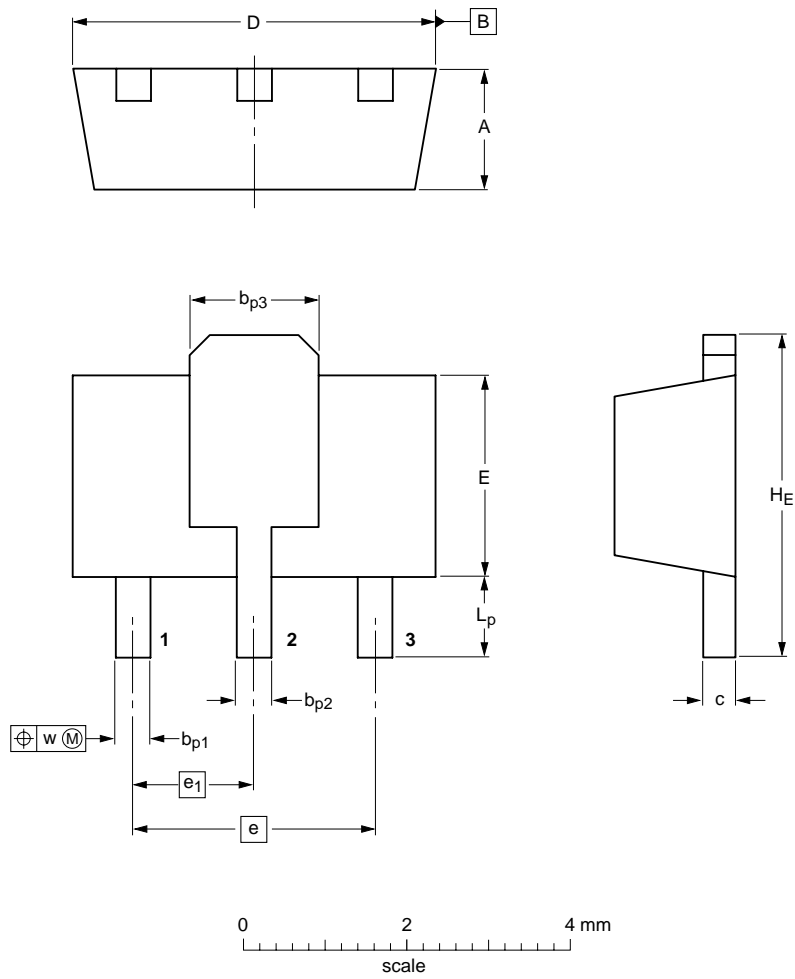
PNP high-voltage transistor

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PACKAGE OUTLINE

Plastic surface-mounted package; collector pad for good heat transfer; 3 leads

SOT89



DIMENSIONS (mm are the original dimensions)

UNIT	A	b <sub>p1</sub>	b <sub>p2</sub>	b <sub>p3</sub>	c	D	E	e	e <sub>1</sub>	H <sub>E</sub>	L <sub>p</sub>	w
mm	1.6 1.4	0.48 0.35	0.53 0.40	1.8 1.4	0.44 0.23	4.6 4.4	2.6 2.4	3.0	1.5	4.25 3.75	1.2 0.8	0.13

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT89		TO-243	SC-62			04-08-03 06-03-16

## PNP high-voltage transistor

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## DATA SHEET STATUS

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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## **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

## **Contact information**

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