

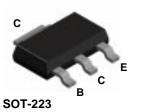
# MPSA65

# MMBTA65

# PZTA65







# **PNP Darlington Transistor**

This device is designed for applications requiring extremely high current gain at currents to 800 mA. Sourced from Process 61. See MPSA64 for characteristics.

## **Absolute Maximum Ratings\***

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CES</sub>	Collector-Emitter Voltage	30	V
V <sub>CBO</sub>	Collector-Base Voltage	30	V
V <sub>EBO</sub>	Emitter-Base Voltage	10	V
I <sub>C</sub>	Collector Current - Continuous	1.2	A
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

## **Thermal Characteristics**

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max		Units	
		MPSA65	*MMBTA65	**PZTA65	
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	1,000 8.0	mW mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3			°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	125	°C/W

<sup>\*</sup>Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

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<sup>1)</sup> These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

<sup>\*\*</sup>Device mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm; mounting pad for the collector lead min. 6 cm<sup>2</sup>.

# **PNP Darlington Transistor**

(continued)

Electrical Characteristics TA = 25°C unless otherwise noted					
Symbol	Parameter	Test Conditions	Min	Max	Unit
OFF CHARACTERISTICS					

V <sub>(BR)CES</sub>	Collector-Emitter Breakdown	$I_C = 100  \mu A, I_B = 0$	30		V
	Voltage	·			
I <sub>CBO</sub>	Collector-Cutoff Current	$V_{CB} = 30 \text{ V}, I_{E} = 0$		100	nA
I <sub>EBO</sub>	Emitter-Cutoff Current	$V_{EB} = 8.0 \text{ V}, I_{C} = 0$		100	nA

## ON CHARACTERISTICS\*

h <sub>FE</sub>	DC Current Gain	$I_C$ = 10 mA, $V_{CE}$ = 5.0 V $I_C$ = 100 mA, $V_{CE}$ = 5.0 V	50,000 20,000		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_C = 100 \text{ mA}, I_B = 0.1 \text{ mA}$		1.5	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = 100 mA, V <sub>CE</sub> = 5.0 V		2.0	V

## SMALL SIGNAL CHARACTERISTICS

f <sub>T</sub>	Current Gain - Bandwidth Product	$I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ V},$	100	MHz
		f = 100 MHz		

<sup>\*</sup>Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%

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