

2STC5200

High power NPN epitaxial planar bipolar transistor

Features

- High breakdown voltage V_{CEO} > 230V
- Complementary to 2STA1943
- Fast-switching speed
- Typical f_T = 30 MHz

Application

■ Audio power amplifier

Description

This device is a NPN transistor manufactured using new BiT-LA (Bipolar Transistor for linear amplifier) technology. The resulting transistor shows good gain linearity behaviour.

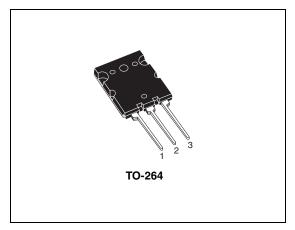


Figure 1. Internal schematic diagram

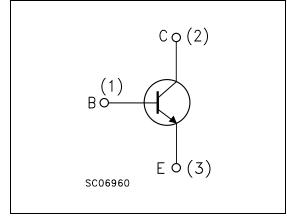


Table 1.	Device	summary
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Order code	Marking	Package	Packaging
2STC5200	2STC5200	TO-264	Tube

December	2007
Decounder	2007

1 Electrical ratings

Table 2.	Absolute maximum ratings		
Symbol	Parameter	Value	Unit
V_{CBO} Collector-base voltage (I _E = 0)		230	V
V _{CEO}	Collector-emitter voltage ($I_B = 0$)	230	V
V _{EBO}	Emitter-base voltage ($I_{C} = 0$)	5	V
$\begin{tabular}{ c c c c c } \hline I_C & Collector current \\ \hline I_{CM} & Collector peak current \\ \hline P_{tot} & Total dissipation at T_C = 25^\circ C \\ \hline T_{stg} & Storage temperature \\ \hline T_J & Operating junction temperature \\ \hline \end{tabular}$		15	А
		30	А
		150	W
		-55 to 150	°C
		150	°C

Table 2. Absolute maximum ratings

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJ-case}	Thermal resistance junction-case Max	0.83	°C/W

2 Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current $(I_E = 0)$	V _{CB} = 230 V			5	μA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 5 V			5	μA
V _{(BR)CEO} ⁽¹⁾	Collector-emitter breakdown voltage ($I_B = 0$)	I _C = 50 mA	230			V
$V_{(BR)CBO}$ Collector-base breakdown voltage (I _E = 0)		I _C = 100 μA	230			۷
$V_{(BR)EBO}^{(1)}$ Emitter-base breakdown voltage (I _C = 0)		I _E = 1 mA	5			۷
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 8 A I _B = 800 mA			3	V
V_{BE}	Base-emitter voltage	$I_{\rm C} = 7 \ {\rm A}$ $V_{\rm CE} = 5 \ {\rm V}$			1.5	V
h _{FE}	DC current gain	$I_{C} = 1 A$ $V_{CE} = 5 V$ $I_{C} = 7 A$ $V_{CE} = 5 V$	80 35		160	
t _{on} t _s t _f	Resistive load Turn-on time Storage time Fall time	$V_{CC} = 60 V I_C = 5A$ $I_{B1} = -I_{B2} = 0.5 A$		0.24 4.7 0.6		μs μs μs
f _T	Transition frequency	$I_{\rm C} = 1 \ {\rm A}$ $V_{\rm CE} = 5 \ {\rm V}$		30		MHz
C _{CBO}	Collector-base capacitance $(I_E = 0)$	V _{CB} = 10 V f = 1 MHz		150		pF

Table 4. Electrical characteristics

1. Pulsed: pulse duration = 300 μ s, duty cycle \leq 1.5%

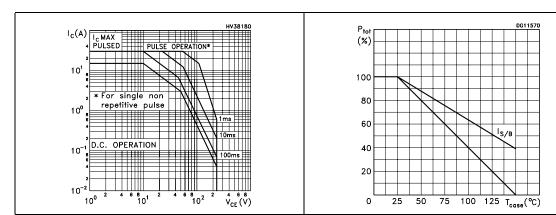
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2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

Figure 3. Derating curve







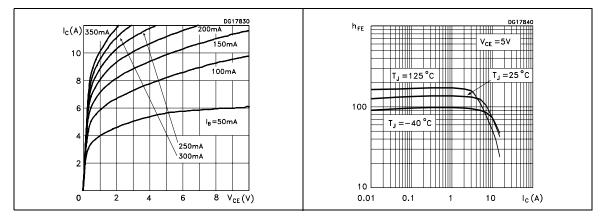
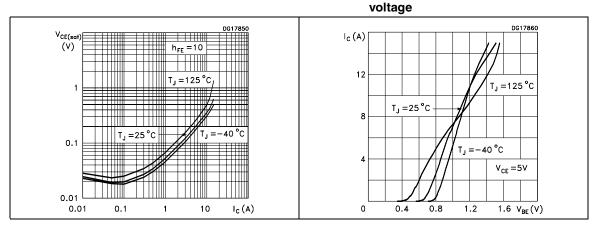


Figure 6. Collector-emitter saturation voltage Figure 7. Collector current vs base-emitter



2.2 Test circuit

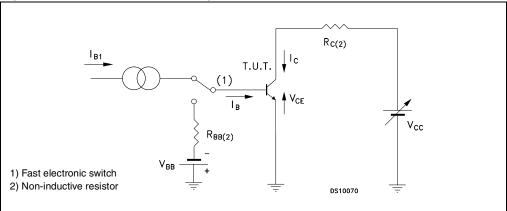


Figure 8. Resistive load switching test circuit

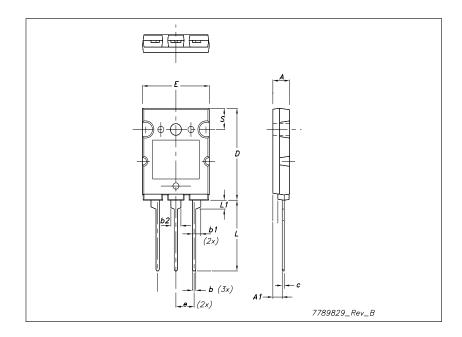


3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



	TO-264 Mechanical data		
Dim.		mm.	1
	Min.	Тур	Max.
А	4.80		5.20
A1	2.50		3.10
b	0.90	1.0	1.25
b1		2.5	
b2		2.8	
С	0.50	0.60	0.85
D	25.6		26.4
Е	19.80		20.20
е	5.15		5.75
L	19.50		20.50
L1	2.30		2.70
øP	3.55		3.65



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4 Revision history

Table 5.Document revision history

Date	Revision	Changes
19-Jun-2007	1	Initial release.
11-Dec-2007	2	Document promoted from preliminary data to datasheet.



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