

TIP142T TIP147T

Complementary power Darlington transistors

Features

- Monolithic Darlington configuration
- Integrated antiparallel collector-emitter diode

Application

■ Linear and switching industrial equipment

Description

The devices are manufactured in planar technology with "base island" layout and monolithic Darlington configuration. The resulting transistors show exceptional high gain performance coupled with very low saturation voltage.

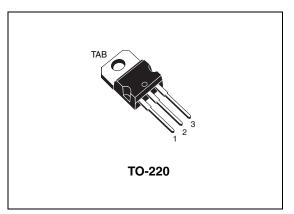


Figure 1. Internal schematic diagrams

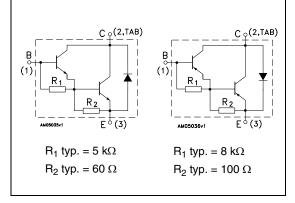


Table 1.Device summary

Part number	Marking	Polarity	Package	Packaging
TIP142T	TIP142T	NPN	TO-220	Tube
TIP147T	TIP147T	PNP	10-220	Tube

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1 Absolute maximum ratings

Table 2.	Absolute maximum ratings		
Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	100	V
V _{CEO}	Collector-emitter voltage $(I_B = 0)$	100	V
V_{EBO}	Emitter-base voltage ($I_C = 0$)	5	V
Ι _C	Collector current	10	А
I _{CM}	Collector peak current	20	A
Ι _Β	Base current	0.5	А
P _{TOT}	Total dissipation at T _{case} = 25 °C	90	W
T _{STG}	Storage temperature	-65 to 150	°C
Τ _J	Max. operating junction temperature	150	°C

Table 2. Absolute maximum ration	nas
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Note: For PNP type voltage and current are negative.

Table 3.Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Thermal resistance junction-case max	1.4	°C/W



2 Electrical characteristics

 $T_{case} = 25 \ ^{\circ}C$; unless otherwise specified.

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Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current $(I_E = 0)$	V _{CB} = 100 V			1	mA
I _{CEO}	Collector cut-off current $(I_B = 0)$	V _{CE} = 50 V			2	mA
I _{EBO}	Emitter cut-off current $(I_{C} = 0)$	V _{EB} = 5 V			2	mA
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage $(I_B = 0)$	I _C = 30 mA	100			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	$I_{C} = 5 A$ $I_{B} = 10 mA$ $I_{C} = 10 A$ $I_{B} = 40 mA$			2 3	V V
V _{BE(on)} ⁽¹⁾	Base-emitter on voltage	I _C = 10 A V _{CE} = 4 V			3	V
h _{FE} ⁽¹⁾	DC current gain		1000 500			
t _{on}	Resistive load Turn-on time	I _C = 10 A R _L = 3 Ω		0.9		μs
t _{off}	Turn-off time	$I_{B1} = -I_{B2} = 40 \text{ mA}$		4		μs

 Table 4.
 Electrical characteristics

1. Pulse test: pulse duration \leq 300 $\mu s,$ duty cycle \leq 2 %.

Note: For PNP type voltage and current are negative.



2.1 Test circuits

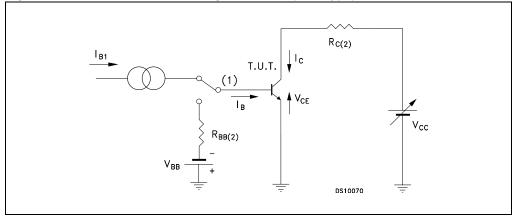
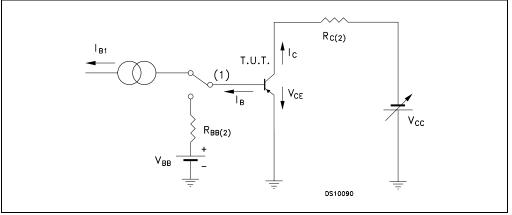


Figure 2. Resistive load switching test circuit (NPN type)

- 1. Fast electronic switch
- 2. Non-inductive resistor





- 1. Fast electronic switch
- 2. Non-inductive resistor



3 Package mechanical data

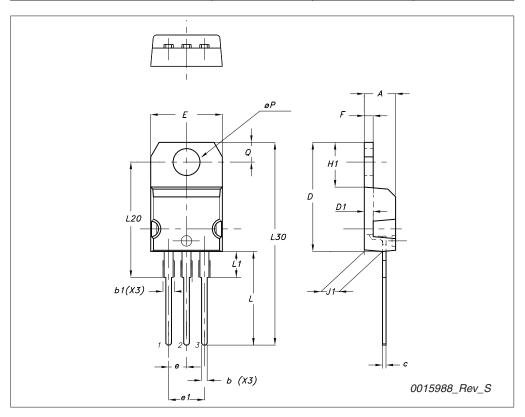
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TO-2201	tvpe A	mechanical	data

Dim	mm			
Dim	Min	Тур	Max	
A	4.40		4.60	
b	0.61		0.88	
b1	1.14		1.70	
С	0.48		0.70	
D	15.25		15.75	
D1		1.27		
E	10		10.40	
е	2.40		2.70	
e1	4.95		5.15	
F	1.23		1.32	
H1	6.20		6.60	
J1	2.40		2.72	
L	13		14	
L1	3.50		3.93	
L20		16.40		
L30		28.90		
ØP	3.75		3.85	
Q	2.65		2.95	



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4 Revision history

Table 5.Document revision history

Date	Revision	Changes
21-Jun-2004	4	
20-May-2010	5	Technology change from epitaxial base to planar base island.



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