

# 2STN2540

### Low voltage fast-switching PNP power bipolar transistor

### Features

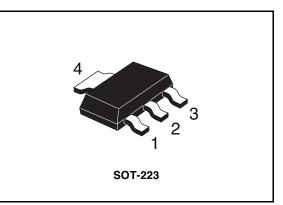
- Very low collector-emitter saturation voltage
- High current gain characteristic
- Fast switching speed
- Surface mounting device in medium power SOT-223 package

### **Applications**

- Emergency lighting
- LED
- CCFL drivers (back lighting)
- Voltage regulation
- Relay driver

### Description

The device is a PNP transistor manufactured using new "PB-HCD" (Power Bipolar High Current Density) technology. The resulting transistor shows exceptional high gain performances coupled with very low saturation voltage.



#### Figure 1. Internal schematic diagram

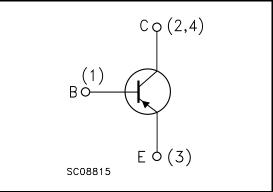


Table 1.	Device	summary
	Device	Summary

Order code	Marking	Package	Packaging
2STN2540	N2540	SOT-223	Tape and reel

# 1 Electrical ratings

Table 2.	Absolute maximum rating
	Absolute maximum rating

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-base voltage ( $I_E = 0$ )	-40	V
$V_{CEO}$	Collector-emitter voltage (I <sub>B</sub> = 0)	-40	V
$V_{\text{EBO}}$	Emitter-base voltage (I <sub>C</sub> = 0)	-6	V
۱ <sub>C</sub>	Collector current	-5	А
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5ms)	-10	А
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5ms)	-2	А
P <sub>tot</sub>	Total dissipation at $T_{amb} = 25 \ ^{\circ}C$	1.6	W
T <sub>stg</sub>	Storage temperature -65 to 150		°C
Т <sub>Ј</sub>	Max. operating junction temperature	150	°C

#### Table 3. Thermal data

Symbol	Parameter		Value	Unit
R <sub>thj-amb</sub> <sup>(1)</sup> Thermal resistant	Thermal resistance junction-amb max		78	°C/W

1. Device mounted on PCB area of 1cm<sup>2</sup>

## 2 Electrical characteristics

(T<sub>case</sub> = 25 °C unless otherwise specified)

Table 4.						
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I <sub>CBO</sub>	Collector cut-off current (I <sub>E</sub> =0)	V <sub>CB</sub> = -30 V			-0.1	μA
I <sub>EBO</sub>	Emitter cut-off current (I <sub>C</sub> =0)	V <sub>EB</sub> = -5 V			-0.1	μA
		I <sub>C</sub> = -0.5 A I <sub>B</sub> = -5 mA		-80	-120	mV
V <sub>CE(sat)</sub> <sup>(1)</sup>	Collector-emitter	I <sub>C</sub> = -1 A I <sub>B</sub> = -10 mA		-120	-180	mV
()	saturation voltage	I <sub>C</sub> = -2 A I <sub>B</sub> = -200 mA		-140	-200	mV
		I <sub>C</sub> = -5 A I <sub>B</sub> = -500 mA		-350	-450	mV
V <sub>BE(sat)</sub> <sup>(1)</sup>	Base-emitter saturation voltage	I <sub>C</sub> = -5 A I <sub>B</sub> = -500 mA			-1.3	V
V <sub>BE(on)</sub> <sup>(1)</sup>	Base-emitter on voltage	$V_{CE} = -2 V I_{C} = -2 A$			-1.25	V
		I <sub>C</sub> = -0.5 A V <sub>CE</sub> = -2 V	250			
h <sub>FE</sub> <sup>(1)</sup>	DC ourrent goin	$I_{C} = -1 A$ $V_{CE} = -2 V$	200			
"FE`'	DC current gain	$I_{C} = -2 A$ $V_{CE} = -2 V$	150			
		$I_{C} = -5 A$ $V_{CE} = -2 V$	50			
C <sub>CBO</sub>	Collector-base capacitance	I <sub>E</sub> = 0 V <sub>CB</sub> = -10 V f = 1 MHz		80		pF
	Resistive load	$I_{\rm C} = -1 \ {\rm A}$ $V_{\rm CC} = -10 \ {\rm V}$				
t <sub>on</sub>	Turn-on time	-I <sub>B1</sub> = I <sub>B2</sub> = -0.1 A		75		ns
t <sub>s</sub>	Storage time	T <sub>p</sub> = 30 μs		426		ns
t <sub>f</sub>	Fall time			62		ns

 Table 4.
 Electrical characteristics

1. Pulsed duration = 300  $\mu$ s, duty cycle  $\leq .5\%$ 

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#### 2.1 Electrical characteristics (curves)

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Figure 2. Output characteristics
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Figure 3. DC current gain

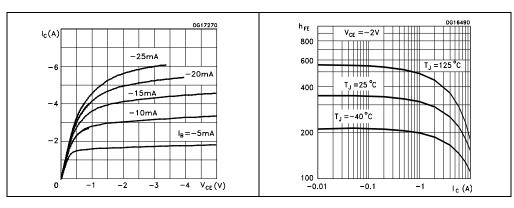


Figure 4. Collector-emitter saturation Figure 5. Base voltage volta

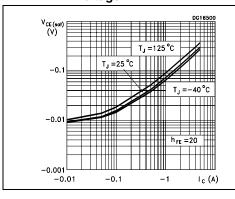


Figure 6. Base-emitter on voltage

Jure 5. Base-emitter saturation voltage

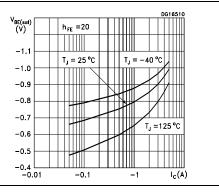
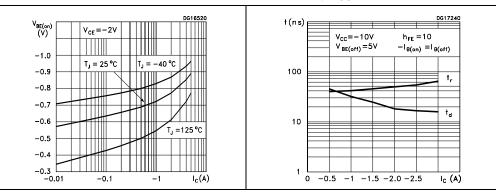


Figure 7. Resistive load switching times



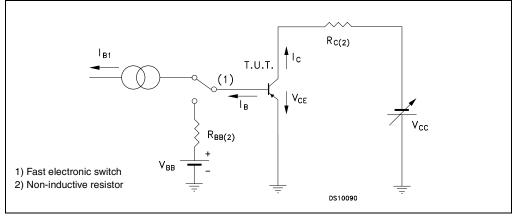
#### times DG17250 DG1726 t (n s) C(pF) $V_{CC} = -10V$ $V_{BE(off)} = 5V$ $h_{FE} = 10$ $-I_{B(on)} = I_{B(off)}$ f=1MHz 1000 1000 $t_s$ 100 100 †<sub>f</sub> Ссв 10 ∟ 0 10 └─ -0.1 -0.5 -1 -1.5 -2.0 -2.5 I<sub>C</sub> (A) -10 $V_{R}(V)$ -1

## Figure 8. Resistive load switching times

Figure 9. Capacitance

### 2.2 Test circuit





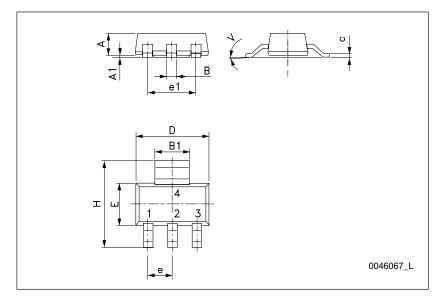


### 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



	SOT-223 mechanical data			
DIM.		mm.		
Diwi.	min.	typ	max.	
A			1.80	
A1	0.02		0.1	
В	0.60	0.70	0.85	
B1	2.90	3.00	3.15	
с	0.24	0.26	0.35	
D	6.30	6.50	6.70	
е		2.30		
e1		4.60		
E	3.30	3.50	3.70	
н	6.70	7.00	7.30	
V			10 °	





# 4 Revision history

#### Table 5.Document revision history

Date	Revision	Changes
23-Oct-2003	1	Initial release
03-Nov-2006	2	Added new graphics: fig.2, fig. 7, fig.8, fig.9.
14-Jan-2008	3	Document status promoted from preliminary data to datasheet.



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