Silicon Power Transistors

The MJW21193 and MJW21194 utilize Perforated Emitter technology and are specifically designed for high power audio output, disk head positioners and linear applications.

Features

- Total Harmonic Distortion Characterized
- High DC Current Gain -

 $h_{FE} = 20 \text{ Min} @ I_C = 8 \text{ Adc}$

- Excellent Gain Linearity
- High SOA: 2.25 A, 80 V, 1 Second
- Pb–Free Packages are Available

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	250	Vdc
Collector-Base Voltage	V _{CBO}	400	Vdc
Emitter-Base Voltage	V _{EBO}	5.0	Vdc
Collector-Emitter Voltage - 1.5 V	V _{CEX}	400	Vdc
Collector Current – Continuous – Peak (Note 1)	Ι _C	16 30	Adc
Base Current – Continuous	Ι _Β	5.0	Adc
Total Power Dissipation @ T _C = 25°C Derate Above 25°C	P _D	200 1.43	W W/°C
Operating and Storage Junction Temperature Range	TJ, T _{stg}	– 65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ extsf{ heta}JC}$	0.7	°C/W
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	40	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

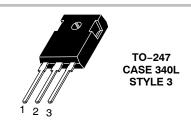
1. Pulse Test: Pulse Width = 5 μ s, Duty Cycle \leq 10%.



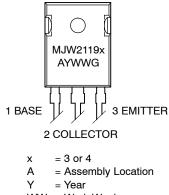
ON Semiconductor®

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16 AMPERES COMPLEMENTARY SILICON POWER TRANSISTORS 250 VOLTS, 200 WATTS







- WW = Work Week
- G = Pb-Free Package

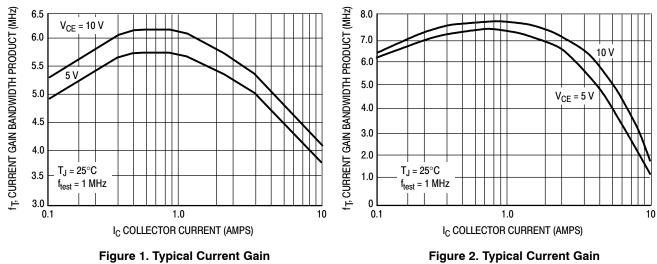
ORDERING INFORMATION

Device	Package	Shipping
MJW21193	TO-247	30 Units/Rail
MJW21193G	TO-247 (Pb-Free)	30 Units/Rail
MJW21194	TO-247	30 Units/Rail
MJW21194G	TO-247 (Pb-Free)	30 Units/Rail

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector-Emitter Sustaining Voltage $(I_{C} = 100 \text{ mAdc}, I_{B} = 0)$		V _{CEO(sus)}	250	_	_	Vdc
Collector Cutoff Current (V _{CE} = 200 Vdc, I _B = 0)		I _{CEO}	_	_	100	μAdc
Emitter Cutoff Current ($V_{CE} = 5 \text{ Vdc}, I_C = 0$)		I _{EBO}	_	_	100	μAdc
Collector Cutoff Current (V _{CE} = 250 Vdc, V _{BE(off)} = 1.5 Vdc)		I _{CEX}	_	_	100	μAdc
SECOND BREAKDOWN						
Second Breakdown Collector Current with Base Forward Biased $(V_{CE} = 50 \text{ Vdc}, t = 1 \text{ s (non-repetitive)})$ $(V_{CE} = 80 \text{ Vdc}, t = 1 \text{ s (non-repetitive)})$		I _{S/b}	4.0 2.25	-		Adc
ON CHARACTERISTICS						
DC Current Gain (I _C = 8 Adc, V _{CE} = 5 Vdc) (I _C = 16 Adc, I _B = 5 Adc)		h _{FE}	20 8		80 -	
Base–Emitter On Voltage (I _C = 8 Adc, V _{CE} = 5 Vdc)		V _{BE(on)}	_	_	2.2	Vdc
Collector–Emitter Saturation Voltage ($I_C = 8 \text{ Adc}, I_B = 0.8 \text{ Adc}$) ($I_C = 16 \text{ Adc}, I_B = 3.2 \text{ Adc}$)		V _{CE(sat)}		-	1.4 4	Vdc
DYNAMIC CHARACTERISTICS						
Total Harmonic Distortion at the Output V _{RMS} = 28.3 V, f = 1 kHz, P _{LOAD} = 100 W _{RMS}	h _{FE} unmatched	T _{HD}		0.8	_	%
(Matched pair h_{FE} = 50 @ 5 A/5 V)	h _{FE} matched		_	0.08	_	
Current Gain Bandwidth Product (I _C = 1 Adc, V _{CE} = 10 Vdc, f _{test} = 1 MHz)		f _T	4	_	-	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f _{test} = 1 MHz)		C _{ob}	_	_	500	pF





Bandwidth Product



NPN MJW21194

TYPICAL CHARACTERISTICS

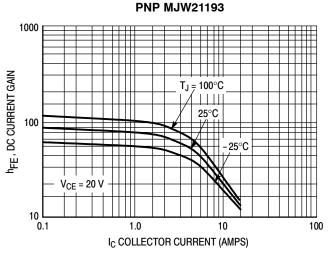
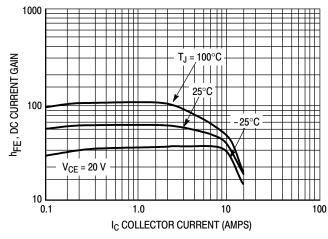


Figure 3. DC Current Gain, V_{CE} = 20 V



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Figure 4. DC Current Gain, V_{CE} = 20 V

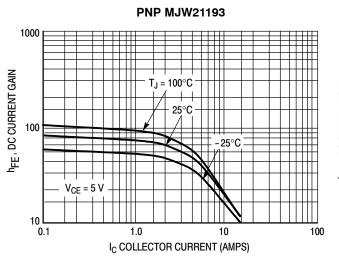
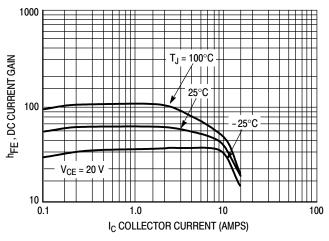


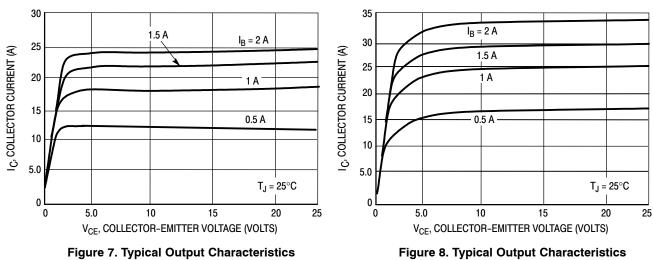
Figure 5. DC Current Gain, V_{CE} = 5 V







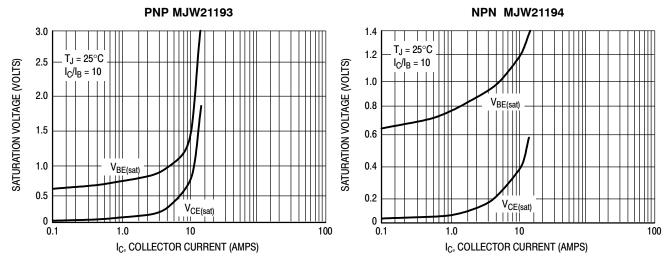
NPN MJW21194

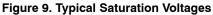


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TYPICAL CHARACTERISTICS







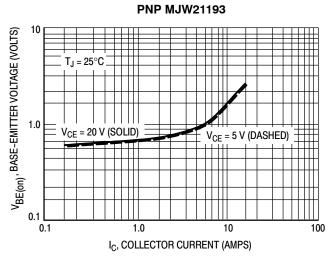


Figure 11. Typical Base–Emitter Voltage

PNP MJW21193

1 Sec

10

10 mSec

100

100 mSec

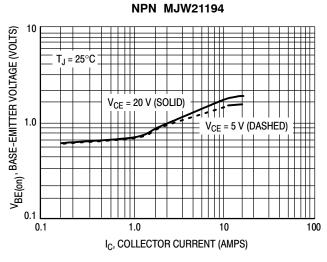
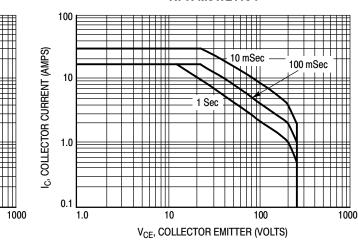


Figure 12. Typical Base-Emitter Voltage



V_{CE}, COLLECTOR EMITTER (VOLTS) Figure 13. Active Region Safe Operating Area

NPN MJW21194



100

10

1.0

0.1

1.0

I_C, Collector Current (AMPS)

There are two limitations on the power handling ability of a transistor; average junction temperature and secondary breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 13 is based on $T_{J(pk)} = 150^{\circ}C$; T_C is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power than can be handled to values less than the limitations imposed by second breakdown.

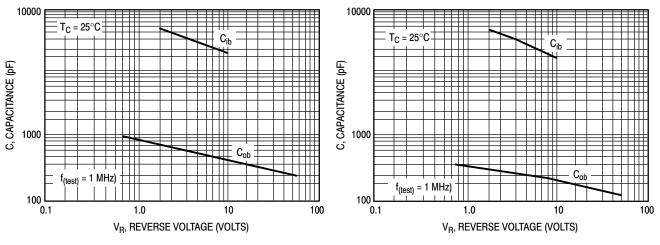


Figure 15. MJW21193 Typical Capacitance

Figure 16. MJW21194 Typical Capacitance

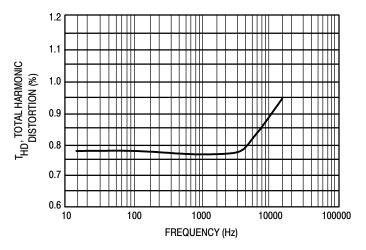


Figure 17. Typical Total Harmonic Distortion

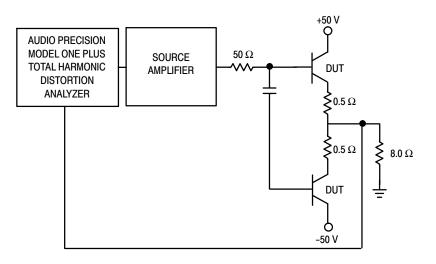
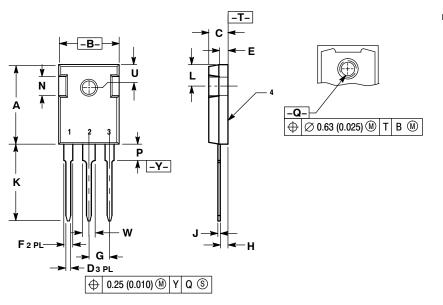


Figure 18. Total Harmonic Distortion Test Circuit

PACKAGE DIMENSIONS

TO-247 CASE 340L-02 ISSUE E



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	20.32	21.08	0.800	8.30	
В	15.75	16.26	0.620	0.640	
С	4.70	5.30	0.185	0.209	
D	1.00	1.40	0.040	0.055	
ш	1.90	2.60	0.075	0.102	
F	1.65	2.13	0.065	0.084	
G	5.45 BSC		0.215 BSC		
Η	1.50	2.49	0.059	0.098	
ſ	0.40	0.80	0.016	0.031	
Κ	19.81	20.83	0.780	0.820	
Г	5.40	6.20	0.212	0.244	
Ν	4.32	5.49	0.170	0.216	
Ρ		4.50		0.177	
Ø	3.55	3.65	0.140	0.144	
C	6.15 BSC		0.242 BSC		
M	2.87	3.12	0.113	0.123	

STYLE 3: PIN 1. BASE 2. COLLECTOR 3. EMITTER

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