# **BU406, BU407**

# **NPN Power Transistors**

These devices are high voltage, high speed transistors for horizontal deflection output stages of TV's and CRT's.

# **Features**

High Voltage: V<sub>CEV</sub> = 330 or 400 V
 Fast Switching Speed: t<sub>f</sub> = 750 ns (max)

• Low Saturation Voltage:  $V_{CE(sat)} = 1 \text{ V (max)} @ 5 \text{ A}$ 

• Pb-Free Packages are Available\*

### **MAXIMUM RATINGS**

Rating		Symbol	Value	Unit
Collector-Emitter Voltage	BU406 BU407	V <sub>CEO</sub>	200 150	Vdc
Collector-Emitter Voltage	BU406 BU407	V <sub>CEV</sub>	400 330	Vdc
Collector-Base Voltage	BU406 BU407	V <sub>CBO</sub>	400 330	Vdc
Emitter-Base Voltage		V <sub>EBO</sub>	6	Vdc
Collector Current - Continuous - Peak Repetitive - Peak (10 ms)		I <sub>C</sub>	7 10 15	Adc
Base Current		Ι <sub>Β</sub>	4	Adc
Total Device Dissipation @ T <sub>C</sub> = 29 Derate above 25°C	5°C	P <sub>D</sub>	60 0.48	W W/°C
Operating and Storage Junction Temperature Storage		T <sub>J</sub> , T <sub>stg</sub>	-65 to 150	°C

# THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.08	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	70	°C/W
Maximum Lead Temperature for Soldering Purposes1/8" from Case for 5 Seconds	TL	260	°C

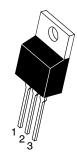
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



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# NPN SILICON POWER TRANSISTORS 7 AMPERES – 60 WATTS 150 AND 200 VOLTS



TO-220AB CASE 221A-09 STYLE 1

# **MARKING DIAGRAM**



BU40x = Specific Device Code

x = 6 or 7

A = Assembly Location

Y = Year
WW = Work Week
G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping
BU406	TO-220AB	50 Units / Rail
BU406G	TO-220AB (Pb-Free)	50 Units / Rail
BU407	TO-220AB	50 Units / Rail
BU407G	TO-220AB (Pb-Free)	50 Units / Rail

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# BU406, BU407

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector–Emitter Sustaining Voltage (Note 1) (I <sub>C</sub> = 100 mAdc, I <sub>B</sub> = 0)	BU406 BU407	V <sub>CEO(sus)</sub>	200 150	_ _	_ _	Vdc
		I <sub>CES</sub>	- - -	- - -	5 0.1 1	mAdc
Emitter Cutoff Current (V <sub>EB</sub> = 6 Vdc, I <sub>C</sub> = 0)	BU406, BU407	I <sub>EBO</sub>	-	_	1	mAdc
ON CHARACTERISTICS (Note 1)						
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 5 Adc, I <sub>B</sub> = 0.5 Adc)		$V_{CE(sat)}$	-	_	1	Vdc
Base-Emitter Saturation Voltage (I <sub>C</sub> = 5 Adc, I <sub>B</sub> = 0.5 Adc)		V <sub>BE(sat)</sub>	-	-	1.2	Vdc
Forward Diode Voltage (I <sub>EC</sub> = 5 Adc) "D" only		V <sub>EC</sub>	-	-	2	Volts
DYNAMIC CHARACTERISTICS						
Current-Gain – Bandwidth Product (I <sub>C</sub> = 0.5 Adc, V <sub>CE</sub> = 10 Vdc, f <sub>test</sub> = 20 MHz)		f <sub>T</sub>	10	-	-	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1 MHz)		C <sub>ob</sub>	-	80	-	pF
SWITCHING CHARACTERISTICS						
Inductive Load Crossover Time $(V_{CC} = 40 \text{ Vdc}, I_C = 5 \text{ Adc}, I_{B1} = I_{B2} = 0.5 \text{ Adc},$	L = 150 μH)	t <sub>c</sub>	-	_	0.75	μs

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu s,$  Duty Cycle  $\leq$  1%.

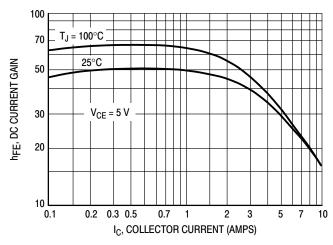


Figure 1. DC Current Gain

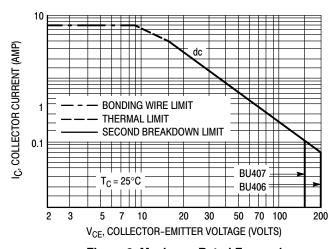
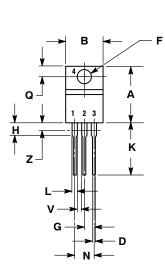


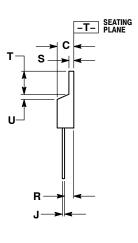
Figure 2. Maximum Rated Forward Bias Safe Operating Area

# BU406, BU407

# PACKAGE DIMENSIONS

TO-220AB CASE 221A-09 **ISSUE AF** 





- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.570	0.620	14.48	15.75	
В	0.380	0.405	9.66	10.28	
С	0.160	0.190	4.07	4.82	
D	0.025	0.035	0.64	0.88	
F	0.142	0.161	3.61	4.09	
G	0.095	0.105	2.42	2.66	
Н	0.110	0.155	2.80	3.93	
J	0.014	0.025	0.36	0.64	
K	0.500	0.562	12.70	14.27	
L	0.045	0.060	1.15	1.52	
N	0.190	0.210	4.83	5.33	
Q	0.100	0.120	2.54	3.04	
R	0.080	0.110	2.04	2.79	
S	0.045	0.055	1.15	1.39	
T	0.235	0.255	5.97	6.47	
U	0.000	0.050	0.00	1.27	
٧	0.045		1.15		
Z		0.080		2.04	

#### STYLE 1:

- PIN 1. BASE
  - COLLECTOR
  - **EMITTER**
  - COLLECTOR

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