BD136, BD138, BD140

Plastic Medium Power Silicon PNP Transistor

This series of plastic, medium-power silicon PNP transistors are designed for use as audio amplifiers and drivers utilizing complementary or quasi complementary circuits.

Features

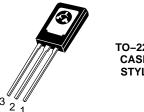
- Pb-Free Packages are Available*
- DC Current Gain $-h_{FE} = 40$ (Min) @ $I_{C} = 0.15$ Adc
- BD 136, 138, 140 are complementary with BD 135, 137, 139



ON Semiconductor[®]

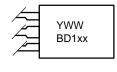
http://onsemi.com

1.5 A POWER TRANSISTORS PNP SILICON 45, 60, 80 V, 12.5 W



TO-225AA CASE 77 STYLE 1

MARKING DIAGRAM



= 36, 38, 40 xx = Year v WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping [†]
BD136	TO-225AA	500 Units/Box
BD136G	TO–225AA (Pb–Free)	500 Units/Box
BD138	TO-225AA	500 Units/Box
BD138G	TO–225AA (Pb–Free)	500 Units/Box
BD140	TO-225AA	500 Units/Box
BD140G	TO-225AA (Pb-Free)	500 Units/Box

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Collector–Emitter Voltage	BD136 BD138 BD140	V _{CEO}	45 60 80	Vdc
Collector-Base Voltage	BD136 BD138 BD140	V _{CBO}	45 60 100	Vdc
Emitter-Base Voltage		V _{EBO}	5.0	Vdc
Collector Current		۱ _C	1.5	Adc
Base Current		Ι _Β	0.5	Adc
Total Device Dissipation @ T _A = Derate above 25°C	= 25°C	P _D	1.25 10	Watts mW/°C
Total Device Dissipation @ T _C = Derate above 25°C	= 25°C	PD	12.5 100	Watts mW/°C
Operating and Storage Junctior Temperature Range	1	T _J , T _{stg}	-55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	θJC	10	°C/W
Thermal Resistance, Junction-to-Ambient	θ_{JA}	100	°C/W

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Downloaded from Elcodis.com electronic components distributor

BD136, BD138, BD140

Characteristic	Symbol	Туре	Min	Max	Unit
Collector–Emitter Sustaining Voltage* (I _C = 0.03 Adc, I _B = 0)	BV _{CEO}	BD 136 BD 138 BD 140	45 60 80	- - -	Vdc
Collector Cutoff Current ($V_{CB} = 30 \text{ Vdc}, I_E = 0$) ($V_{CB} = 30 \text{ Vdc}, I_E = 0, T_C = 125 \text{ °C}$)	I _{CBO}		-	0.1 10	μAdc
Emitter Cutoff Current ($V_{BE} = 5.0 \text{ Vdc}, I_C = 0$)	I _{EBO}		-	10	μAdc
$ \begin{array}{l} \text{DC Current Gain} \\ (I_{C} = 0.005 \text{ A}, \text{ V}_{CE} = 2 \text{ V}) \\ (I_{C} = 0.15 \text{ A}, \text{ V}_{CE} = 2 \text{ V}) \\ (I_{C} = 0.5 \text{ A}, \text{ V}_{CE} = 2 \text{ V}) \end{array} $	h _{FE} *		25 40 25	_ 250 _	-
Collector–Emitter Saturation Voltage* ($I_C = 0.5 \text{ Adc}, I_B = 0.05 \text{ Adc}$)	V _{CE(sat)} *		-	0.5	Vdc
Base–Emitter On Voltage* (I _C = 0.5 Adc, V _{CE} = 2.0 Vdc)	V _{BE(on)} *		-	1	Vdc

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

*Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

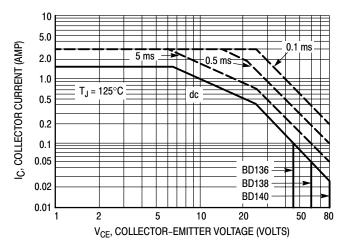
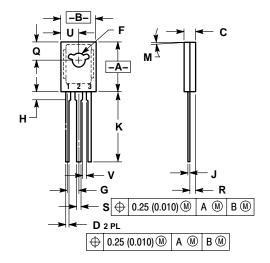
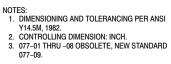


Figure 1. Active–Region Safe Operating Area

PACKAGE DIMENSIONS

TO-225AA CASE 77-09 ISSUE Z





	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.425	0.435	10.80	11.04
В	0.295	0.305	7.50	7.74
С	0.095	0.105	2.42	2.66
D	0.020	0.026	0.51	0.66
F	0.115	0.130	2.93	3.30
G	0.094	BSC	2.39 BSC	
Н	0.050	0.095	1.27	2.41
J	0.015	0.025	0.39	0.63
K	0.575	0.655	14.61	16.63
М	5° TYP		5°	TYP
Q	0.148	0.158	3.76	4.01
R	0.045	0.065	1.15	1.65
S	0.025	0.035	0.64	0.88
U	0.145	0.155	3.69	3.93
٧	0.040		1.02	

STYLE 1: PIN 1. EMITTER 2. COLLECTOR 3. BASE

BD136, BD138, BD140

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