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.M195/LM395 Ultra Reliable Power Transistors



LM195/LM395 Ultra Reliable Power Transistors General Description

The LM195/LM395 are fast, monolithic power integrated circuits with complete overload protection. These devices, which act as high gain power transistors, have included on the chip, current limiting, power limiting, and thermal overload protection making them virtually impossible to destroy from any type of overload. In the standard TO-3 transistor power package, the LM195 will deliver load currents in excess of 1.0A and can switch 40V in 500 ns.

The inclusion of thermal limiting, a feature not easily available in discrete designs, provides virtually absolute protection against overload. Excessive power dissipation or inadequate heat sinking causes the thermal limiting circuitry to turn off the device preventing excessive heating.

The LM195 offers a significant increase in reliability as well as simplifying power circuitry. In some applications, where protection is unusually difficult, such as switching regulators, lamp or solenoid drivers where normal power dissipation is low, the LM195 is especially advantageous.

The LM195 is easy to use and only a few precautions need be observed. Excessive collector to emitter voltage can destroy the LM195 as with any power transistor. When the device is used as an emitter follower with low source impedance, it is necessary to insert a 5.0k resistor in series with the base lead to prevent possible emitter follower oscillations. Although the device is usually stable as an emitter follower, the resistor eliminates the possibility of trouble without degrading performance. Finally, since it has good high frequency response, supply bypassing is recommended.

For low-power applications (under 100 mA), refer to the LP395 Ultra Reliable Power Transistor.

The LM195/LM395 are available in the standard TO-3, Kovar TO-5, and TO-220 packages. The LM195 is rated for operation from -55° C to $+150^{\circ}$ C and the LM395 from 0°C to $+125^{\circ}$ C.

Features

- Internal thermal limiting
- Greater than 1.0A output current
- 3.0 µA typical base current
- 500 ns switching time
- 2.0V saturation
- Base can be driven up to 40V without damage
- Directly interfaces with CMOS or TTL
- 100% electrical burn-in









Connection Diagrams







CASE IS EMITTER

00600904 **Bottom View** Order Number LM195H/883 See NS Package Number H03B (Note 5)

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Collector to Emitter Voltage	
LM195	42V
LM395	36V
Collector to Base Voltage	
LM195	42V
LM395	36V
Base to Emitter Voltage (Forward)	
LM195	42V
LM395	36V

Base to Emitter Voltage (Reverse)	20V		
Collector Current	Internally Limited		
Power Dissipation	Internally Limited		
Operating Temperature Range			
LM195	–55°C to +150°C		
LM395	0°C to +125°C		
Storage Temperature Range	–65°C to +150°C		
Lead Temperature			
(Soldering, 10 sec.)	260°C		

Preconditioning

100% Burn-In In Thermal Limit

Electrical Characteristics

(Note 2)

Parameter	Conditions	LM195			LM395			Units	
		Min	Тур	Max	Min	Тур	Max		
Collector-Emitter Operating Voltage	$I_Q \le I_C \le I_{MAX}$			42			36	V	
(Note 4)									
Base to Emitter Breakdown Voltage	$0 \le V_{CE} \le V_{CEMAX}$	42			36	60		V	
Collector Current									
TO-3, TO-220	$V_{CE} \le 15V$	1.2	2.2		1.0	2.2		A	
TO-5	$V_{CE} \le 7.0V$	1.2	1.8		1.0	1.8		A	
Saturation Voltage	$I_C \le 1.0A, T_A = 25^{\circ}C$		1.8	2.0		1.8	2.2	V	
Base Current	$0 \le I_C \le I_{MAX}$		3.0 5.0	5.0		3.0	10	μΑ	
	$0 \le V_{CE} \le V_{CEMAX}$			5.0					
Quiescent Current (I _Q)	$V_{be} = 0$		2.0 5.0	20 50	5.0		2.0	10	
	$0 \le V_{CE} \le V_{CEMAX}$			2.0	2.0	5.0		2.0	
Base to Emitter Voltage	I _C = 1.0A, T _A = +25°C		0.9			0.9		V	
Switching Time	$V_{CE} = 36V, R_{L} = 36\Omega,$	500	500		500				
	$T_A = 25^{\circ}C$			500			500		ns
Thermal Resistance Junction to	TO-3 Package (K)		2.3	3.0		2.3	3.0	°C/W	
Case (Note 3)	TO-5 Package (H)		12	15		12	15	°C/W	
	TO-220 Package (T)					4	6	°C/W	

Note 1: "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.

Note 2: Unless otherwise specified, these specifications apply for $-55^{\circ}C \le T_j \le +150^{\circ}C$ for the LM195 and $0^{\circ}C \le +125^{\circ}C$ for the LM395.

Note 3: Without a heat sink, the thermal resistance of the TO-5 package is about +150°C/W, while that of the TO-3 package is +35°C/W.

Note 4: Selected devices with higher breakdown available.

Note 5: Refer to RETS195H and RETS195K drawings of military LM195H and LM195K versions for specifications.

LM195/LM395

LM195/LM395

Typical Performance Characteristics

(for K and T Packages)







LM195/LM395





Typical Applications (Continued)



Typical Applications (Continued)

Fast Optically Isolated Switch



CMOS or TTL Lamp Interface



Optically Isolated Power Transistor



Two Terminal Current Limiter



00600922





















