

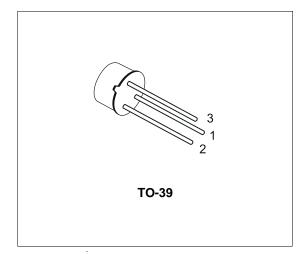
2N1711

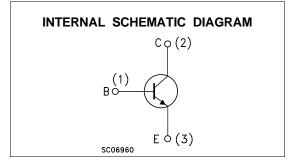
EPITAXIAL PLANAR NPN

DESCRIPTION

The 2N1711 is a silicon Planar Epitaxial NPN transistor in Jedec TO-39 metal case. It is intented for use in high performance amplifier, oscillator and switching circuits.

The 2N1711 is also used to advantage in amplifiers where low noise is an important factor.





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit	
V _{СВО}	Collector-Base Voltage (I _E = 0)	75	V	
VCER	Collector-Emitter Voltage ($R_{BE} \le 10\Omega$)	50	V	
V _{EBO}	Emitter-Base Voltage ($I_C = 0$)	7	V	
Ic	Collector Current	500	mA	
P _{tot}	Total Dissipation at $T_{amb} \le 25$ °C at $T_C \le 25$ °C at $T_C \le 100$ °C	0.8 3 1.7	W W W	
T _{stg}	Storage Temperature	-65 to 175	°C	
Tj	Max. Operating Junction Temperature	175	°C	

September 2002

THERMAL DATA

R _{thj-case}	Thermal Resistance Junction-Case	Max	50	°C/W
Rthj-amb	Thermal Resistance Junction-Ambient	Max	187.5	°C/W

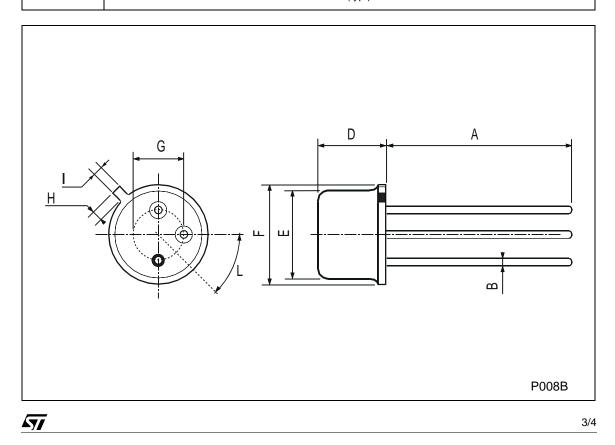
ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{СВО}	Collector Cut-off Current (I _E = 0)	$V_{CB} = 60 V$ $V_{CB} = 60 V$ $T_{C} = 150 °C$			10 10	nA μA
I _{EBO}	Emitter Cut-off Current (I _C = 0)	$V_{EB} = 5 V$			5	nA
V _(BR) CBO	Collector-Base Breakdown Voltage (I _E = 0)	Ic = 100 μA	75			V
V _{(BR)CER} *	Collector-Emitter Breakdown Voltage ($R_{BE} \le 10\Omega$)	Ic = 10 mA	50			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage (I _C = 0)	I _E = 100 μA	7			V
$V_{CE(sat)^*}$	Collector-Emitter Saturation Voltage	$I_{C} = 150 \text{ mA}$ $I_{B} = 15 \text{ mA}$		0.5	1.5	V
$V_{BE(sat)^*}$	Base-Emitter Saturation Voltage	I _C = 150 mA I _B = 15 mA		0.95	1.3	V
h _{FE} *	DC Current Gain		20 35 75 100 40 35	60 80 130 130 75 65	300	
h _{fe}	Small Signal Current Gain	$I_C = 1 \text{ mA}$ $V_{CE} = 10 \text{ V} \text{ f} = 1 \text{ KHz}$	70	135	300	
f _T	Transition Frequency	$I_{C} = 50 \text{ mA} \text{ V}_{CE} = 10 \text{ V} \text{ f} = 20 \text{ MHz}$	70	100		MHz
CEBO	Emitter-Base Capacitance	$I_{C} = 0$ $V_{EB} = 0.5 V f = 1 MHz$		50	80	pF
Ссво	Collector-Base Capacitance	$I_E = 0 \qquad V_{CB} = 10 V f = 1 \text{ MHz}$		18	25	pF
NF	Noise Figure	I _C = 0.3 mA V _{CE} = 10 V R_g = 510 Ω f = 1 KHz		3.5	8	dB
h _{ie}	Input Impedance	$I_C = 1 \text{ mA}$ $V_{CE} = 5 \text{ V}$ $f = 1 \text{ KHz}$		4.4		KΩ
h _{re}	Reverse Voltage Ratio	I _C = 1 mA V _{CE} = 5 V f = 1 KHz		7.3 x 10 ⁻⁴		
h _{oe}	Output Admittance	$I_C = 1 \text{ mA}$ $V_{CE} = 5 \text{ V}$ $f = 1 \text{ KHz}$		23.8		μS

57

* Pulsed: Pulse duration = 300 $\mu s,$ duty cycle \leq 1 %

5.04	mm		inch			
DIM.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	12.7			0.500		
В			0.49			0.019
D			6.6			0.260
E			8.5			0.334
F			9.4			0.370
G	5.08			0.200		
Н			1.2			0.047
I			0.9			0.035



TO-39 MECHANICAL DATA

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specification mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics. The ST logo is a trademark of STMicroelectronics

© 2002 STMicroelectronics - Printed in Italy - All Rights Reserved

STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - Canada - China - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco -Singapore - Spain - Sweden - Switzerland - United Kingdom - United States.

http://www.st.com

4/4

57