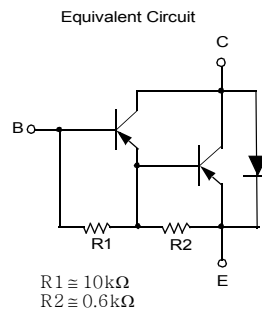
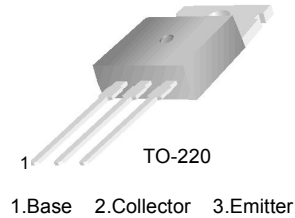


TIP105/TIP106/TIP107 PNP Epitaxial Silicon Darlington Transistor

- Monolithic Construction With Built In Base-Emitter Shunt Resistors
- High DC Current Gain : $h_{FE}=1000$ @ $V_{CE}=-4V$, $I_C=-3A$ (Min.)
- Collector-Emitter Sustaining Voltage
- Low Collector-Emitter Saturation Voltage
- Industrial Use
- Complementary to TIP100/101/102



Absolute Maximum Ratings* $T_a = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{CBO}	Collector-Base Voltage	: TIP105	- 60
		: TIP106	- 80
		: TIP107	- 100
V_{CEO}	Collector-Emitter Voltage	: TIP105	- 60
		: TIP106	- 80
		: TIP107	- 100
V_{EBO}	Emitter-Base Voltage	- 5	V
I_C	Collector Current (DC)	- 8	A
I_{CP}	Collector Current (Pulse)	- 15	A
I_B	Base Current (DC)	- 1	A
P_C	Collector Dissipation ($T_a=25^\circ\text{C}$)	2	W
	Collector Dissipation ($T_C=25^\circ\text{C}$)	80	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature	- 65 ~ 150	$^\circ\text{C}$

* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Electrical Characteristics* $T_a=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$V_{CEO(sus)}$	Collector-Emitter Sustaining Voltage : TIP105 : TIP106 : TIP107	$I_C = -30\text{mA}, I_B = 0$	-60 -80 -100			V V V
I_{CEO}	Collector Cut-off Current : TIP105 : TIP106 : TIP107	$V_{CE} = -30\text{V}, I_B = 0$ $V_{CE} = -40\text{V}, I_B = 0$ $V_{CE} = -50\text{V}, I_B = 0$			-50 -50 -50	μA μA μA
I_{CBO}	Collector Cut-off Current : TIP105 : TIP106 : TIP107	$V_{CB} = -60\text{V}, I_E = 0$ $V_{CB} = -80\text{V}, I_E = 0$ $V_{CB} = -100\text{V}, I_E = 0$			-50 -50 -50	μA μA μA
I_{EBO}	Emitter Cut-off Current	$V_{BE} = -5\text{V}, I_C = 0$			-2	mA
h_{FE}	DC Current Gain	$V_{CE} = -4\text{V}, I_C = -3\text{A}$ $V_{CE} = -4\text{V}, I_C = -8\text{A}$	1000 200		20000	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -3\text{A}, I_B = -6\text{mA}$ $I_C = -8\text{A}, I_B = -80\text{mA}$			-2 -2.5	V V
$V_{BE(on)}$	Base-Emitter On Voltage	$V_{CE} = -4\text{V}, I_C = -8\text{A}$			-2.8	V
C_{ob}	Output Capacitance	$V_{CB} = -10\text{V}, I_E = 0, f = 0.1\text{MHz}$			300	pF

* Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

Typical Characteristics

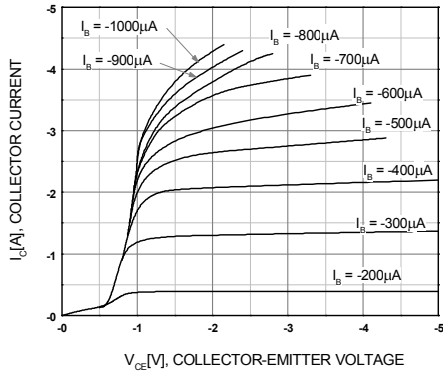


Figure 1. Static Characteristic

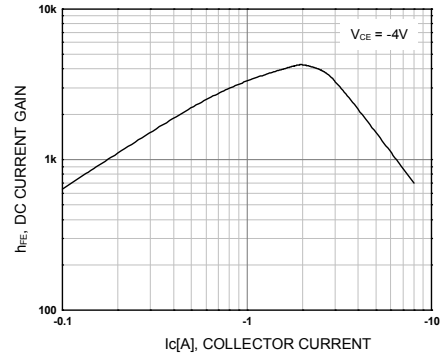


Figure 2. DC current Gain

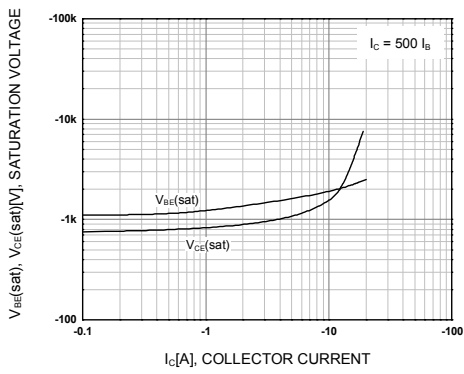


Figure 3. Collector-Emitter Saturation Voltage Base-Emitter Saturation Voltage

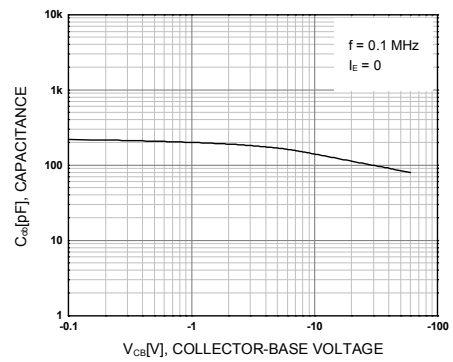


Figure 4. Collector Output Capacitance

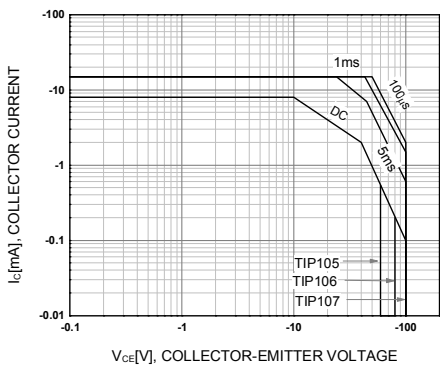


Figure 5. Safe Operating Area

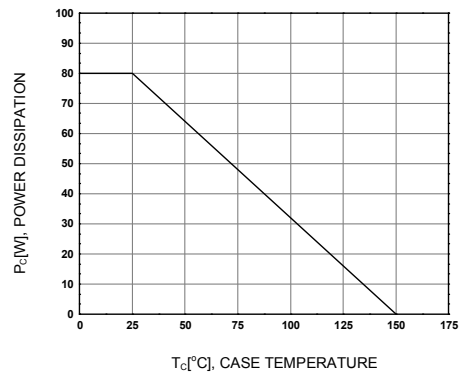
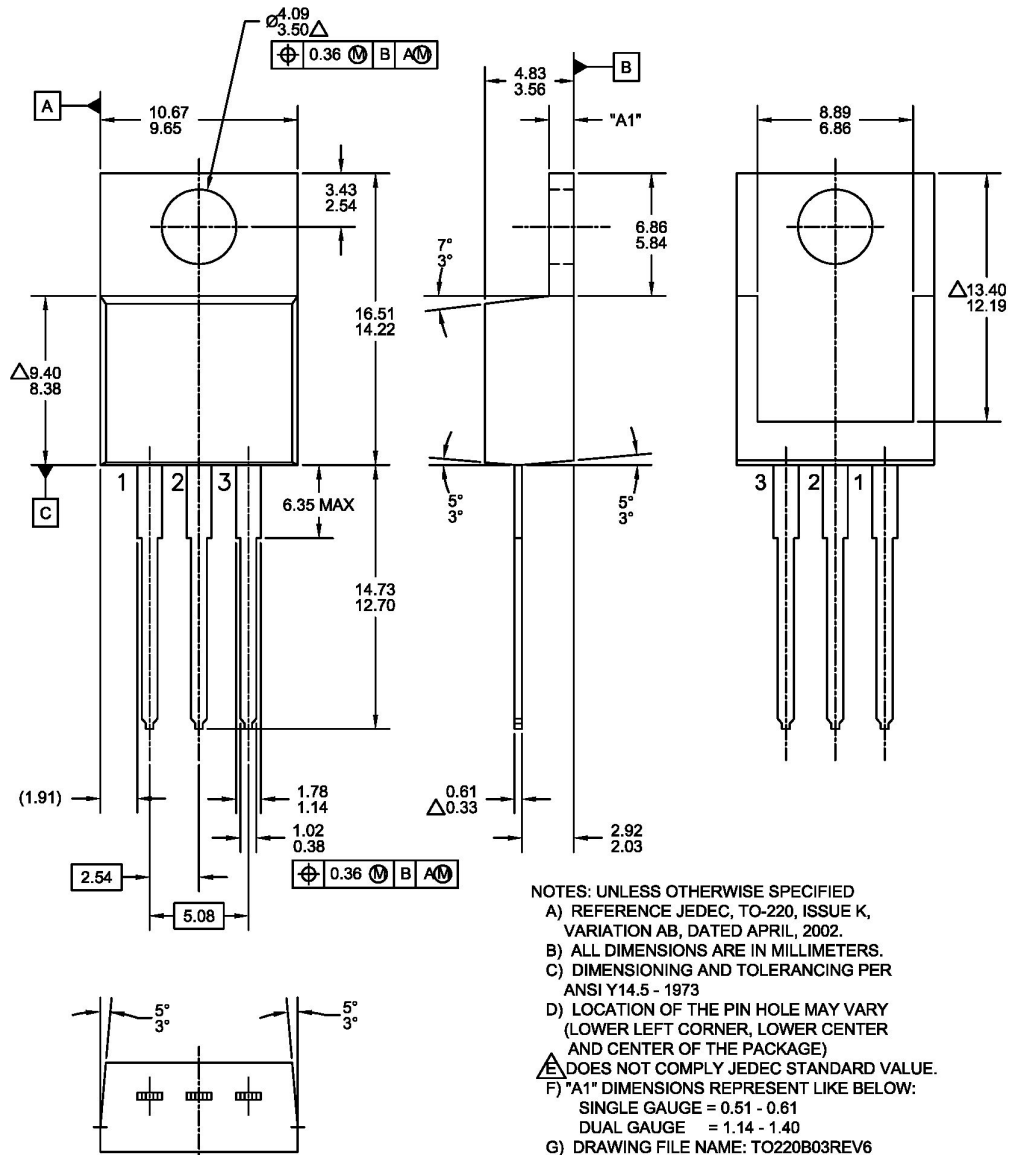


Figure 6. Power Derating

Mechanical Dimensions

TO220



- NOTES: UNLESS OTHERWISE SPECIFIED
 A) REFERENCE JEDEC, TO-220, ISSUE K, VARIATION AB, DATED APRIL, 2002.
 B) ALL DIMENSIONS ARE IN MILLIMETERS.
 C) DIMENSIONING AND TOLERANCING PER ANSI Y14.5 - 1973
 D) LOCATION OF THE PIN HOLE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE)
 E) DOES NOT COMPLY JEDEC STANDARD VALUE.
 F) "A1" DIMENSIONS REPRESENT LIKE BELOW:
 SINGLE GAUGE = 0.51 - 0.61
 DUAL GAUGE = 1.14 - 1.40
 G) DRAWING FILE NAME: TO220B03REV6

TIP105/TIP106/TIP107 — PNP Epitaxial Silicon Darlington Transistor



TRADEMARKS

The following are registered and unregistered trademarks and service marks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE [®]	Green FPS [™]	Power247 [®]	SuperSOT [™] -8
Build it Now [™]	Green FPS [™] e-Series [™]	POWEREDGE [®]	SyncFET [™]
CorePLUS [™]	GTO [™]	Power-SPM [™]	The Power Franchise [®]
CROSSVOLT [™]	i-Lo [™]	PowerTrench [®]	power the franchise
CTL [™]	IntelliMAX [™]	Programmable Active Droop [™]	TinyBoost [™]
Current Transfer Logic [™]	ISOPANAR [™]	QFET [®]	TinyBuck [™]
EcoSPARK [®]	MegaBuck [™]	QS [™]	TinyLogic [®]
F [®]	MICROCOUPLER [™]	QT Optoelectronics [™]	TINYOPTO [™]
Fairchild [®]	MicroFET [™]	Quiet Series [™]	TinyPower [™]
Fairchild Semiconductor [®]	MicroPak [™]	RapidConfigure [™]	TinyPWM [™]
FACT Quiet Series [™]	MillerDrive [™]	SMART START [™]	TinyWire [™]
FACT [®]	Motion-SPM [™]	SPM [®]	μSerDes [™]
FAST [®]	OPTOLOGIC [®]	STEALTH [™]	UHC [®]
FastvCore [™]	OPTOPLANAR [®]	SuperFET [™]	UniFET [™]
FPS [™]	 [®]	SuperSOT [™] -3	VCX [™]
FRFET [®]	PDP-SPM [™]	SuperSOT [™] -6	
Global Power Resource SM	Power220 [®]		

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. I31