

December 2009

TIP42/TIP42A/TIP42B/TIP42C PNP Epitaxial Silicon Transistor

Features

- Medium Power Linear Switching Applications
- Complement to TIP41/TIP41A/TIP41B/TIP41C



1.Base 2.Collector 3.Emitter

Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage : TIP42 : TIP42A : TIP42B : TIP42C	- 40 - 60 - 80 - 100	V V V
V _{CEO}	Collector-Emitter Voltage : TIP42 : TIP42A : TIP42B : TIP42C	- 40 - 60 - 80 - 100	V V V
V _{EBO}	Emitter-Base Voltage	- 5	V
I _C	Collector Current (DC)	- 6	А
I _{CP}	Collector Current (Pulse)	-10	А
I _B	Base Current	-2	А
P _C	Collector Dissipation (T _C =25°C)	65	W
	Collector Dissipation (T _A =25°C)	2	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 to 150	°C

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$\textbf{Electrical Characteristics} \ \, \textbf{T}_{A} \!\!=\!\! 25^{\circ} \text{C unless} \, \, \underline{\textbf{otherwise noted}}$

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	* Collector-Emitter Sustaining Voltage : TIP42 : TIP42A : TIP42B : TIP42C	I _C = -30mA, I _B = 0	-40 -60 -80 -100		V V V
I _{CEO}	Collector Cut-off Current : TIP42/42A : TIP42B/42C	$V_{CE} = -30V, I_{B} = 0$ $V_{CE} = -60V, I_{B} = 0$		-0.7 -0.7	mA mA
I _{CES}	Collector Cut-off Current : TIP42 : TIP42A : TIP42B : TIP42C	$V_{CE} = -40V, V_{EB} = 0$ $V_{CE} = -60V, V_{EB} = 0$ $V_{CE} = -80V, V_{EB} = 0$ $V_{CE} = -100V, V_{EB} = 0$		-400 -400 -400 -400	μΑ μΑ μΑ μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$		-1	mA
h _{FE}	* DC Current Gain	$V_{CE} = -4V, I_{C} = -0.3A$ $V_{CE} = -4V, I_{C} = -3A$	30 15	75	
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	$I_C = -6A, I_B = -600mA$		-1.5	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	$V_{CE} = -4V, I_{C} = -6A$		-2.0	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -500mA,$ f = 1MHz	3.0		MHz

^{*} Pulse Test: PW≤300μs, Duty Cycle≤2%

Typical Performance Characteristics

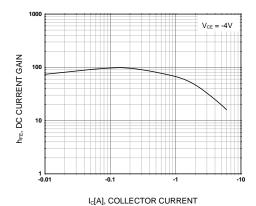


Figure 1. DC current Gain

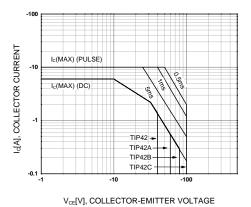


Figure 3. Safe Operating Area

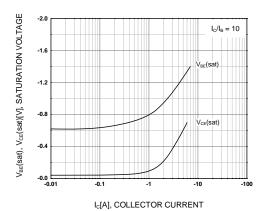


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

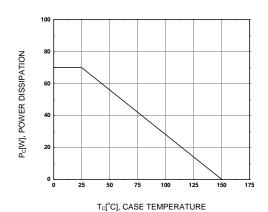


Figure 4. Power derating

Mechanical Dimensions TO-220 0.36 M B AM 4.83 3.56 10.67 9.65 8.89 6.86 3.43 2.54 △13.40 12.19 6.35 MAX 0.61 △0.33 1.02 0.38 0.36 M B AM 2.54 NOTES: UNLESS OTHERWISE SPECIFIED A) REFERENCE JEDEC, TO-220, ISSUE K, VARIATION AB, DATED APRIL, 2002. B) ALL DIMENSIONS ARE IN MILLIMETERS. 5.08 C) DIMENSIONING AND TOLERANCING PER ANSI Y14.5 - 1973 D) LOCATION OF THE PIN HOLE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE) AND CENTER OF THE PACKAGE) DOES NOT COMPLY JEDEC STANDARD VALUE. F) "A1" DIMENSIONS REPRESENT LIKE BELOW: SINGLE GAUGE = 0.51 - 0.61 DUAL GAUGE = 1.14 - 1.40 G) DRAWING FILE NAME: TO220B03REV6 Dimensions in Millimeters





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