

# ZXTP19060CZ 60V PNP medium transistor in SOT89

## Summary

 $BV_{CEO} > -60V$ 

 $BV_{ECO} > -7V$ 

 $I_{C(cont)} = 4.5A$ 

 $V_{CE(sat)} < -80 \text{mV} @ -1 \text{A}$ 

 $R_{CE(sat)} = 50 m\Omega$ 

 $P_D = 2.4W$ 

Complementary part number ZXTN19060CZ

## Description

Packaged in the SOT89 outline this new low saturation PNP transistor offers extremely low on state losses making it ideal for use in DC-DC circuits and various driving and power management functions.

### **Features**

- · High gain
- · Low saturation voltage
- High peak current
- 7V reverse blocking voltage

## **Applications**

- · High side driver
- Motor drive
- · Load disconnect switch

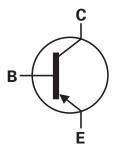
### **Ordering information**

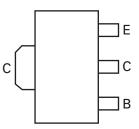
Device	Reel size (inches)	Tape width (mm)	Quantity per reel	
ZXTP19060CZTA	7	12	1000	

## **Device marking**

1M2







Pinout - top view

## **Absolute maximum ratings**

Parameter	Symbol	Limit	Unit
Collector-Base voltage	V <sub>CBO</sub>	-60	V
Collector-Emitter voltage	V <sub>CEO</sub>	-60	V
Emitter-Collector voltage (reverse blocking)	V <sub>ECX</sub>	-7	V
Emitter-Base voltage	V <sub>EBO</sub>	-7	V
Continuous Collector current(c)	I <sub>C</sub>	-4.5	Α
Base current	I <sub>B</sub>	-1	А
Peak pulse current	I <sub>CM</sub>	-7	А
Power dissipation at T <sub>A</sub> =25°C <sup>(a)</sup>	P <sub>D</sub>	1.1	W
Linear derating factor		8.8	mW/°C
Power dissipation at T <sub>A</sub> =25°C <sup>(b)</sup>	P <sub>D</sub>	1.8	W
Linear derating factor		14.4	mW/°C
Power dissipation at T <sub>A</sub> =25°C <sup>(c)</sup>	P <sub>D</sub>	2.4	W
Linear derating factor		19.2	mW/°C
Power dissipation at T <sub>A</sub> =25°C <sup>(d)</sup>	P <sub>D</sub>	4.46	W
Linear derating factor		35.7	mW/°C
Power dissipation at T <sub>C</sub> =25°C <sup>(e)</sup>	P <sub>D</sub>	26.7	W
Linear derating factor		213	mW/°C
Operating and storage temperature range	T <sub>j</sub> , T <sub>stg</sub>	-55 to 150	°C

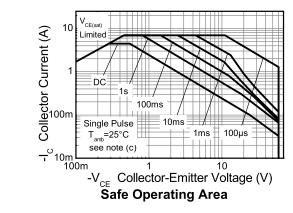
### Thermal resistance

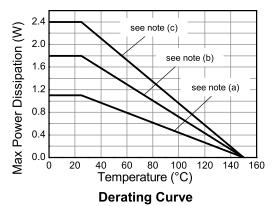
Parameter	Symbol	Limit	Unit
Junction to ambient <sup>(a)</sup>	$R_{\Theta JA}$	117	°C/W
Junction to ambient <sup>(b)</sup>	$R_{\Theta JA}$	68	°C/W
Junction to ambient <sup>(c)</sup>	$R_{\Theta JA}$	51	°C/W
Junction to ambient <sup>(d)</sup>	$R_{\Theta JA}$	28	°C/W
Junction to case <sup>(e)</sup>	$R_{\Theta JC}$	4.69	°C/W

### NOTES:

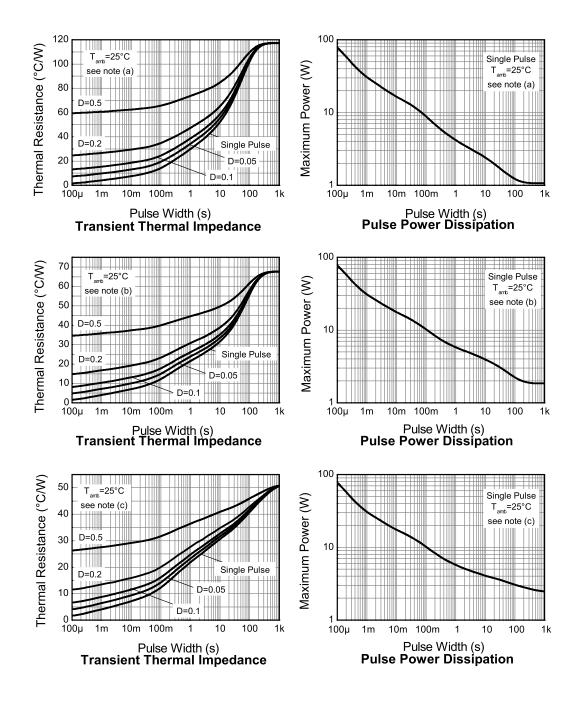
- (a) For a device surface mounted on 15mm x 15mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- (b) Mounted on 25mm x 25mm x 0.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- (c) Mounted on 50mm x 50mm x 0.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.
- (d) As (c) above measured at t<10 seconds.
- (e) Junction to case (collector tab). Typical

## **Thermal characteristics**





### Thermal characteristics



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# Electrical characteristics (at T<sub>amb</sub> = 25°C unless otherwise stated)

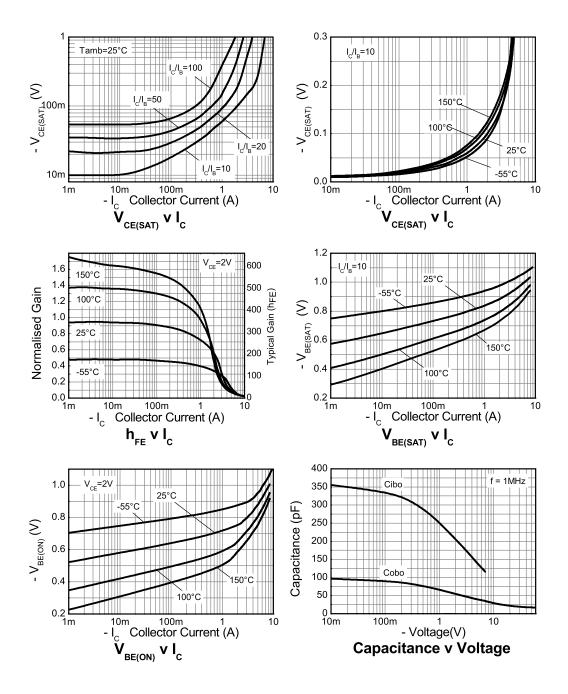
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-Base breakdown voltage	BV <sub>CBO</sub>	-60	-110		V	I <sub>C</sub> = -100μA
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	-60	-90		V	I <sub>C</sub> = -10mA (*)
Emitter-Collector breakdown voltage (reverse blocking)	BV <sub>ECX</sub>	-7	-8.4		V	$I_E$ = -100μA, $R_{BC}$ < 1k $\Omega$ or 0.25V > $V_{BC}$ > -0.25V
Emitter-Collector breakdown voltage (reverse blocking)	BV <sub>ECO</sub>	-7	-8.8		V	I <sub>E</sub> = -100μA
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	-7	-8.4		V	$I_E = -100\mu A$
Collector-Base cut-off	I <sub>CBO</sub>		<1	-50	nA	V <sub>CB</sub> = -60V
current				-0.5	μΑ	$V_{CB} = -60V, T_{amb} = 100^{\circ}C$
Emitter cut-off current	I <sub>EBO</sub>		<1	-50	nA	V <sub>EB</sub> = -5.6V
Collector-Emitter	V <sub>CE(sat)</sub>		-62	-80	mV	$I_C = -1A$ , $I_B = -100 \text{mA}^{(*)}$
saturation voltage			-150	-205	mV	$I_C = -1A$ , $I_B = -20mA^{(*)}$
			-500	-750	mV	$I_C = -2A$ , $I_B = -40mA^{(*)}$
			-105	-165	mV	$I_C = -2A$ , $I_B = -200 \text{mA}^{(*)}$
			-145	-200	mV	$I_C = -3A$ , $I_B = -300 \text{mA}^{(*)}$
			-240	-410	mV	$I_C = -4.5A$ , $I_B = -450 \text{mA}^{(*)}$
Base-Emitter saturation voltage	V <sub>BE(sat)</sub>		-965	-1050	mV	$I_C = -4.5A$ , $I_B = -450 \text{mA}^{(*)}$
Base-Emitter turn-on voltage	V <sub>BE(on)</sub>		-875	-1000	mV	$I_C = -4.5A$ , $V_{CE} = -2V^{(*)}$
Static forward current	h <sub>FE</sub>	200	330	500		$I_C = -100 \text{mA}, V_{CE} = -2V^{(*)}$
transfer ratio		160	260			$I_C = -1A$ , $V_{CE} = -2V^{(*)}$
		25	45			$I_C = -4.5A$ , $V_{CE} = -2V^{(*)}$
Transition frequency	f <sub>T</sub>		180		MHz	$I_C = -50 \text{mA}, V_{CE} = -10 \text{V}$ f = 50MHz
Input capacitance	C <sub>ibo</sub>		280	400	pF	V <sub>EB</sub> = -0.5V, f = 1MHz <sup>(*)</sup>
Output capacitance	C <sub>obo</sub>		29.5	40	pF	V <sub>CB</sub> = -10V, f = 1MHz <sup>(*)</sup>
Delay time	t <sub>d</sub>		24.3		ns	
Rise time	t <sub>r</sub>		13.2		ns	$I_C = -500 \text{mA}, V_{CC} = -10 \text{V},$
Storage time	t <sub>s</sub>		456		ns	$I_{B1} = -I_{B2} = -50 \text{mA}$
Fall time	t <sub>f</sub>		68.2		ns	

### NOTES

(\*) Measured under pulsed conditions. Pulse width  $\leq$  300µs; duty cycle  $\leq$  2%.

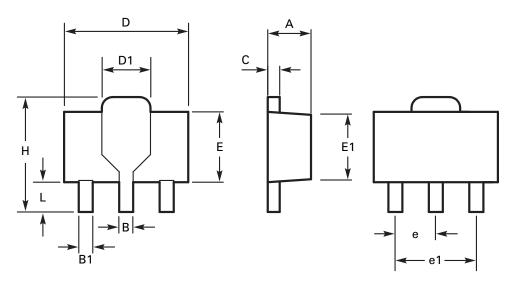


## **Typical characteristics**



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## Package outline - SOT89



DIM	Millin	neters	Inc	hes	DIM	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
Α	1.40	1.60	0.550	0.630	Е	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50 BSC		0.059 BSC	
С	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118	BSC
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.52	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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### Zetex sales offices

Europe	Americas	Asia Pacific	Corporate Headquarters
Zetex GmbH Kustermann-park Balanstraße 59 D-81541 München Germanv	Zetex Inc 700 Veterans Memorial Highway Hauppauge, NY 11788 USA	Zetex (Asia Ltd) 3701-04 Metroplaza Tower 1 Hing Fong Road, Kwai Fong Hong Kong	Zetex Semiconductors plc Zetex Technology Park, Chadderton Oldham, OL9 9LL United Kingdom
Telefon: (49) 89 45 49 49 0 Fax: (49) 89 45 49 49 49 europe.sales@zetex.com	Telephone: (1) 631 360 2222 Fax: (1) 631 360 8222 usa.sales@zetex.com	Telephone: (852) 26100 611 Fax: (852) 24250 494 asia.sales@zetex.com	Telephone: (44) 161 622 4444 Fax: (44) 161 622 4446 hq@zetex.com

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