

# **PNP High Voltage Amplifier**

This device is designed for high voltage driver applications. Sourced from Process 76.

#### **Absolute Maximum Ratings\*** TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	300	V
V <sub>CBO</sub>	Collector-Base Voltage	300	V
V <sub>EBO</sub>	Emitter-Base Voltage	5.0	V
lc	Collector Current - Continuous	500	mA
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

#### **Thermal Characteristics** TA = 25°C unless otherwise noted

Symbol	Characteristic	Мах			Units
		MPSA92	*MMBTA92	**PZTA92	
P <sub>D</sub>	Total Device Dissipation	625	350	1,000	mW
	Derate above 25°C	5.0	2.8	8.0	mW/∘C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3			°C/W
$R_{ ext{ hetaJA}}$	Thermal Resistance, Junction to Ambient	200	357	125	°C/W

\*Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

\*\* Device mounted on FR-4 PCB 36 mm X 18 mm X 1.5 mm; mounting pad for the collector lead min. 6 cm<sup>2</sup>.

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## **PNP High Voltage Amplifier** (

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Electrical Characteristics	TA = 25°C unless otherwise noted
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Symbol Parameter	Test Conditions	Min	Мах	Units
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## **OFF CHARACTERISTICS**

V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage*	$I_{\rm C} = 1.0$ mA, $I_{\rm B} = 0$	300		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_{\rm C} = 100 \ \mu {\rm A}, \ I_{\rm E} = 0$	300		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_{E} = 100 \ \mu A, \ I_{C} = 0$	5.0		V
I <sub>CBO</sub>	Collector-Cutoff Current	$V_{CB} = 200 \text{ V}, I_E = 0$		0.25	μΑ
I <sub>EBO</sub>	Emitter-Cutoff Current	$V_{EB} = 3.0 \text{ V}, \text{ I}_{C} = 0$		0.1	μΑ

## **ON CHARACTERISTICS\***

h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 1.0 mA, V <sub>CE</sub> = 10 V	25		
		$I_{C} = 10 \text{ mA}, V_{CE} = 10 \text{ V}$	40		
		$I_{C} = 30 \text{ mA}, V_{CE} = 10 \text{ V}$	25		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_{\rm C} = 20$ mA, $I_{\rm B} = 2.0$ mA		0.5	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	$I_{\rm C} = 20$ mA, $I_{\rm B} = 2.0$ mA		0.9	V

## SMALL SIGNAL CHARACTERISTICS

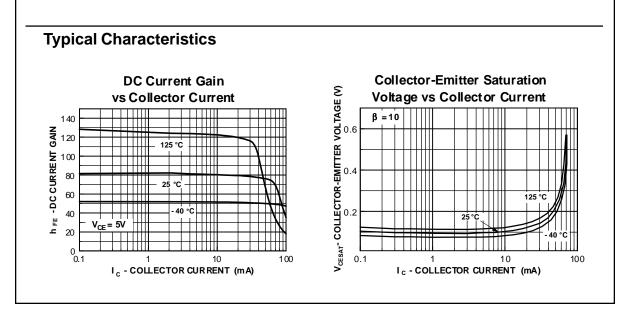
f <sub>T</sub>	Current Gain - Bandwidth Product	$I_{C} = 10 \text{ mA}, V_{CE} = 20 \text{ V},$ f = 100 MHz	50		MHz
C <sub>cb</sub>	Collector-Base Capacitance	$V_{CB} = 20 \text{ V}, I_E = 0, f = 1.0 \text{ MHz}$		6.0	pF

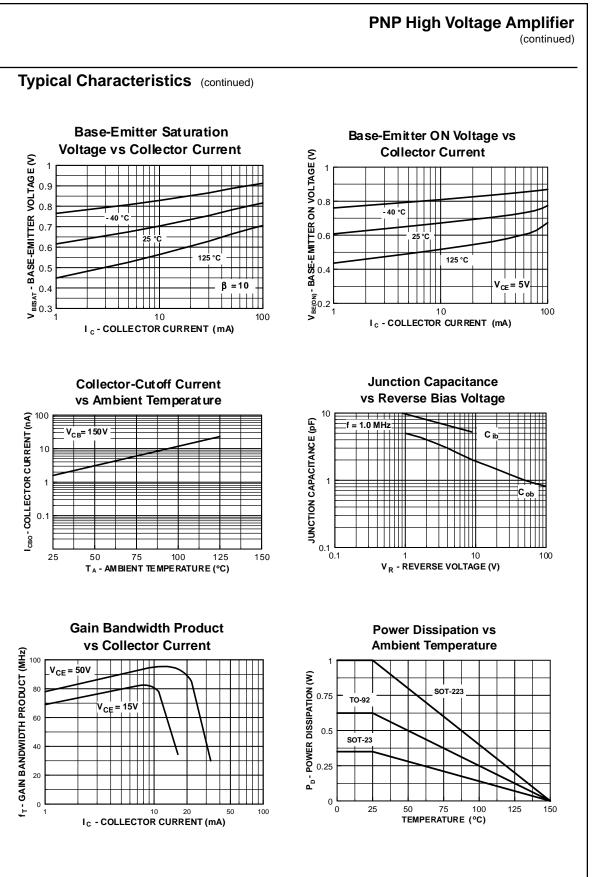
\*Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%

NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

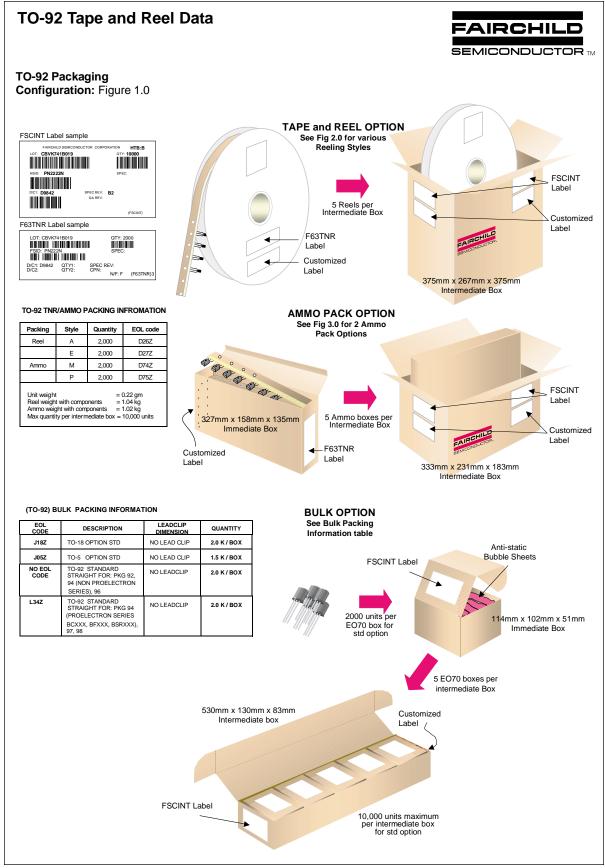
## **Spice Model**

PNP (Is=218.9f Xti=3 Eg=1.11 Vaf=100 Bf=99 Ne=1.307 Is=218.9f Ikf=.2016 Xtb=1.5 Br=24.67 Nc=2 Isc=0 Ikr=0 Rc=7 Cjc=19.88p Mjc=.4876 Vjc=.75 Fc=.5 Cje=81.49p Mje=.3493 Vje=.75 Tr=516.9p Tf=1.395n Itf=1.5 Vtf=22 Xtf=270 Rb=10)



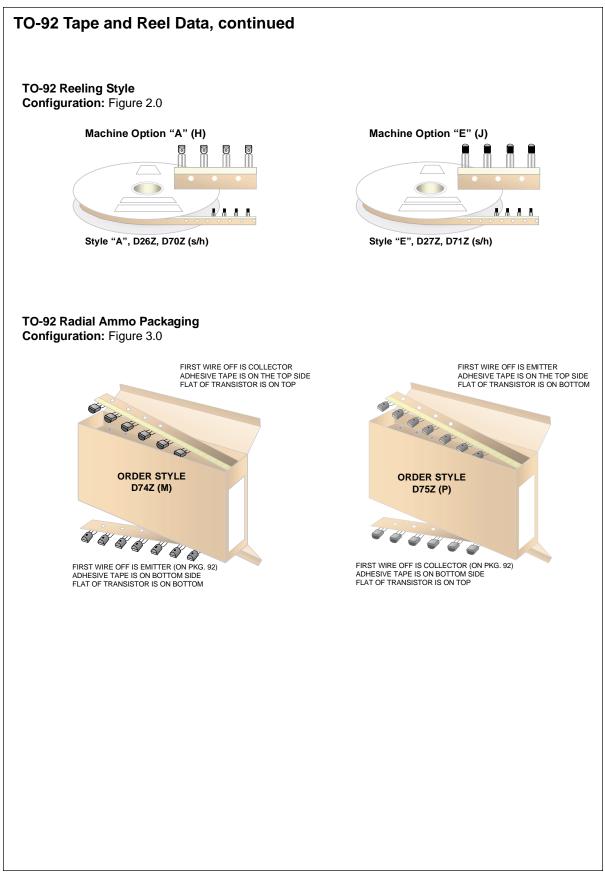


MPSA92 / MMBTA92 / PZTA92

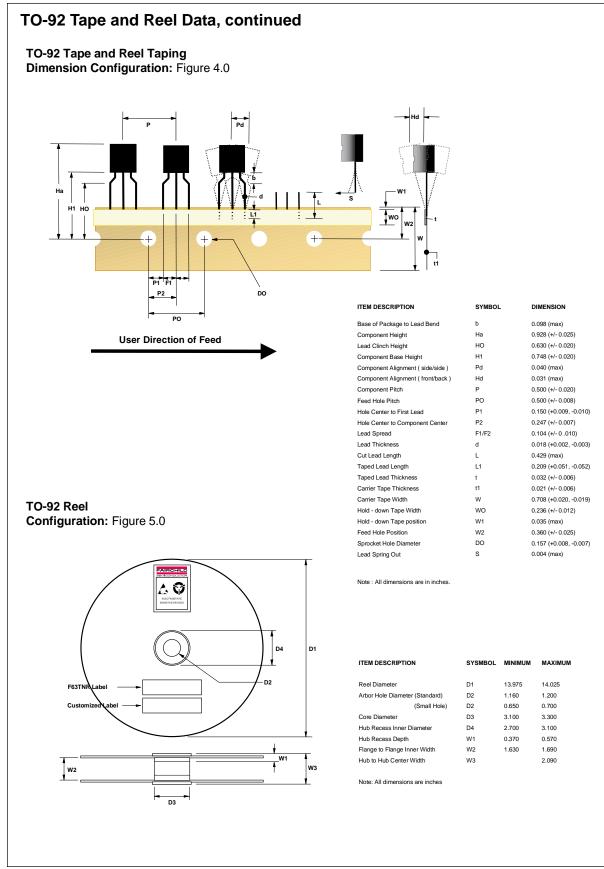


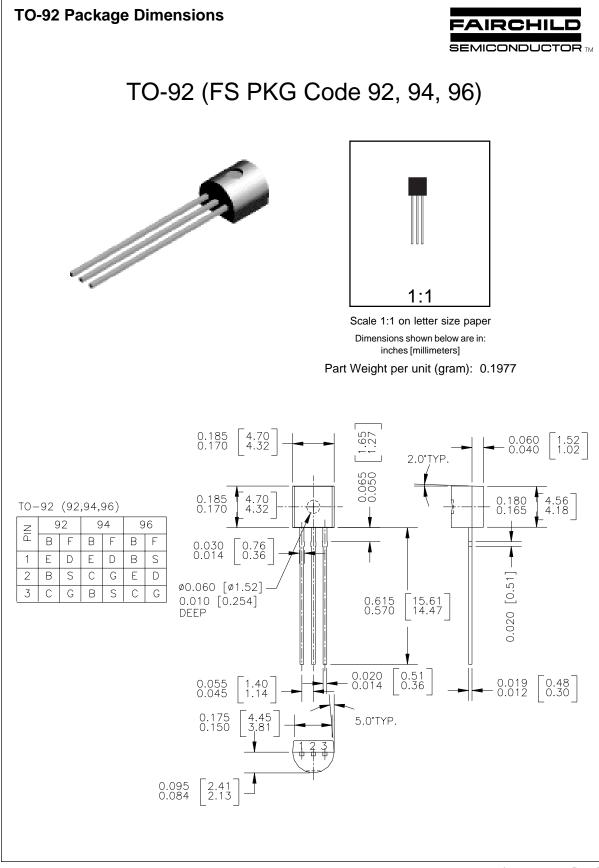
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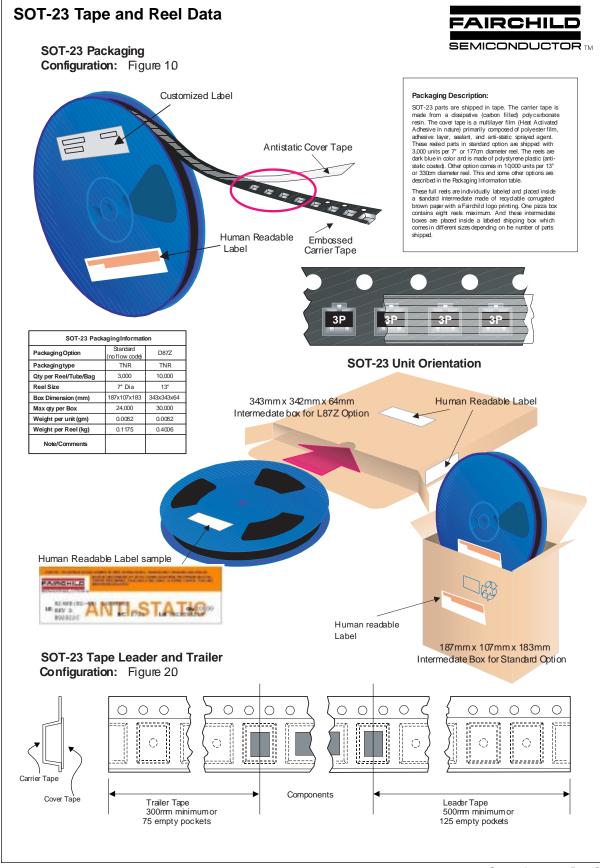
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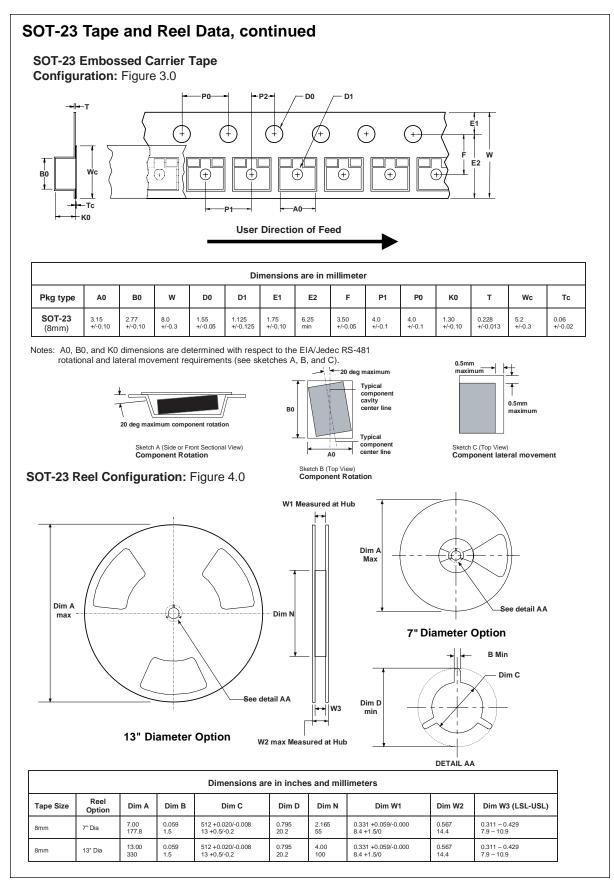
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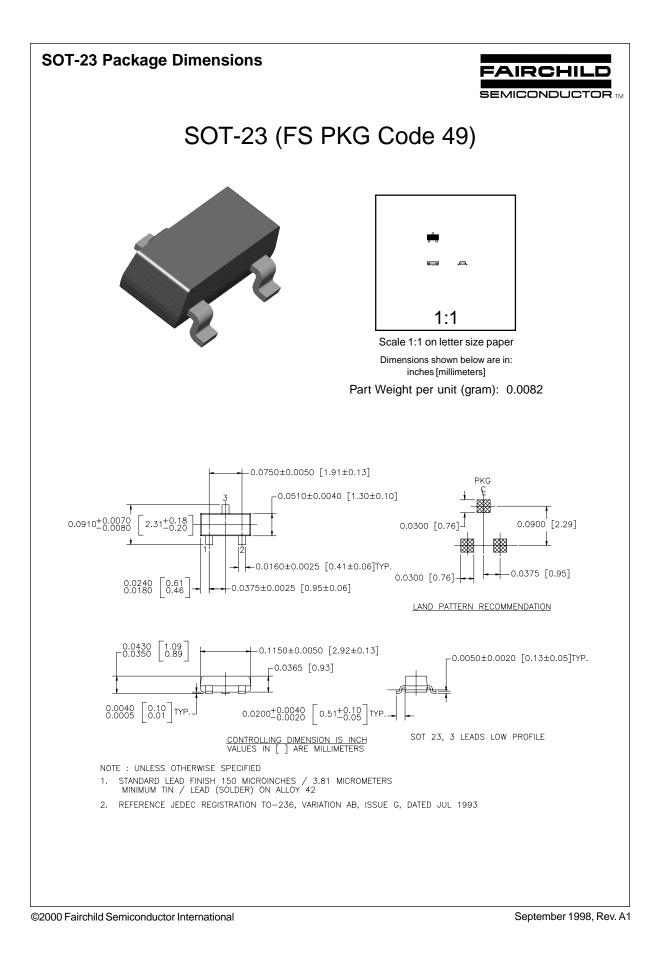


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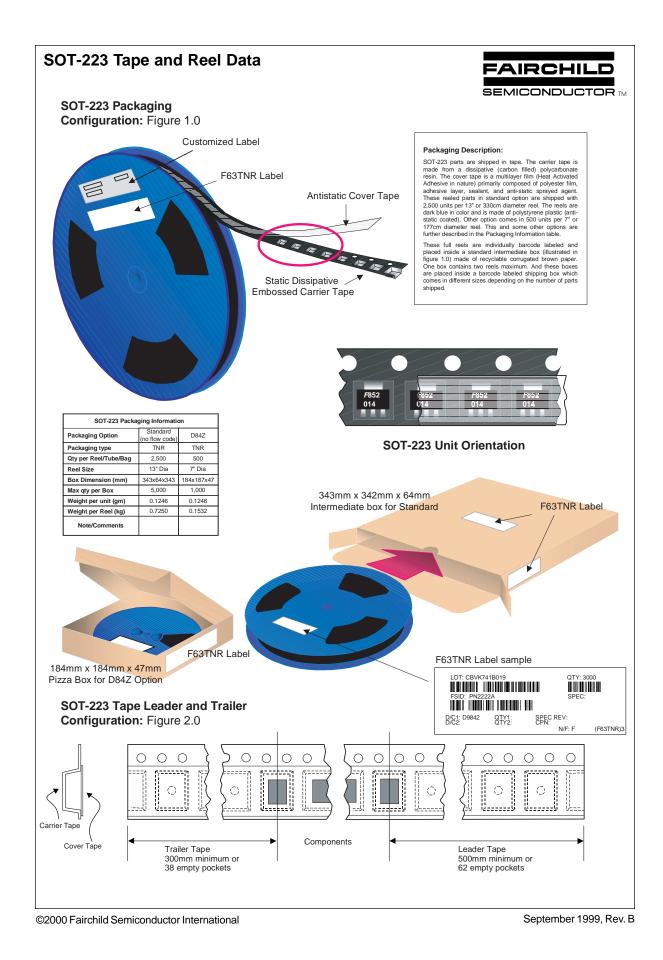
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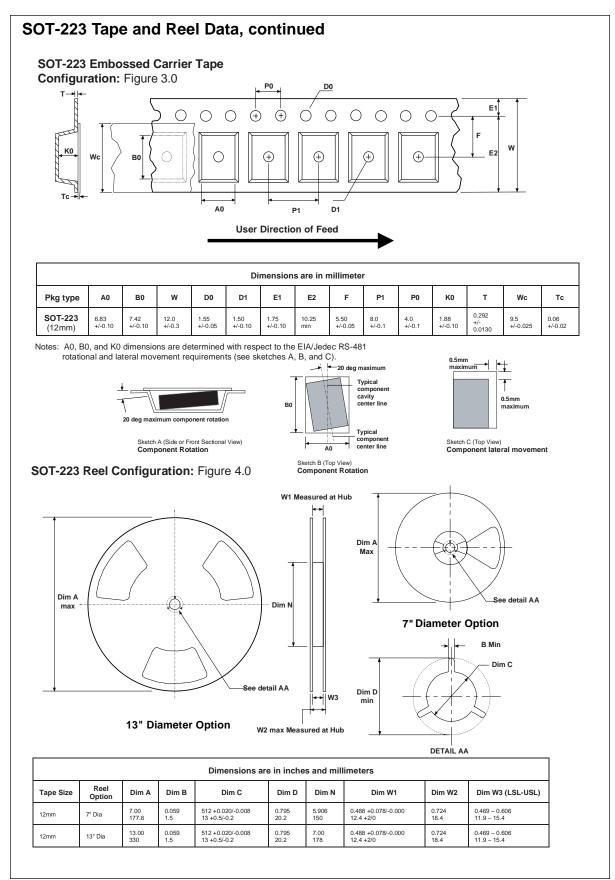
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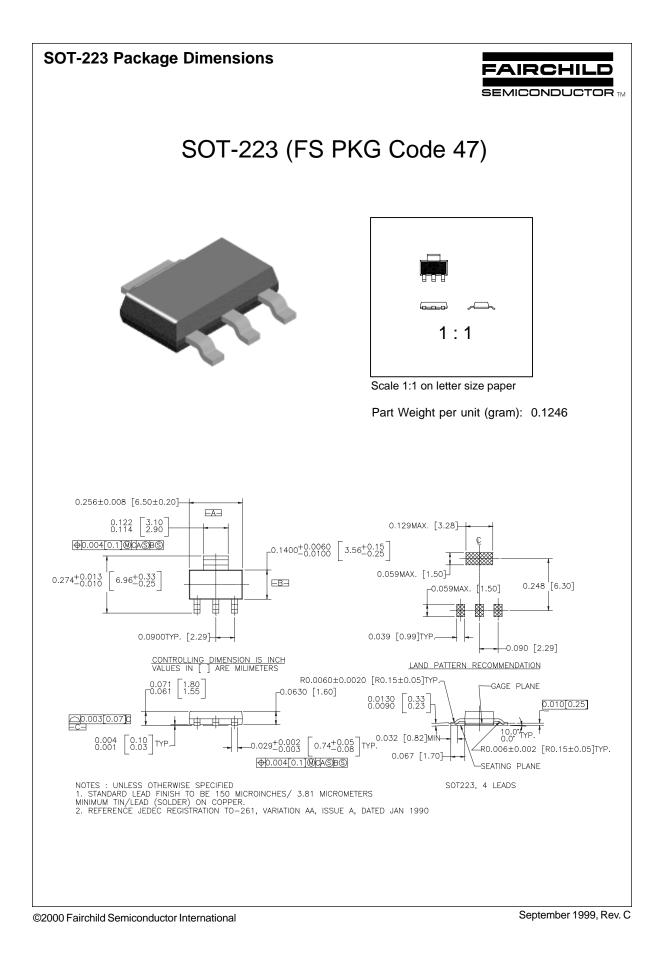
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