MUN5211T1G Series

Bias Resistor Transistor

NPN Silicon Surface Mount Transistor with Monolithic Bias Resistor Network

This new series of digital transistors is designed to replace a single device and its external resistor bias network. The BRT (Bias Resistor Transistor) contains a single transistor with a monolithic bias network consisting of two resistors; a series base resistor and a base-emitter resistor. The BRT eliminates these individual components by integrating them into a single device. The use of a BRT can reduce both system cost and board space. The device is housed in the SC-70/SOT-323 package which is designed for low power surface mount applications.

Features

- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- The SC-70/SOT-323 package can be soldered using wave or reflow. The modified gull-winged leads absorb thermal stress during soldering eliminating the possibility of damage to the die.
- Available in 8 mm embossed tape and reel. Use the Device Number to order the 7 inch/3000 unit reel.
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	50	Vdc
Collector-Emitter Voltage	V_{CEO}	50	Vdc
Collector Current	I_C	100	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation T_A = 25°C Derate above 25°C	P_D	202 (Note 1) 310 (Note 2) 1.6 (Note 1) 2.5 (Note 2)	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	618 (Note 1) 403 (Note 2)	°C/W
Thermal Resistance, Junction-to-Lead	$R_{\theta JL}$	280 (Note 1) 332 (Note 2)	°C/W
Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-4 @ Minimum Pad.
2. FR-4 @ 1.0 x 1.0 inch Pad.

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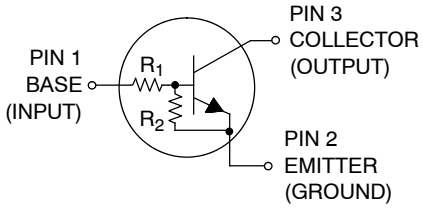
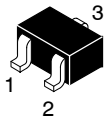
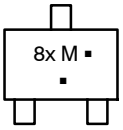
NPN SILICON BIAS RESISTOR TRANSISTORS





SC-70/SOT-323
CASE 419
STYLE 3

MARKING DIAGRAM



8x = Device Code
M = Date Code*
▪ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

DEVICE MARKING INFORMATION

See specific marking information in the device marking table on page 2 of this data sheet.

DEVICE MARKING AND RESISTOR VALUES

Device	Package	Marking	R1 (K)	R2 (K)	Shipping†
MUN5211T1G	SC−70/SOT−323 (Pb−Free)	8A	10	10	3000 / Tape & Reel
MUN5212T1G	SC−70/SOT−323 (Pb−Free)	8B	22	22	3000 / Tape & Reel
MUN5213T1G	SC−70/SOT−323 (Pb−Free)	8C	47	47	3000 / Tape & Reel
MUN5214T1G	SC−70/SOT−323 (Pb−Free)	8D	10	47	3000 / Tape & Reel
MUN5215T1G	SC−70/SOT−323 (Pb−Free)	8E	10	∞	3000 / Tape & Reel
MUN5216T1G (Note 3)	SC−70/SOT−323 (Pb−Free)	8F	4.7	∞	3000 / Tape & Reel
MUN5230T1G	SC−70/SOT−323 (Pb−Free)	8G	1.0	1.0	3000 / Tape & Reel
MUN5231T1G (Note 3)	SC−70/SOT−323 (Pb−Free)	8H	2.2	2.2	3000 / Tape & Reel
MUN5232T1G	SC−70/SOT−323 (Pb−Free)	8J	4.7	4.7	3000 / Tape & Reel
MUN5233T1G	SC−70/SOT−323 (Pb−Free)	8K	4.7	47	3000 / Tape & Reel
MUN5234T1G (Note 3)	SC−70/SOT−323 (Pb−Free)	8L	22	47	3000 / Tape & Reel
MUN5235T1G	SC−70/SOT−323 (Pb−Free)	8M	2.2	47	3000 / Tape & Reel
MUN5236T1G (Note 3)	SC−70/SOT−323 (Pb−Free)	8N	100	100	3000 / Tape & Reel
MUN5237T1G (Note 3)	SC−70/SOT−323 (Pb−Free)	8P	47	22	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

3. New devices. Updated curves to follow in subsequent data sheets.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Collector−Base Cutoff Current (V_{CB} = 50 V, I_E = 0)		I_{CBO}	−	−	100	nAdc
Collector−Emitter Cutoff Current (V_{CE} = 50 V, I_B = 0)		I_{CEO}	−	−	500	nAdc
Emitter−Base Cutoff Current (V_{EB} = 6.0 V, I_C = 0)	MUN5211T1G	I_{EBO}	−	−	0.5	mAdc
	MUN5212T1G		−	−	0.2	
	MUN5213T1G		−	−	0.1	
	MUN5214T1G		−	−	0.2	
	MUN5215T1G		−	−	0.9	
	MUN5216T1G		−	−	1.9	
	MUN5230T1G		−	−	4.3	
	MUN5231T1G		−	−	2.3	
	MUN5232T1G		−	−	1.5	
	MUN5233T1G		−	−	0.18	
	MUN5234T1G		−	−	0.13	
	MUN5235T1G		−	−	0.2	
	MUN5236T1G		−	−	0.05	
	MUN5237T1G		−	−	0.13	
Collector−Base Breakdown Voltage (I_C = 10 μA, I_E = 0)		$V_{(BR)CBO}$	50	−	−	Vdc
Collector−Emitter Breakdown Voltage (Note 4) (I_C = 2.0 mA, I_B = 0)		$V_{(BR)CEO}$	50	−	−	Vdc
ON CHARACTERISTICS (Note 4)						
DC Current Gain (V_{CE} = 10 V, I_C = 5.0 mA)	MUN5211T1G	h_{FE}	35	60	−	
	MUN5212T1G		60	100	−	
	MUN5213T1G		80	140	−	
	MUN5214T1G		80	140	−	
	MUN5215T1G		160	350	−	
	MUN5216T1G		160	350	−	
	MUN5230T1G		3.0	5.0	−	
	MUN5231T1G		8.0	15	−	
	MUN5232T1G		15	30	−	
	MUN5233T1G		80	200	−	
	MUN5234T1G		80	150	−	
	MUN5235T1G		80	140	−	
	MUN5236T1G		80	150	−	
	MUN5237T1G		80	140	−	
Collector−Emitter Saturation Voltage		$V_{CE(sat)}$				Vdc
(I_C = 10 mA, I_B = 0.3 mA)	MUN5211T1G		−	−	0.25	
	MUN5212T1G		−	−	0.25	
	MUN5213T1G		−	−	0.25	
	MUN5214T1G		−	−	0.25	
	MUN5236T1G		−	−	0.25	
(I_C = 10 mA, I_B = 5 mA)	MUN5230T1G		−	−	0.25	
	MUN5231T1G		−	−	0.25	
	MUN5237T1G		−	−	0.25	
(I_C = 10 mA, I_B = 1 mA)	MUN5215T1G		−	−	0.25	
	MUN5216T1G		−	−	0.25	
	MUN5232T1G		−	−	0.25	
	MUN5233T1G		−	−	0.25	
	MUN5234T1G		−	−	0.25	
	MUN5235T1G		−	−	0.25	

4. Pulse Test: Pulse Width < 300 μs, Duty Cycle < 2.0%

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Typ	Max	Unit
ON CHARACTERISTICS (Note 5) (Continued)						
Output Voltage (on)		V_{OL}				Vdc
(V_{CC} = 5.0 V, V_B = 2.5 V, R_L = 1.0 kΩ)	MUN5211T1G		–	–	0.2	
	MUN5212T1G		–	–	0.2	
	MUN5214T1G		–	–	0.2	
	MUN5215T1G		–	–	0.2	
	MUN5216T1G		–	–	0.2	
	MUN5230T1G		–	–	0.2	
	MUN5231T1G		–	–	0.2	
	MUN5232T1G		–	–	0.2	
	MUN5233T1G		–	–	0.2	
	MUN5234T1G		–	–	0.2	
	MUN5235T1G		–	–	0.2	
(V_{CC} = 5.0 V, V_B = 3.5 V, R_L = 1.0 kΩ)	MUN5213T1G		–	–	0.2	
(V_{CC} = 5.0 V, V_B = 5.5 V, R_L = 1.0 kΩ)	MUN5236T1G		–	–	0.2	
(V_{CC} = 5.0 V, V_B = 4.0 V, R_L = 1.0 kΩ)	MUN5237T1G		–	–	0.2	
Output Voltage (off)		V_{OH}				Vdc
(V_{CC} = 5.0 V, V_B = 0.5 V, R_L = 1.0 kΩ)	MUN5211T1G		4.9	–	–	
	MUN5212T1G		4.9	–	–	
	MUN5213T1G		4.9	–	–	
	MUN5214T1G		4.9	–	–	
	MUN5234T1G		4.9	–	–	
	MUN5235T1G		4.9	–	–	
(V_{CC} = 5.0 V, V_B = 0.05 V, R_L = 1.0 kΩ)	MUN5230T1G		4.9	–	–	
(V_{CC} = 5.0 V, V_B = 0.25 V, R_L = 1.0 kΩ)	MUN5215T1G		4.9	–	–	
	MUN5216T1G		4.9	–	–	
	MUN5231T1G		4.9	–	–	
	MUN5232T1G		4.9	–	–	
	MUN5233T1G		4.9	–	–	
	MUN5236T1G		4.9	–	–	
	MUN5237T1G		4.9	–	–	
Input Resistor		R_1				kΩ
	MUN5211T1G		7.0	10	13	
	MUN5212T1G		15.4	22	28.6	
	MUN5213T1G		32.9	47	61.1	
	MUN5214T1G		7.0	10	13	
	MUN5215T1G		7.0	10	13	
	MUN5216T1G		3.3	4.7	6.1	
	MUN5230T1G		0.7	1.0	1.3	
	MUN5231T1G		1.5	2.2	2.9	
	MUN5232T1G		3.3	4.7	6.1	
	MUN5233T1G		3.3	4.7	6.1	
	MUN5234T1G		15.4	22	28.6	
	MUN5235T1G		1.54	2.2	2.86	
	MUN5236T1G		70	100	130	
	MUN5237T1G		32.9	47	61.1	
Resistor Ratio		R_1/R_2				
	MUN5211T1G		0.8	1.0	1.2	
	MUN5212T1G		0.8	1.0	1.2	
	MUN5213T1G		0.8	1.0	1.2	
	MUN5214T1G		0.17	0.21	0.25	
	MUN5215T1G		–	–	–	
	MUN5216T1G		–	–	–	
	MUN5230T1G		0.8	1.0	1.2	
	MUN5231T1G		0.8	1.0	1.2	
	MUN5232T1G		0.8	1.0	1.2	
	MUN5233T1G		0.055	0.1	0.185	
	MUN5234T1G		0.38	0.47	0.56	
	MUN5235T1G		0.038	0.047	0.056	
	MUN5236T1G		0.8	1.0	1.2	
	MUN5237T1G		1.7	2.1	2.6	

5. Pulse Test: Pulse Width < 300 μs, Duty Cycle < 2.0%

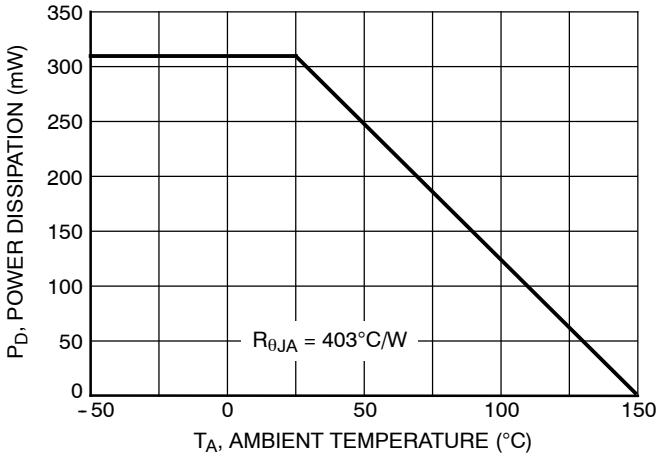


Figure 1. Derating Curve

TYPICAL ELECTRICAL CHARACTERISTICS – MUN5211T1G

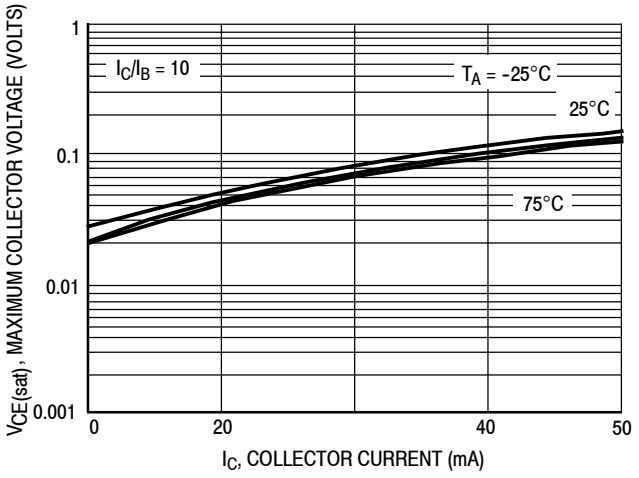


Figure 2. $V_{CE(sat)}$ versus I_C

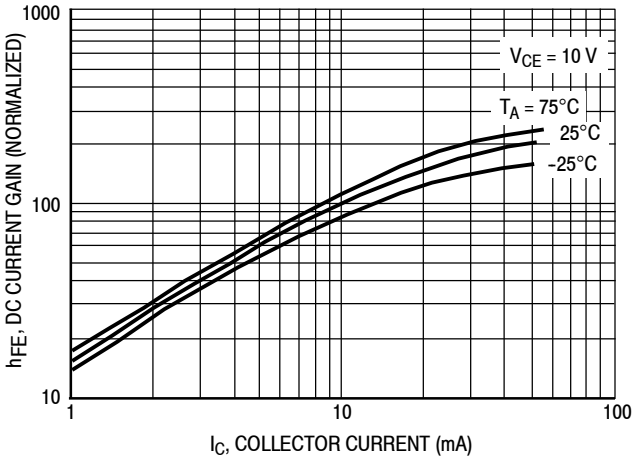


Figure 3. DC Current Gain

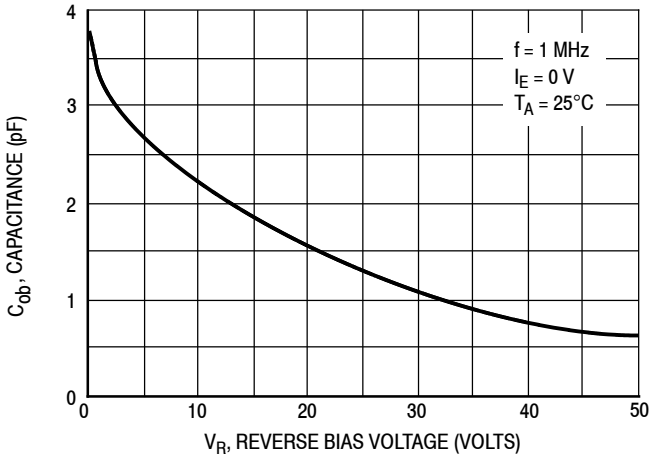


Figure 4. Output Capacitance

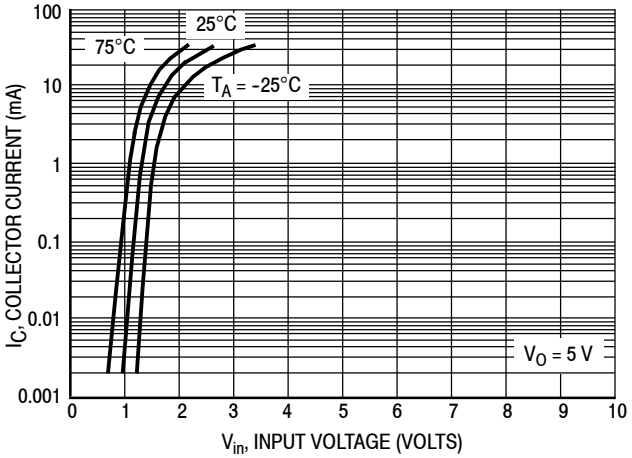


Figure 5. Output Current versus Input Voltage

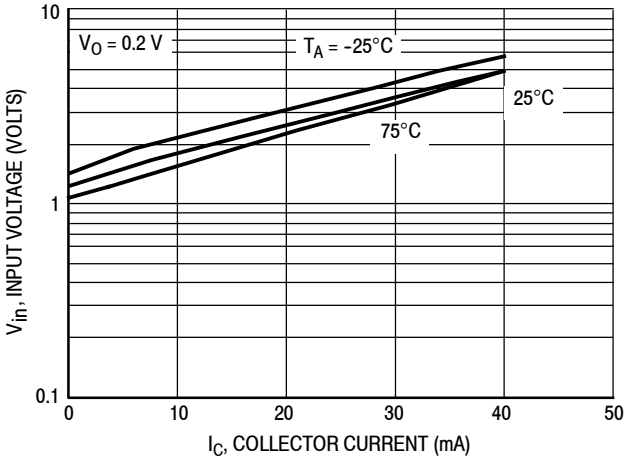


Figure 6. Input Voltage versus Output Current

TYPICAL ELECTRICAL CHARACTERISTICS – MUN5212T1G

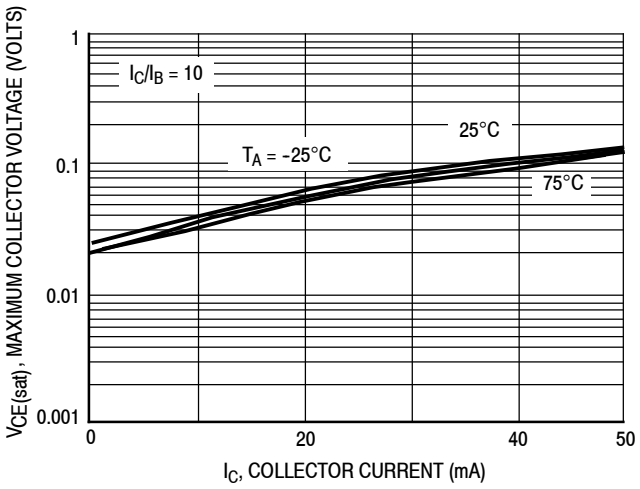


Figure 7. $V_{CE(sat)}$ versus I_C

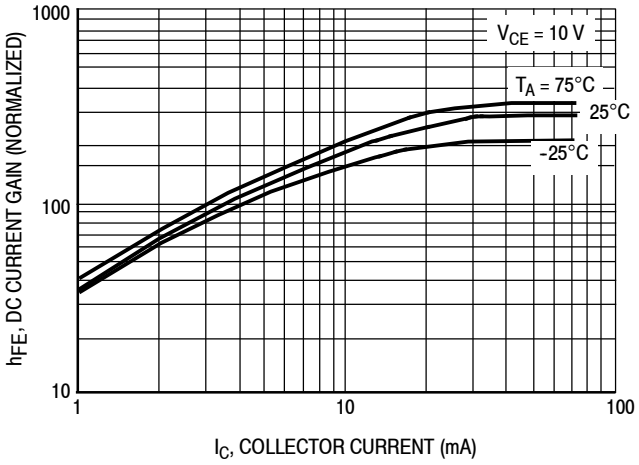


Figure 8. DC Current Gain

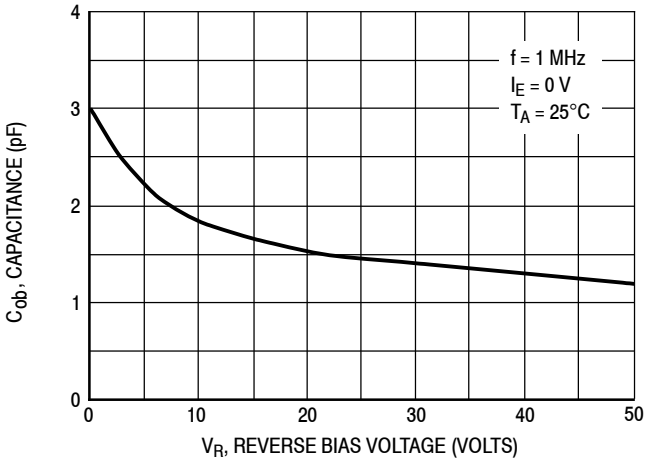


Figure 9. Output Capacitance

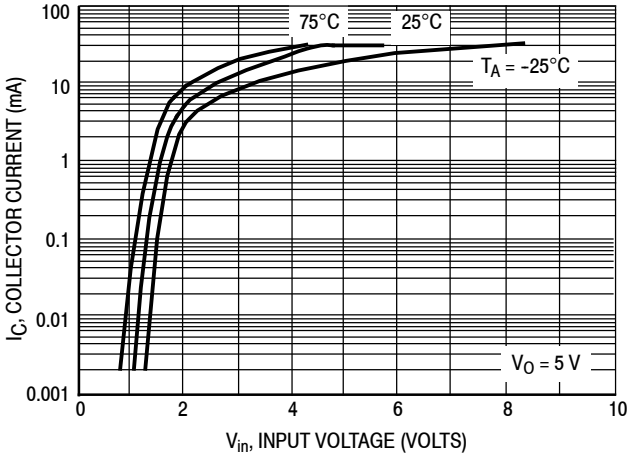


Figure 10. Output Current versus Input Voltage

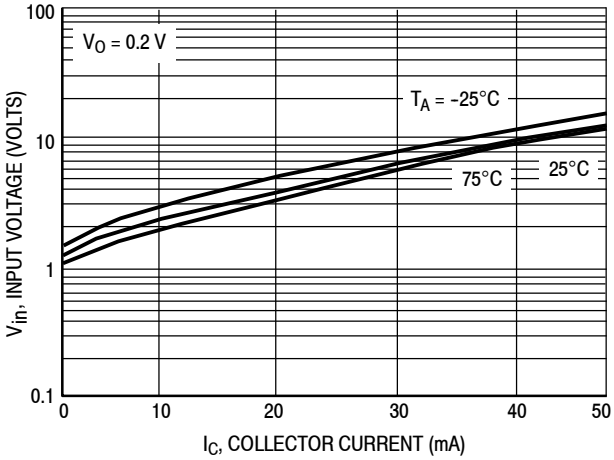


Figure 11. Input Voltage versus Output Current

TYPICAL ELECTRICAL CHARACTERISTICS – MUN5213T1G

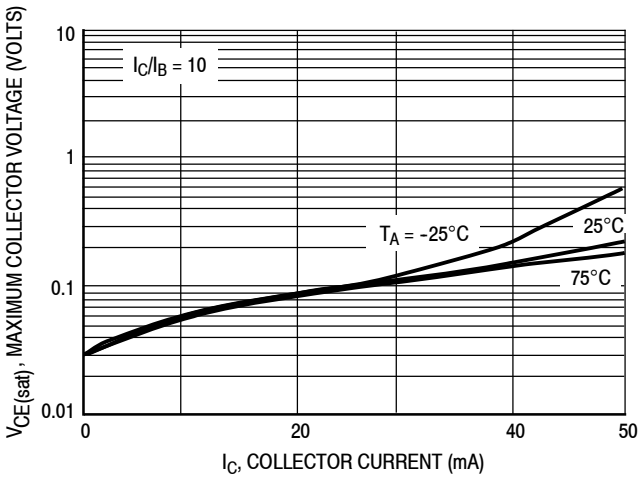


Figure 12. $V_{CE(sat)}$ versus I_C

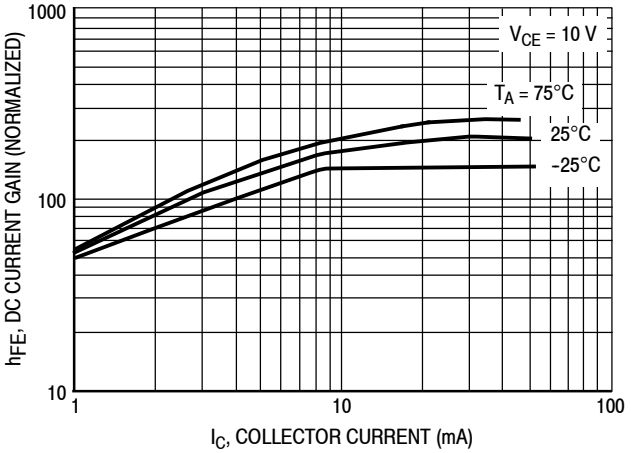


Figure 13. DC Current Gain

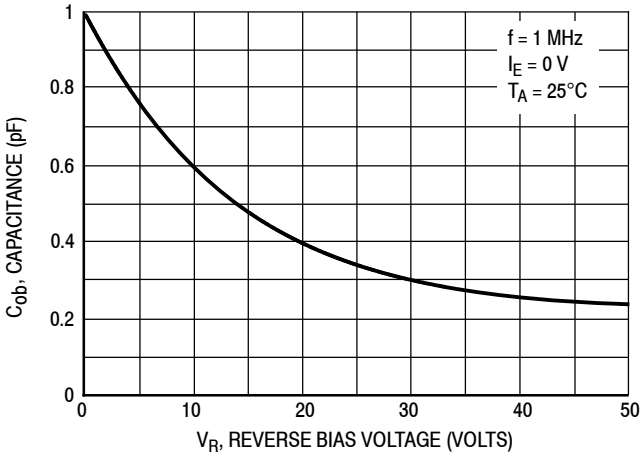


Figure 14. Output Capacitance

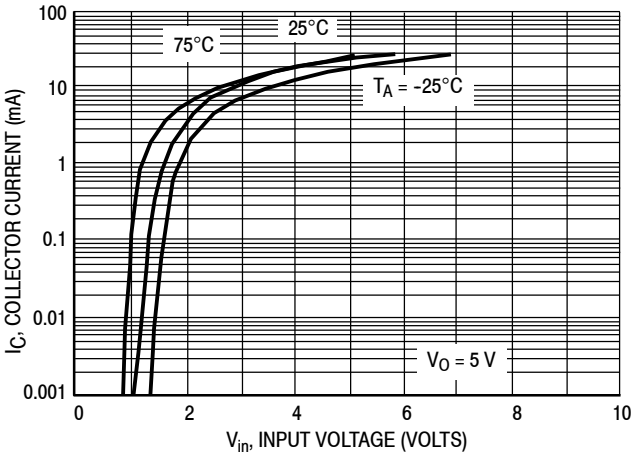


Figure 15. Output Current versus Input Voltage

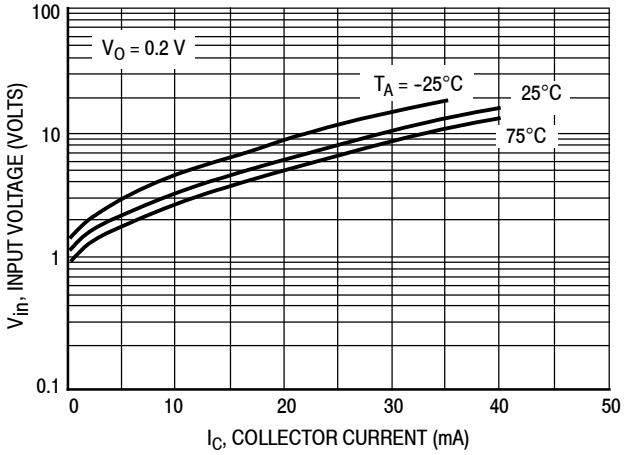


Figure 16. Input Voltage versus Output Current

TYPICAL ELECTRICAL CHARACTERISTICS – MUN5214T1G

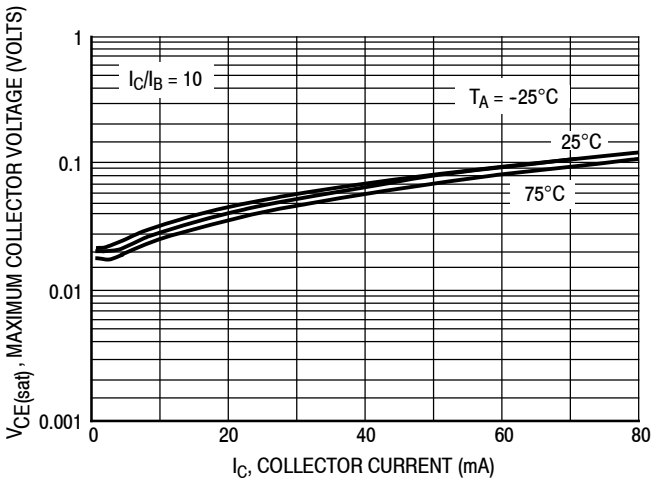


Figure 17. $V_{CE(sat)}$ versus I_C

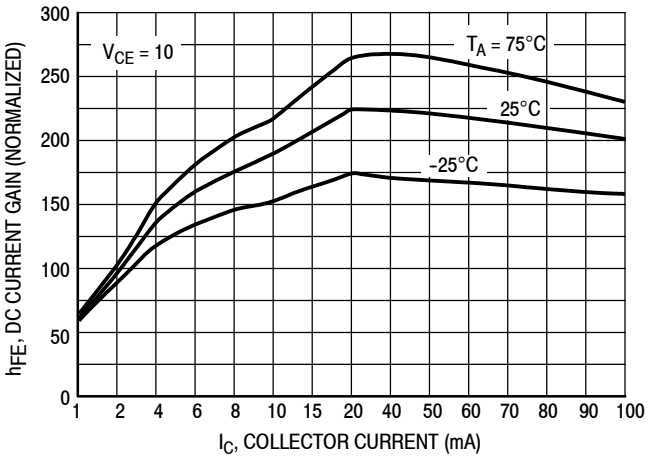


Figure 18. DC Current Gain

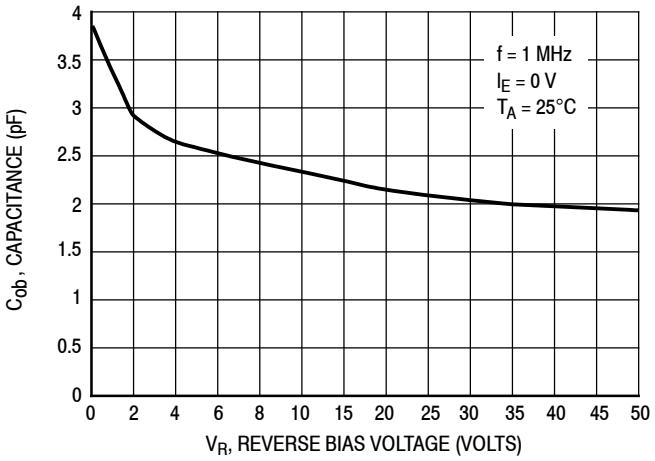


Figure 19. Output Capacitance

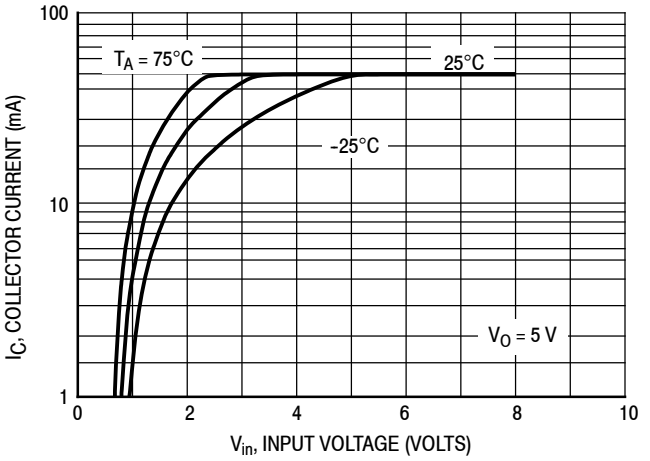


Figure 20. Output Current versus Input Voltage

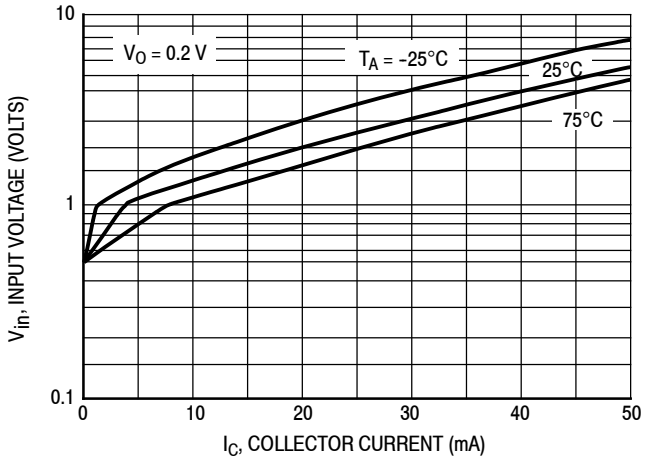


Figure 21. Input Voltage versus Output Current

TYPICAL ELECTRICAL CHARACTERISTICS — MUN5215T1G

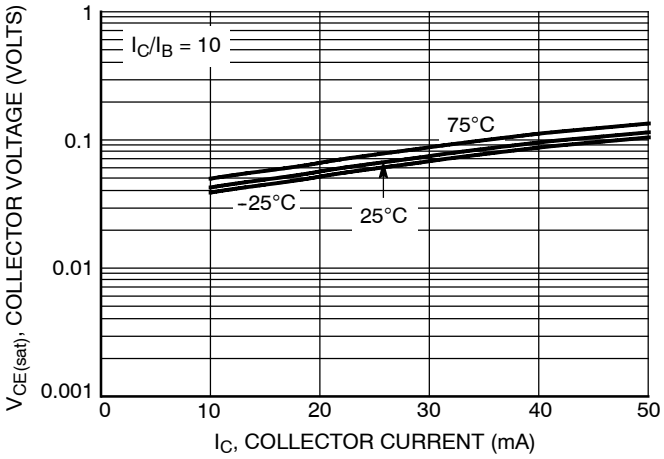


Figure 22. $V_{CE(sat)}$ versus I_C

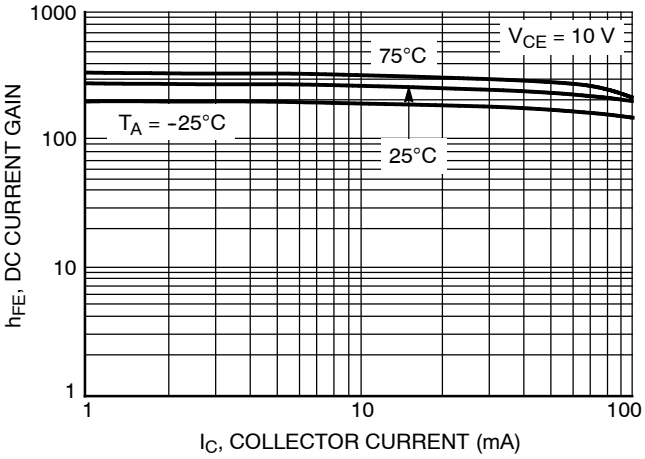


Figure 23. DC Current Gain

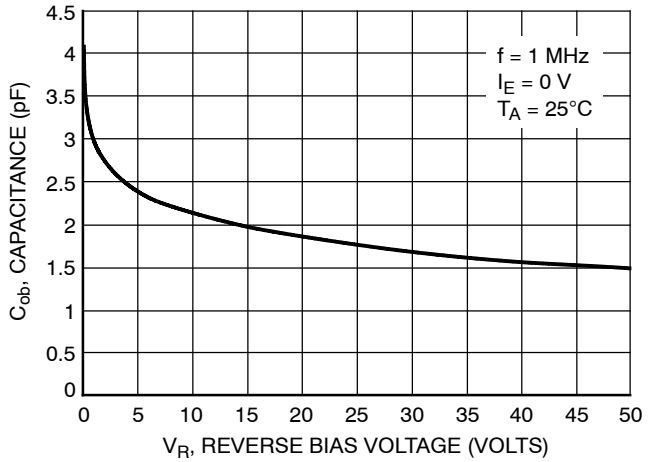


Figure 24. Output Capacitance

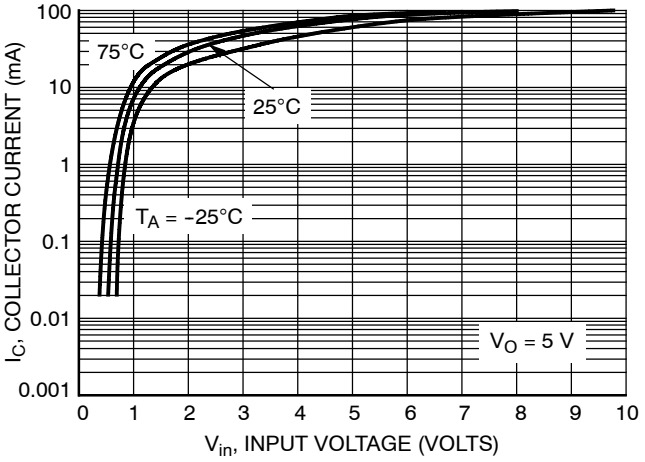


Figure 25. Output Current versus Input Voltage

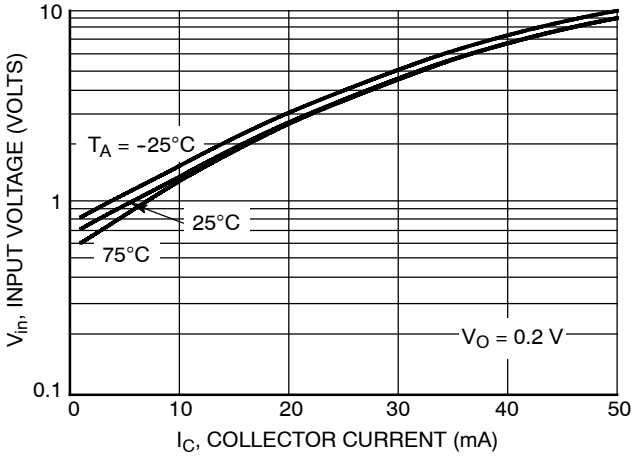


Figure 26. Input Voltage versus Output Current

TYPICAL ELECTRICAL CHARACTERISTICS — MUN5230T1G

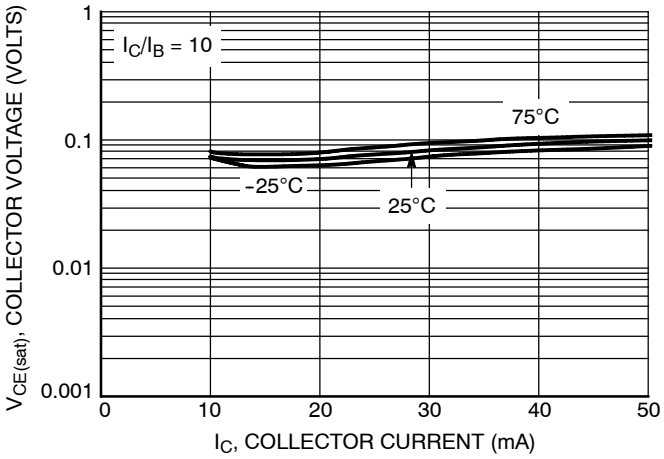


Figure 27. $V_{CE(sat)}$ versus I_C

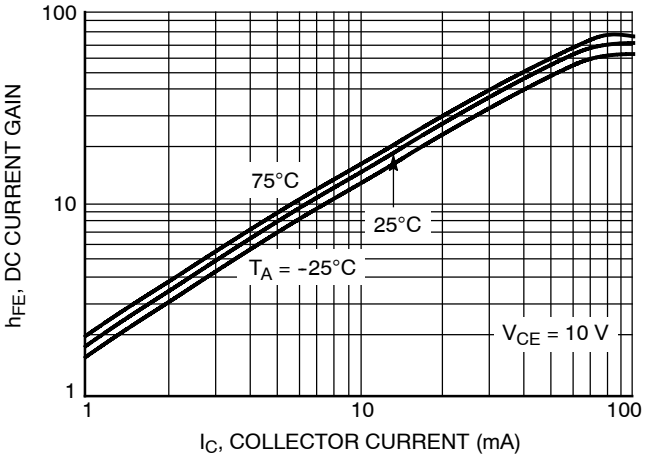


Figure 28. DC Current Gain

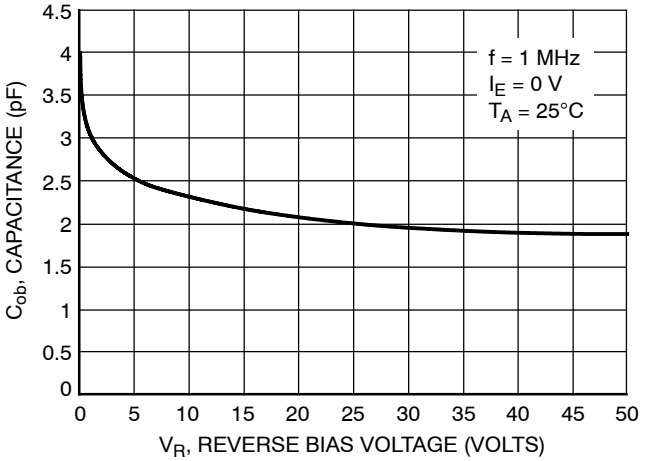


Figure 29. Output Capacitance

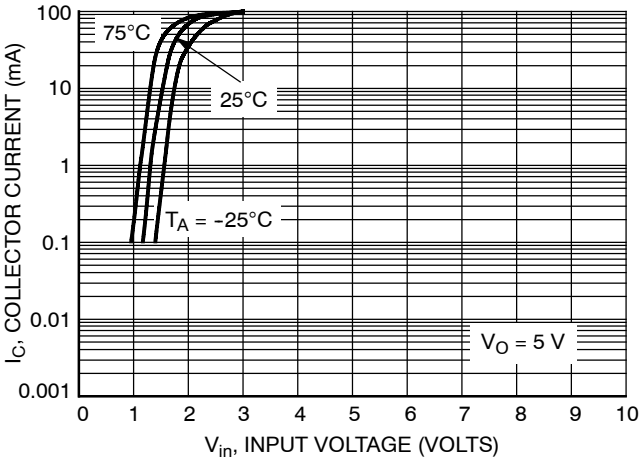


Figure 30. Output Current versus Input Voltage

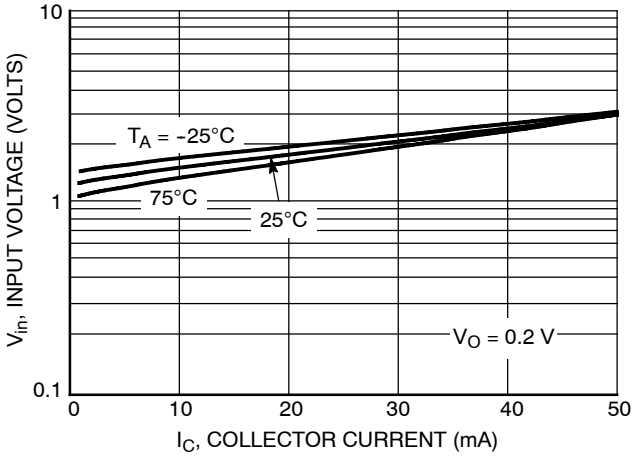


Figure 31. Input Voltage versus Output Current

TYPICAL ELECTRICAL CHARACTERISTICS — MUN5232T1G

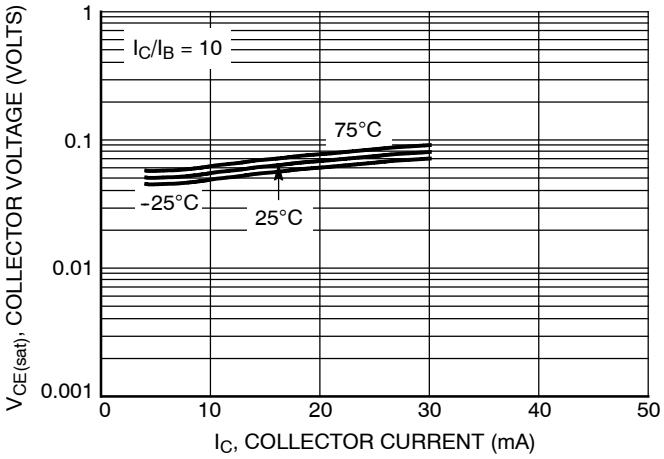


Figure 32. $V_{CE(sat)}$ versus I_C

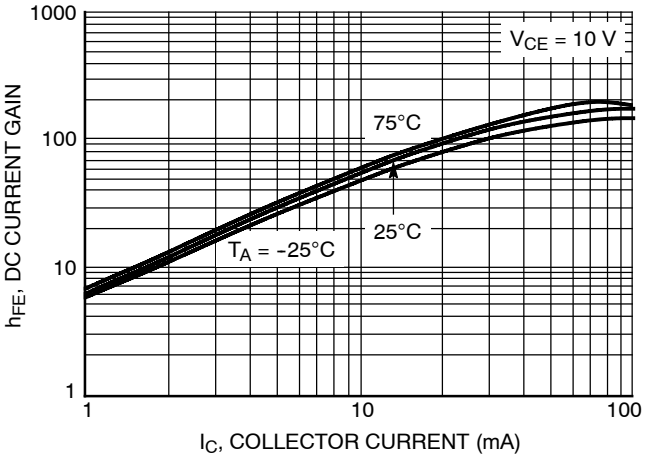


Figure 33. DC Current Gain

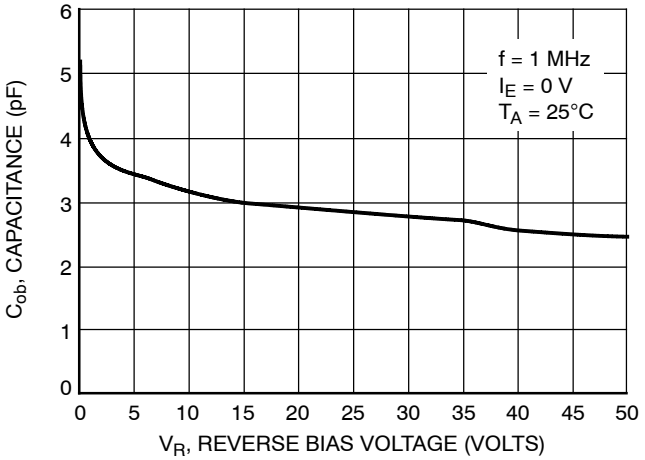


Figure 34. Output Capacitance

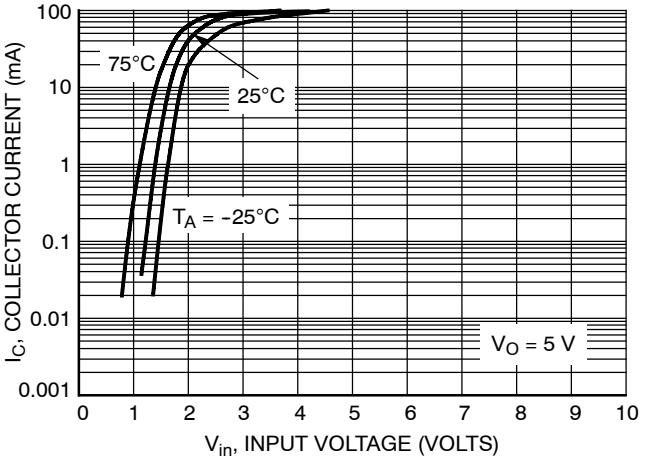


Figure 35. Output Current versus Input Voltage

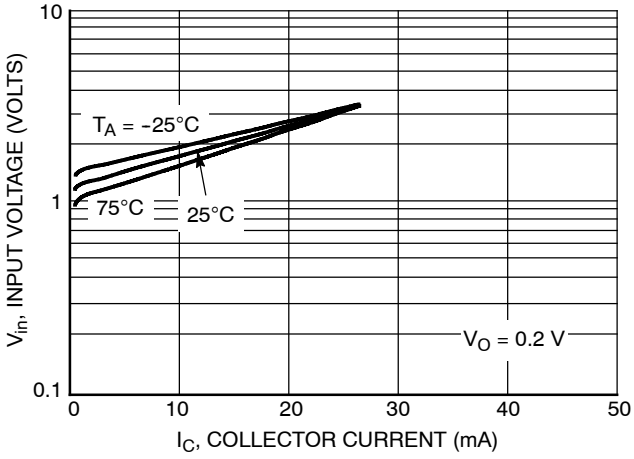


Figure 36. Input Voltage versus Output Current

TYPICAL ELECTRICAL CHARACTERISTICS — MUN5233T1G

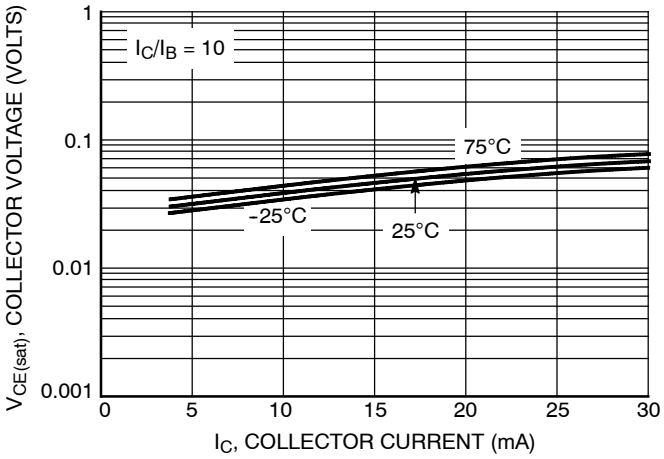


Figure 37. $V_{CE(sat)}$ versus I_C

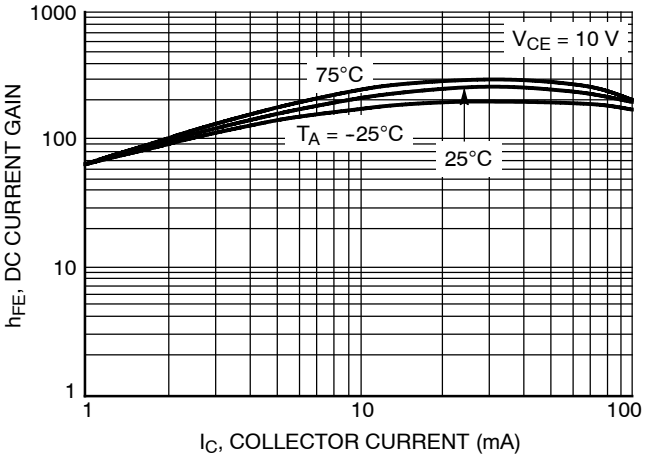


Figure 38. DC Current Gain

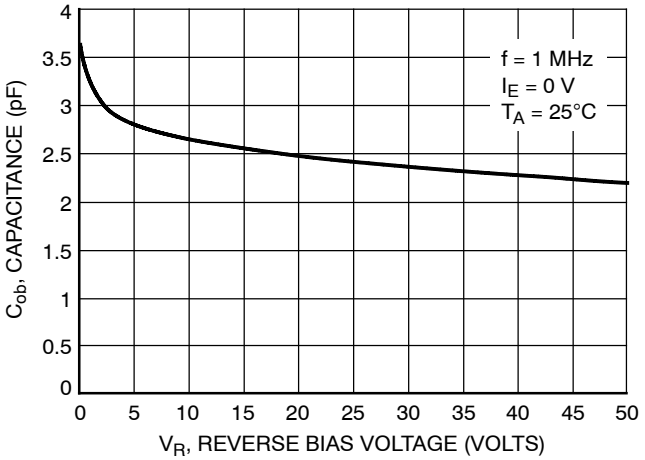


Figure 39. Output Capacitance

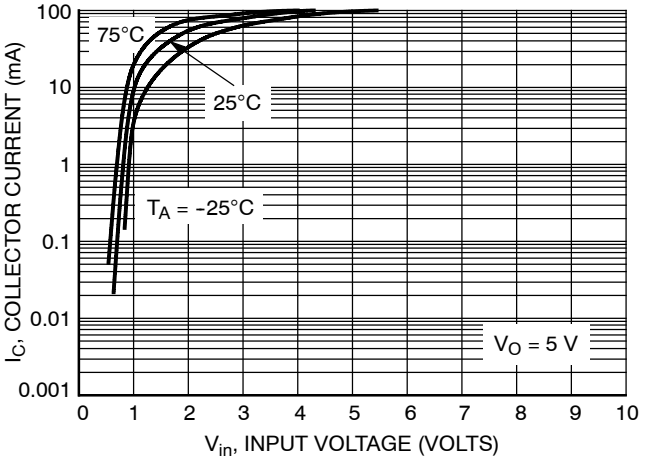


Figure 40. Output Current versus Input Voltage

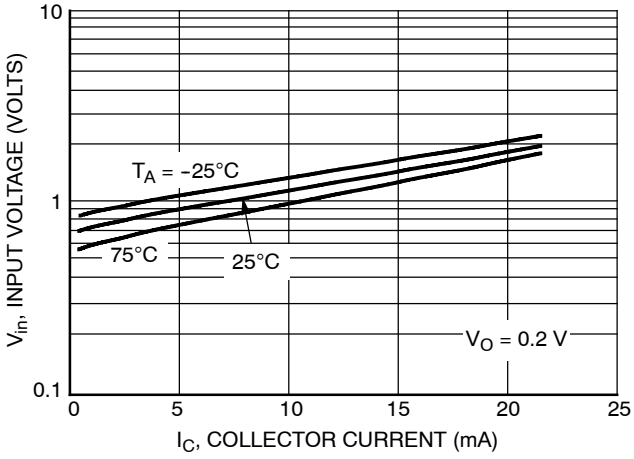


Figure 41. Input Voltage versus Output Current

TYPICAL ELECTRICAL CHARACTERISTICS — MUN5235T1G

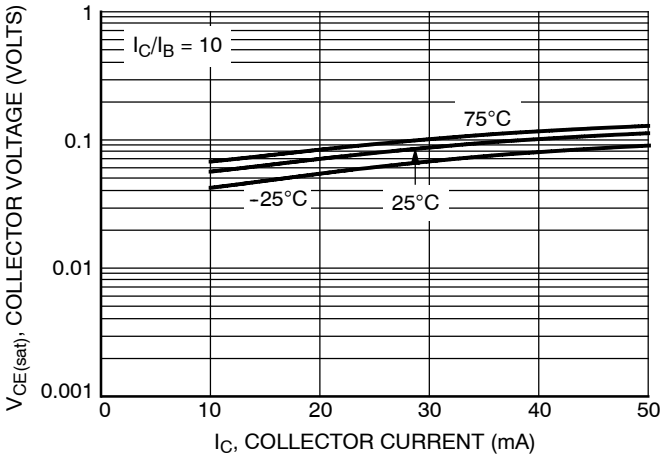


Figure 42. $V_{CE(sat)}$ versus I_C

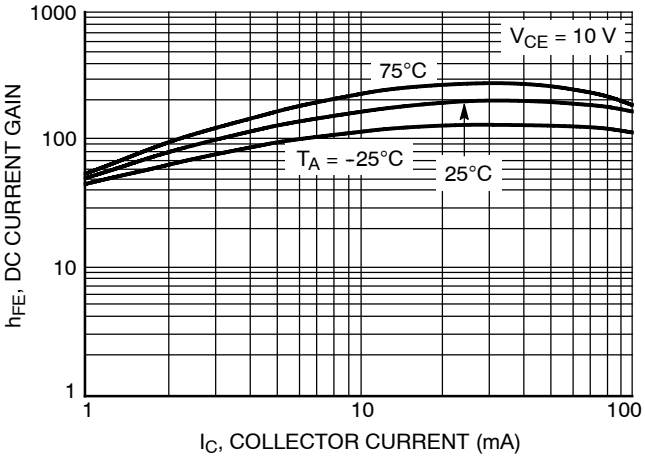


Figure 43. DC Current Gain

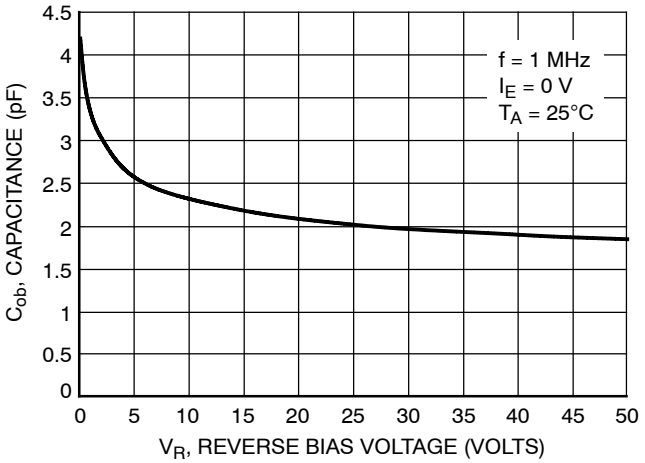


Figure 44. Output Capacitance

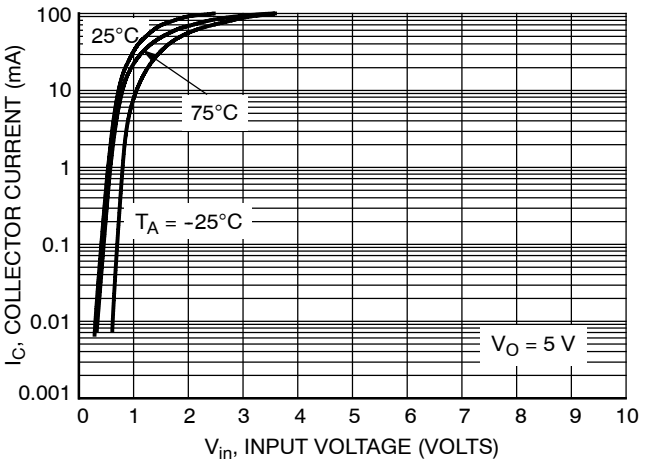


Figure 45. Output Current versus Input Voltage

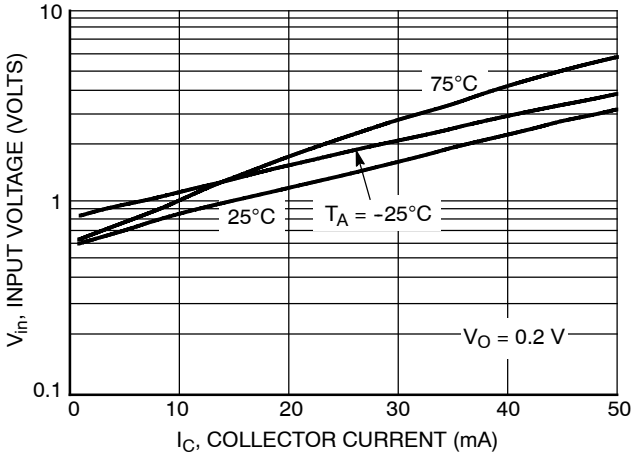


Figure 46. Input Voltage versus Output Current

TYPICAL APPLICATIONS FOR NPN BRTs

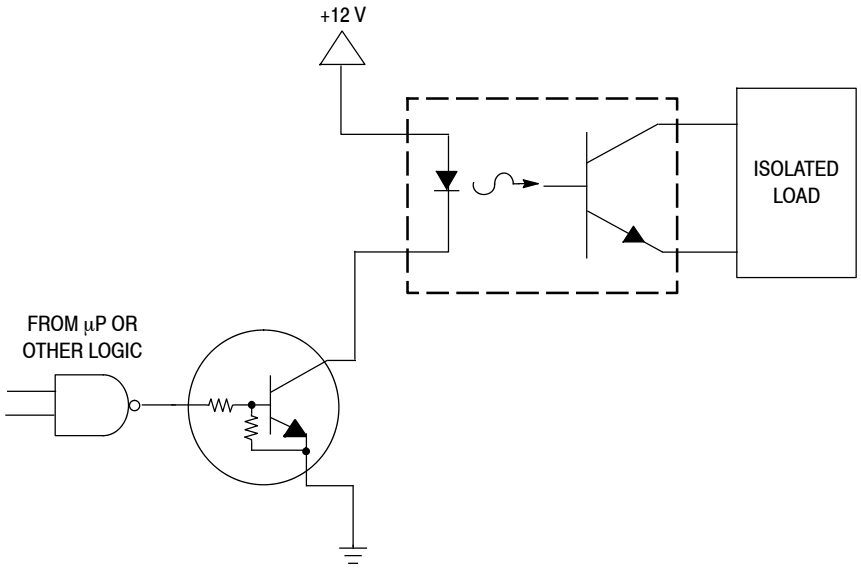


Figure 47. Level Shifter: Connects 12 or 24 Volt Circuits to Logic

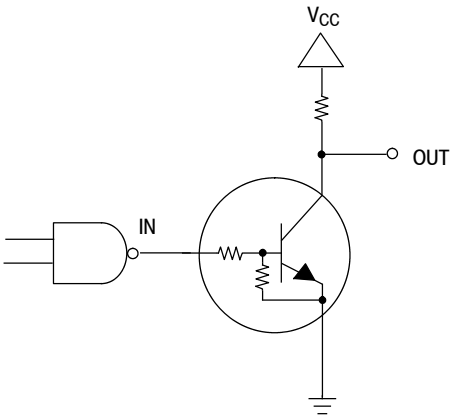


Figure 48. Open Collector Inverter: Inverts the Input Signal

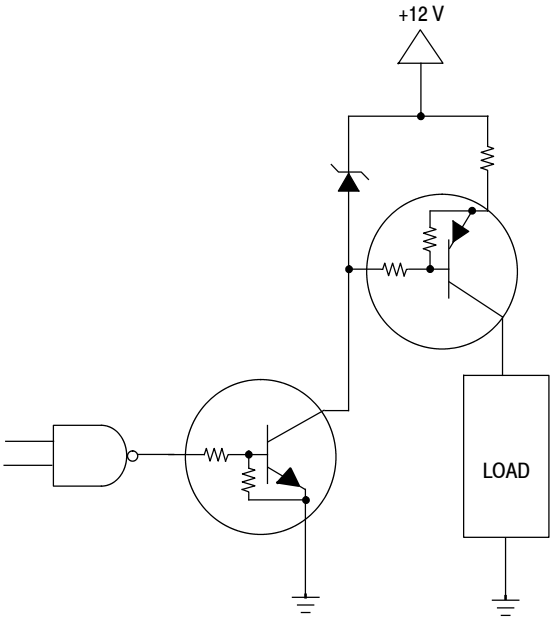
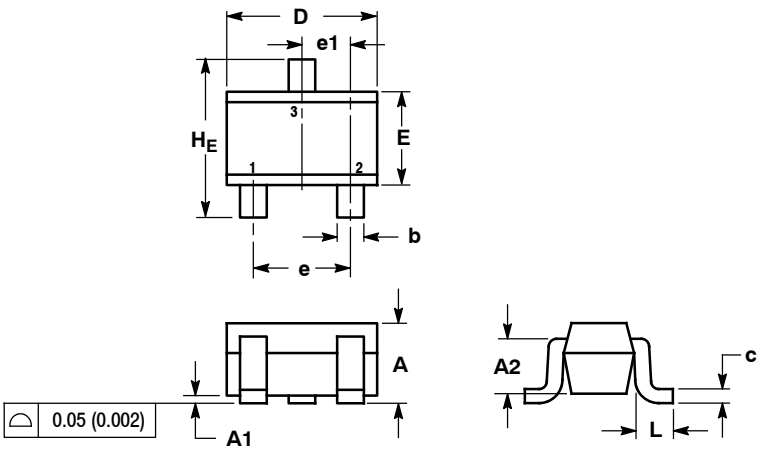


Figure 49. Inexpensive, Unregulated Current Source

PACKAGE DIMENSIONS

SC−70 (SOT−323)
CASE 419−04
ISSUE N

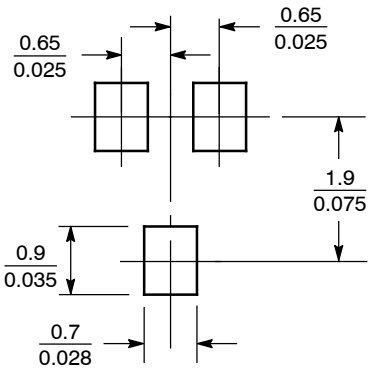


NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2		0.70 REF			0.028 REF	
b	0.30	0.35	0.40	0.012	0.014	0.016
c	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
e	1.20	1.30	1.40	0.047	0.051	0.055
e1		0.65 BSC			0.026 BSC	
L	0.20	0.38	0.56	0.008	0.015	0.022
H_E	2.00	2.10	2.40	0.079	0.083	0.095

STYLE 3:
PIN 1. BASE
2. EMITTER
3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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