General purpose (dual digital transistors) EMH11/UMH11N/IMH11A

● Features

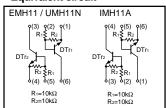
- Two DTC114E chips in a EMT or UMT or SMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

●Structure

Epitaxial planar type NPN silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr1 and DTr2.

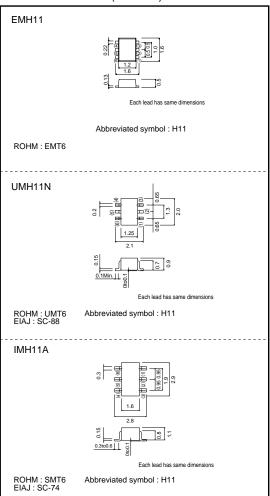
●Equivalent circuit



Packaging specifications

	Package	Taping		
	Code	T2R	TN	T110
Туре	Basic ordering unit (pieces)	8000	3000	3000
EMH11		0	-	-
UMH11N		-	0	-
IMH11A		-	-	0

●External dimensions (Unit:mm)



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		Vin	40	V	
		VIN	-10		
Output current		lo	50	mA	
Collector current		Ic (Max.)	100	mA	
Power dissipation	EMH11,UMH11N	Pd	150 (TOTAL)	*1 mW	
	IMH11A	Pu	300 (TOTAL)	*2	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

^{*1 120}mW per element must not be exceeded. *2 200mW per element must not be exceeded.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltage	VI (off)	-	-	0.5	.,	Vcc=5V, Io=100μA	
	VI (on)	3	-	-	V	Vo=0.3V, Io=10mA	
Output voltage	Vo (on)	-	0.1	0.3	V	lo/l=10mA/0.5mA	
Input current	lı .	-	-	0.88	mA	Vi=5V	
Output current	IO (off)	-	-	0.5	μΑ	Vcc=50V, Vi=0V	
DC current gain	Gı	30	-	-	-	Vo=5V, Io=5mA	
Transition frequency	fτ	_	250	-	MHz	Vc==10V, I==-5mA, f=100MHz *	
Input resistance	R ₁	7	10	13	kΩ	_	
Resistance ratio	R2/R1	0.8	1	1.2	-	_	

^{*} Transition frequency of the device

•Electrical characteristic curves

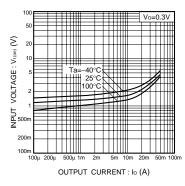


Fig.1 Input voltage vs. output current (ON characteristics)

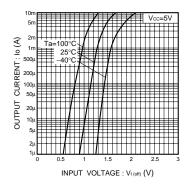


Fig.2 Output current vs. input voltage (OFF characteristics)

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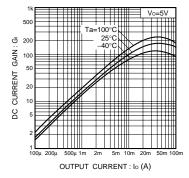


Fig.3 DC current gain vs. output current

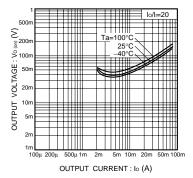


Fig.4 Output voltage vs. output current

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