

General purpose (dual digital transistors)

EMH9 / UMH9N / IMH9A

●Features

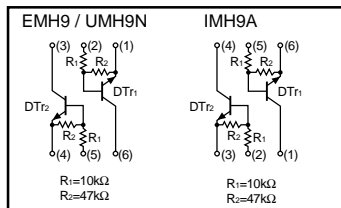
- 1) Two DTC114Ys chips in a EMT or UMT or SMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

●Structure

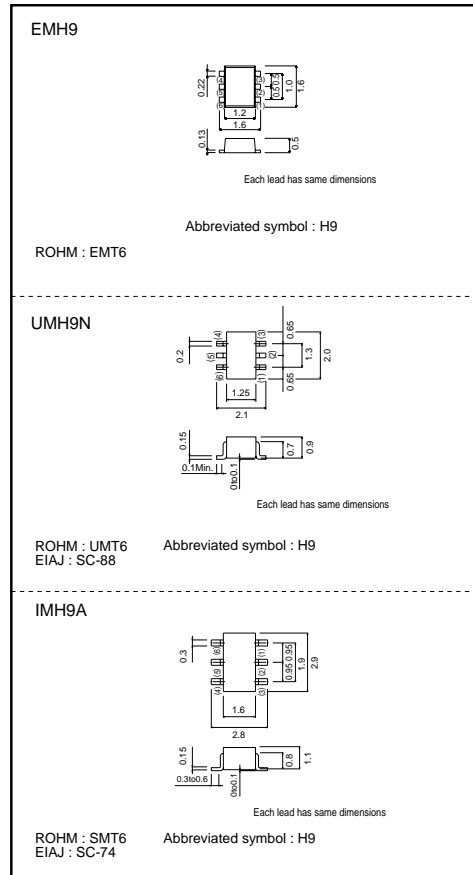
Epitaxial planar type
NPN silicon transistor
(Built-in resistor type)

The following characteristics apply to both DT_{r1} and DT_{r2}.

●Equivalent circuit



●External dimensions (Unit : mm)



●Packaging specifications

Type	Package	Taping		
		Code	T2R	TN
	Basic ordering unit (pieces)	8000	3000	3000
EMH9		○	-	-
UMH9N		-	○	-
IMH9A		-	-	○

Transistors

●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit		
Supply voltage	V _{CC}	50	V		
Input voltage	V _{IN}	40	V		
		-6			
Output current	I _o	70	mA		
	I _{c (Max.)}	100			
Power dissipation	EMH9,UMH9N	Pd	150 (TOTAL)	mW	*1
	IMH9A		300 (TOTAL)		
Junction temperature	T _j	150	°C		
Storage temperature	T _{stg}	-55 to +150	°C		

*1 120mW per element must not be exceeded.
 *2 200mW per element must not be exceeded.

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V _{I (off)}	-	-	0.3	V	V _{CC} =5V, I _o =100μA
	V _{I (on)}	1.4	-	-		V _O =0.3V, I _o =1mA
Output voltage	V _{O (on)}	-	0.1	0.3	V	I _o /I _i =5mA/0.25mA
Input current	I _i	-	-	0.88	mA	V _I =5V
Output current	I _{o (off)}	-	-	0.5	μA	V _{CC} =50V, V _I =0V
DC current gain	G _i	68	-	-	-	V _O =5V, I _o =5mA
Transition frequency	f _T	-	250	-	MHz	V _{CE} =10V, I _E =-5mA, f=100MHz *
Input resistance	R ₁	7	10	13	kΩ	-
Resistance ratio	R ₂ /R ₁	3.7	4.7	5.7	-	-

* Transition frequency of the device

●Electrical characteristic curves

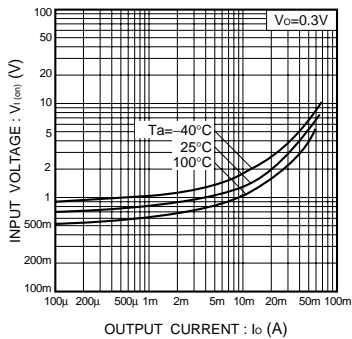


Fig.1 Input voltage vs. output current (ON characteristics)

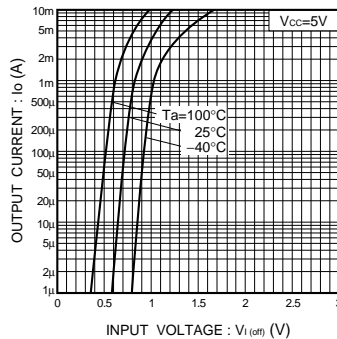


Fig.2 Output current vs. input voltage (OFF characteristics)

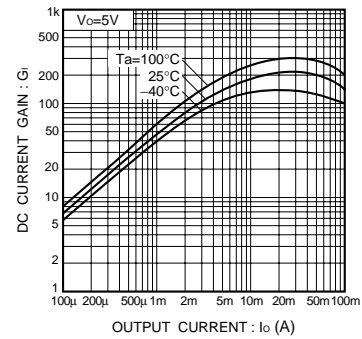


Fig.3 DC current gain vs. output current

Transistors

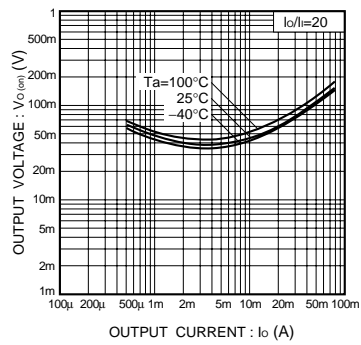


Fig.4 Output voltage vs. output current

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