# **Dual General Purpose Transistors**

# **NPN Duals**

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-563 which is designed for low power surface mount applications.

#### **Features**

• These are Pb-Free Devices

### **MAXIMUM RATINGS**

Rating	Symbol	BC847	BC848	Unit
Collector - Emitter Voltage	V <sub>CEO</sub>	45	30	V
Collector - Base Voltage	V <sub>CBO</sub>	50	30	V
Emitter - Base Voltage	V <sub>EBO</sub>	6.0	5.0	V
Collector Current - Continuous	I <sub>C</sub>	100	100	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### THERMAL CHARACTERISTICS

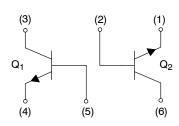
Characteristic (One Junction Heated)	Symbol	Max	Unit
Total Device Dissipation, (Note 1)  T <sub>A</sub> = 25°C  Derate above 25°C	P <sub>D</sub>	357 2.9	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	350	°C/W
Characteristic (Both Junctions Heated)	Symbol	Max	Unit
Total Device Dissipation, (Note 1)  T <sub>A</sub> = 25°C  Derate above 25°C	P <sub>D</sub>	500 4.0	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	250	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

<sup>1.</sup> FR-4 @ Minimum Pad



## ON Semiconductor®

http://onsemi.com



BC847CDXV6T1



CASE 463A

#### **MARKING DIAGRAMS**



= Device Code x = G or M

= Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = 10 mA)	BC847CDXV6T1 BC848CDXV6T1	V <sub>(BR)CEO</sub>	45 30	_ _	- -	V
Collector – Emitter Breakdown Voltage $(I_C = 10 \ \mu\text{A}, \ V_{EB} = 0)$	BC847CDXV6T1 BC848CDXV6T1	V <sub>(BR)CES</sub>	50 30	- -	- -	٧
Collector – Base Breakdown Voltage ( $I_C = 10 \mu A$ )	BC847CDXV6T1 BC848CDXV6T1	V <sub>(BR)CBO</sub>	50 30	_ _	- -	V
Emitter – Base Breakdown Voltage (I <sub>E</sub> = 1.0 μA)	BC847CDXV6T1 BC848CDXV6T1	V <sub>(BR)EBO</sub>	6.0 5.0	_ _	- -	V
Collector Cutoff Current (V <sub>CB</sub> = 30 V) (V <sub>CB</sub> = 30 V, T <sub>A</sub> = 150°C)		I <sub>CBO</sub>	-	_ _	15 5.0	nA μA
ON CHARACTERISTICS						
DC Current Gain $ \begin{aligned} &(I_C = 10 \; \mu\text{A, V}_{CE} = 5.0 \; \text{V}) \\ &(I_C = 2.0 \; \text{mA, V}_{CE} = 5.0 \; \text{V}) \end{aligned} $		h <sub>FE</sub>	- 420	270 520	- 800	_
Collector – Emitter Saturation Voltage ( $I_C$ = 10 mA, $I_B$ = 0 ( $I_C$ = 100 mA, $I_B$ = 1	V <sub>CE(sat)</sub>	- -	_ _	0.25 0.6	V	
Base – Emitter Saturation Voltage ( $I_C$ = 10 mA, $I_B$ = 0.5 mA) ( $I_C$ = 100 mA, $I_B$ = 5.0 mA)		V <sub>BE(sat)</sub>	-	0.7 0.9	- -	V
Base – Emitter Voltage ( $I_C$ = 2.0 mA, $V_{CE}$ = 5.0 V) ( $I_C$ = 10 mA, $V_{CE}$ = 5.0 V)		V <sub>BE(on)</sub>	580 -	660 -	700 770	mV
SMALL-SIGNAL CHARACTERISTICS						
Current – Gain – Bandwidth Product (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 Vdc, f = 100 MHz)		f <sub>T</sub>	100	-	-	MHz
Output Capacitance (V <sub>CB</sub> = 10 V, f = 1.0 MHz)		C <sub>obo</sub>	_	-	1.5	pF
Noise Figure (I <sub>C</sub> = 0.2 mA, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 2.0 k $\Omega$ ,f = 1.0 kHz,	BW = 200 Hz)	NF	-	-	10	dB

### **ORDERING INFORMATION**

Device	Specific Marking	Package	Shipping <sup>†</sup>
BC847CDXV6T1G	1G	SOT-563 (Pb-Free)	4000 Units / Tape & Reel
BC847CDXV6T5G	IG	SOT-563 (Pb-Free)	8000 Units / Tape & Reel
BC848CDXV6T1G	1L	SOT-563 (Pb-Free)	4000 Units / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### **TYPICAL CHARACTERISTICS**

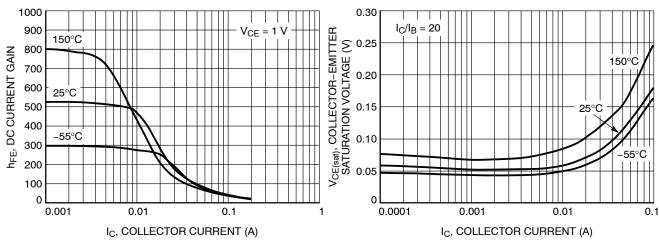


Figure 1. DC Current Gain vs. Collector Current

Figure 2. Collector Emitter Saturation Voltage vs. Collector Current

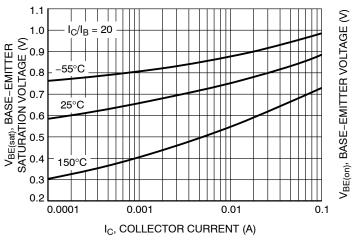


Figure 3. Base Emitter Saturation Voltage vs.
Collector Current

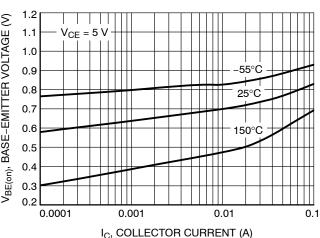
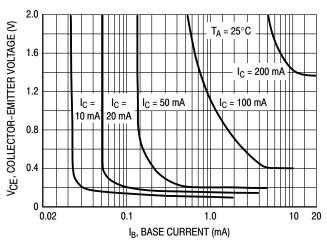


Figure 4. Base Emitter Voltage vs. Collector Current

### **TYPICAL CHARACTERISTICS**



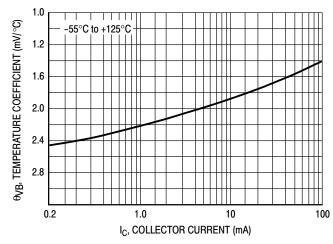
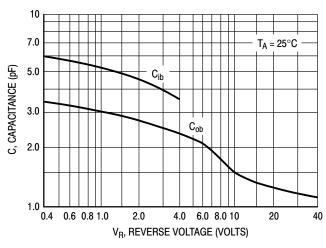


Figure 5. Collector Saturation Region

Figure 6. Base-Emitter Temperature Coefficient



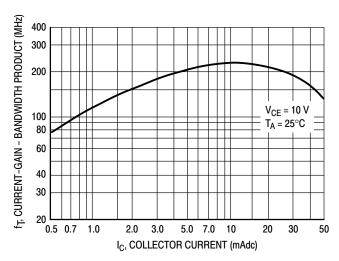


Figure 7. Capacitances

Figure 8. Current-Gain - Bandwidth Product

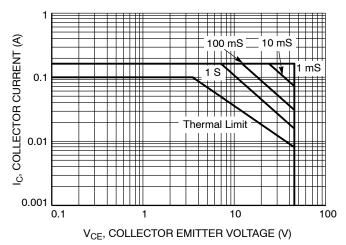
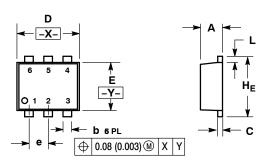


Figure 9. Safe Operating Area

#### PACKAGE DIMENSIONS

SOT-563, 6 LEAD CASE 463A-01 ISSUE F



#### NOTES

- 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETERS MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	MILLIMETERS			MILLIMETERS INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.50	0.55	0.60	0.020	0.021	0.023
b	0.17	0.22	0.27	0.007	0.009	0.011
С	0.08	0.12	0.18	0.003	0.005	0.007
D	1.50	1.60	1.70	0.059	0.062	0.066
E	1.10	1.20	1.30	0.043	0.047	0.051
е	0.5 BSC				0.02 BS0	
L	0.10	0.20	0.30	0.004	0.008	0.012
He	1.50	1.60	1.70	0.059	0.062	0.066

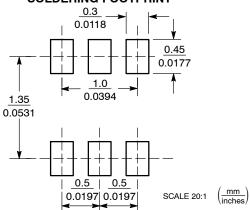
#### STYLE 1:

- PIN 1. EMITTER 1

  - 2. BASE 1 3. COLLECTOR 2 4. EMITTER 2

  - 5. BASE 2 6. COLLECTOR 1

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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