TOSHIBA Multi-Chip Transistor Silicon NPN Epitaxial Type

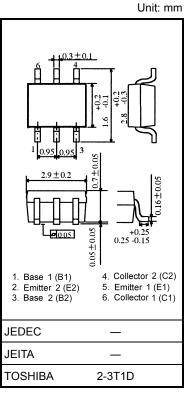
## **TPC6701**

High-Speed Switching Applications Motor Drive Applications Inverter Lighting Applications

- Two NPN transistors are mounted on a compact and slim package.
- High DC current gain:  $h_{FE} = 400$  to 1000 (IC = 0.1 A)
- Low collector-emitter saturation voltage:  $V_{CE}$  (sat) = 0.17 V (max)
- High-speed switching:  $t_f = 85 \text{ ns (typ.)}$

#### Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Collector-base voltage		V <sub>CBO</sub>	100	V	
Collector-emitter voltage		V <sub>CEX</sub>	80	V	
Collector-emitter voltage		V <sub>CEO</sub>	50	V	
Emitter-base voltage		V <sub>EBO</sub>	7	V	
Collector current	DC	IC	1.0	Α	
	Pulse	I <sub>CP</sub>	2.0	A	
Base current		ΙΒ	0.1	mA	
Collector power dissipation (single-device operation)		P <sub>C</sub> (Note 1)	400	mW	
Total collector power dissipation (simultaneous operation)		P <sub>CT</sub> (Note 2)	660	mW	
Thermal resistance, junction to ambient (single-device operation)		R <sub>th (j-a)</sub> (Note 1)	312	°C/W	
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 0.011 g (typ.)

Note 1: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm<sup>2</sup>)

Note 2: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

Total collector power dissipation value when two devices are operated at the same time

### **Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I <sub>CBO</sub>	$V_{CB} = 100 \text{ V}, I_E = 0$	_	_	100	nA
Emitter cut-off current		I <sub>EBO</sub>	$V_{EB} = 7 \text{ V}, I_{C} = 0$	_	_	100	nA
Collector-emitter breakdown voltage		V (BR) CEO	$I_C = 10 \text{ mA}, I_B = 0$	50	_	_	V
DC current gain		h <sub>FE</sub> (1)	$V_{CE} = 2 \text{ V}, I_{C} = 0.1 \text{ A}$	400	_	1000	
		h <sub>FE</sub> (2)	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 0.3 A	200	_	_	
Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	$I_C = 300 \text{ mA}, I_B = 6 \text{ mA}$	_	_	0.17	V
Base-emitter saturation voltage		V <sub>BE (sat)</sub>	$I_C = 300 \text{ mA}, I_B = 6 \text{ mA}$	_	_	1.10	V
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	_	5	_	pF
Switching time	Rise time	t <sub>r</sub>	See Figure 1 circuit diagram.	_	35	_	
	Storage time	t <sub>stg</sub>	$V_{CC} \approx 30 \text{ V}, R_L = 100 \Omega$	_	680	_	ns
	Fall time	t <sub>f</sub>	$I_{B1} = -I_{B2} = 10 \text{ mA}$		85	_	

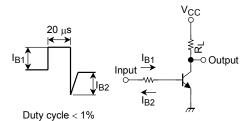
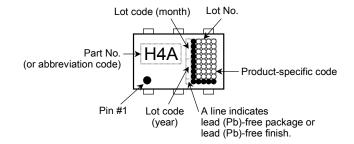


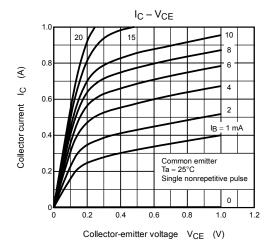
Figure 1 Switching Time Test Circuit & Timing Chart

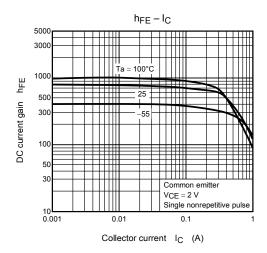
#### **Circuit Configuration**

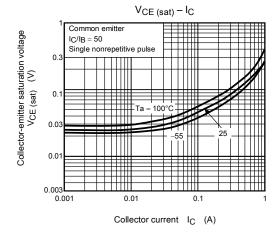
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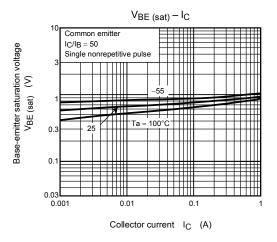
#### Marking

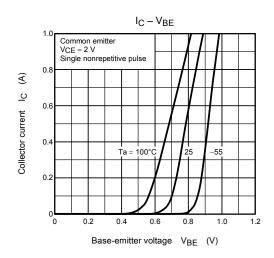




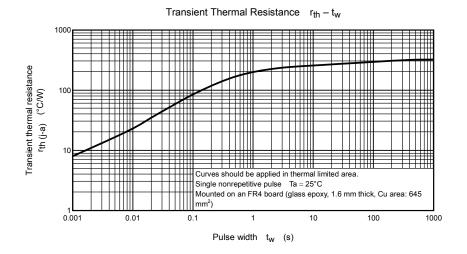


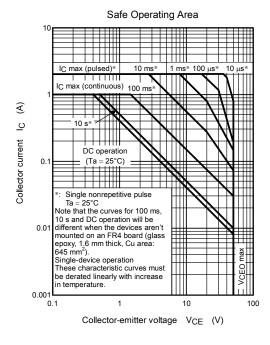


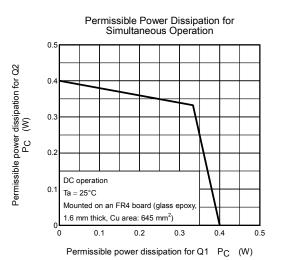




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