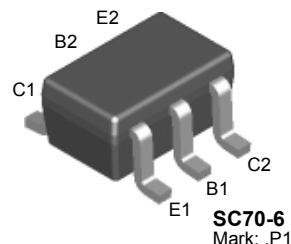


FFB5551

FFB5551

Dual-Chip NPN General Purpose Amplifier

- This device is designed for general purpose high voltage amplifiers.
- E1 is Pin 1.



Absolute Maximum Ratings* $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{CEO}	Collector-Emitter Voltage	160	V
V_{CBO}	Collector-Base Voltage	180	V
V_{EBO}	Emitter-Base Voltage	6.0	V
I_C	Collector Current - Continuous	200	mA
T_J, T_{STG}	Operating and Storage Junction Temperature Range	- 55 ~ 150	$^\circ\text{C}$

* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Characteristics					
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage *	$I_C = 1.0\text{mA}, I_B = 0$	160		V
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage	$I_C = 100\mu\text{A}, I_E = 0$	180		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E = 10\mu\text{A}, I_C = 0$	6.0		V
I_{CBO}	Collector Cut-off Current	$V_{CB} = 120\text{V}, I_E = 0$ $V_{CB} = 120\text{V}, I_E = 0, T_A = 100^\circ\text{C}$		50 50	nA μA
I_{EBO}	Emitter Cut-off Current	$V_{EB} = 4.0\text{V}, I_C = 0$		50	nA
On Characteristics *					
h_{FE}	DC Current Gain	$V_{CE} = 5.0\text{V}, I_C = 1.0\text{mA}$ $V_{CE} = 5.0\text{V}, I_C = 10\text{mA}$ $V_{CE} = 5.0\text{V}, I_C = 50\text{mA}$	80 80 30	250	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 10\text{mA}, I_B = 1.0\text{mA}$ $I_C = 50\text{mA}, I_B = 5.0\text{mA}$		0.15 0.20	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = 10\text{mA}, I_B = 1.0\text{mA}$ $I_C = 50\text{mA}, I_B = 5.0\text{mA}$		1.0 1.0	V
Small Signal Characteristics					
f_T	Current gain Bandwidth Product	$V_{CE} = 10\text{V}, I_C = 10\text{mA}$ $f = 100\text{MHz}$	100	300	MHz
C_{obo}	Output Capacitance	$V_{CB} = 10\text{V}, I_E = 0, f = 1.0\text{MHz}$		6.0	pF

* Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2.0\%$

Thermal Characteristics $T_A=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Max.	Units
P_D	Total Device Dissipation	200	mW
	Derate above 25°C	1.6	mW/ $^{\circ}\text{C}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	625	$^{\circ}\text{C}/\text{W}$

Typical Characteristics

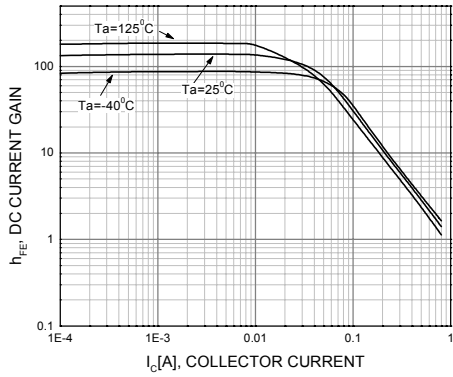


Figure 1. DC Current Gain

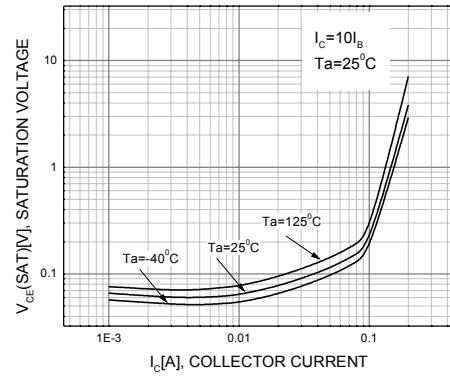


Figure 2. Collector-Emitter Saturation Voltage

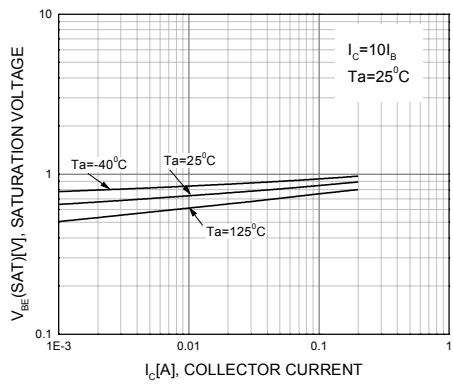


Figure 3. Base-Emitter Saturation Voltage

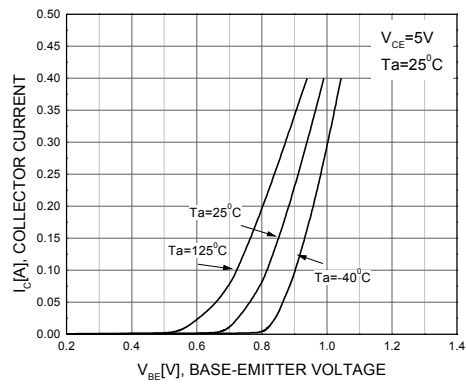
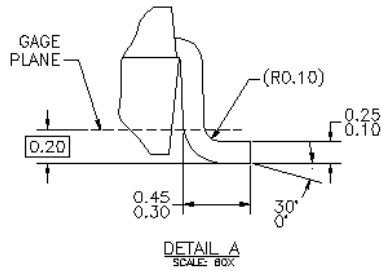
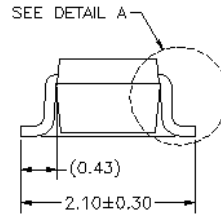
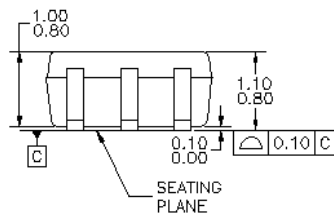
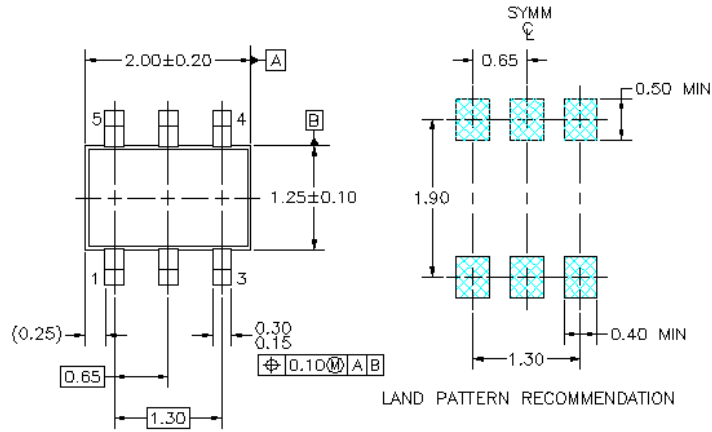


Figure 4. Base-Emitter On Voltage

Package Dimensions

SC70-6



NOTES: UNLESS OTHERWISE SPECIFIED

- A) THIS PACKAGE CONFORMS TO EIAJ SC-88, 1996.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH.

Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE ^x TM	FACT TM	ImpliedDisconnect TM	PACMAN TM	SPM TM
ActiveArray TM	FACT Quiet series TM	ISOPLANAR TM	POP TM	Stealth TM
Bottomless TM	FAST [®]	LittleFET TM	Power247 TM	SuperSOT TM -3
CoolFET TM	FAST ^r TM	MicroFET TM	PowerTrench [®]	SuperSOT TM -6
CROSSVOLT TM	FRFET TM	MicroPak TM	QFET TM	SuperSOT TM -8
DOME TM	GlobalOptoisolator TM	MICROWIRE TM	QS TM	SyncFET TM
EcoSPARK TM	GTO TM	MSX TM	QT Optoelectronics TM	TinyLogic [®]
E ² CMOS TM	HiSeC TM	MSXPro TM	Quiet Series TM	TruTranslation TM
EnSigna TM	I ² C TM	OCX TM	RapidConfigure TM	UHC TM
Across the board. Around the world. TM		OCXPro TM	RapidConnect TM	UltraFET [®]
The Power Franchise TM		OPTOLOGIC [®]	SILENT SWITCHER [®]	VCX TM
Programmable Active Droop TM		OPTOPLANAR TM	SMART START TM	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.