# General purpose transistors (dual transistors) EMX1 / UMX1N / IMX1

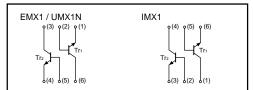
# Features

- 1) Two 2SC2412K chips in a EMT or UMT or SMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

# Structure

Epitaxial planar type NPN silicon transistor

### Equivalent circuit



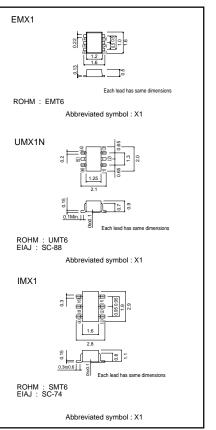
The following characteristics apply to both Tr1 and Tr2.

#### Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit			
Collector-base voltage		Vсво	60	V			
Collector-emitter voltage		Vceo	50	V			
Emitter-base voltage		Vево	7	V			
Collector current		lc	150	mA			
Power dissipation	EMX1, UMX1N	Pc	150 (TOTAL)	- mW *1 *2			
	IMX1	FC	300 (TOTAL)				
Junction temperature		Tj	150	°C			
Storage temperature		Tstg	-55~+150	°C			

\*1 120mW per element must not be exceeded. \*2 200mW per element must not be exceeded.

# • External dimensions (Units : mm)



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# Transistors

# •Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	ВVсво	60	-	-	V	lc=50μA	
Collector-emitter breakdown voltage	BVCEO	50	-	-	V	lc=1mA	
Emitter-base breakdown voltage	ВVево	7	-	-	V	Ι <sub>Ε</sub> =50μΑ	
Collector cutoff current	Ісво	-	-	0.1	μΑ	V <sub>CB</sub> =60V	
Emitter cutoff current	Іево	-	-	0.1	μΑ	V <sub>EB</sub> =7V	
Collector-emitter saturation voltage	VCE (sat)	-	-	0.4	V	Ic/I <sub>B</sub> =50mA/5mA	
DC current transfer ratio	hfe	120	-	560	-	Vce=6V, lc=1mA	
Transition frequency	f⊤	-	180	-	MHz	Vce=12V, Ie=-2mA, f=100MHz	
Output capacitance	Cob	-	2	3.5	PF	V <sub>CB</sub> =12V, I <sub>E</sub> =0A, f=1MHz	

# Packaging specifications

	Package	Taping			
	Code	T2R	TN	T110	
Туре	Basic ordering unit (pieces)	8000	3000	3000	
EMX1		0	—	—	
UMX1N		_	0	_	
IMX1		—	—	0	

#### Electrical characteristic curves

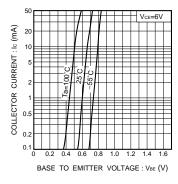
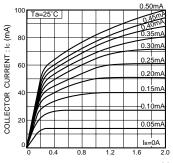
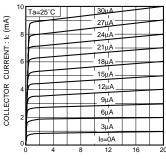


Fig.1 Grounded emitter propagation characteristics



COLLECTOR TO EMITTER VOLTAGE :  $V_{CE}$  (V)

Fig.2 Grounded emitter output characteristics (1)

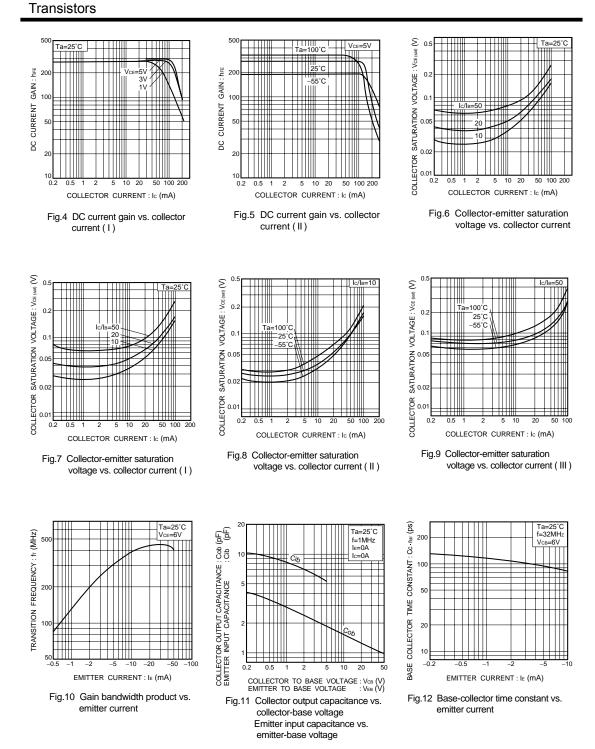


COLLECTOR TO EMITTER VOLTAGE : VCE (V)

Fig.3 Grounded emitter output characteristics ( II )

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# EMX1 / UMX1N / IMX1



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