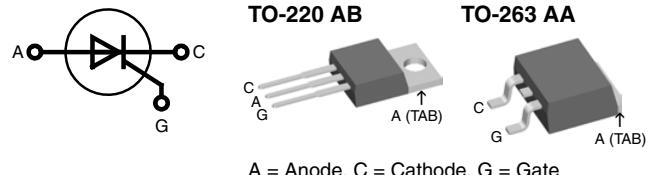


# Phase Control Thyristor

**V<sub>RRM</sub> = 800/1200 V**  
**I<sub>T(RMS)</sub> = 29 A**  
**I<sub>T(AV)M</sub> = 19 A**

V <sub>RSM</sub> V <sub>DSM</sub> V	V <sub>RRM</sub> V <sub>DRM</sub> V	Type TO-220	Type TO-263
800	800	CS 19-08ho1	CS 19-08ho1S
1200	1200	CS 19-12ho1	CS 19-12ho1S



A = Anode, C = Cathode, G = Gate

Symbol	Conditions	Maximum Ratings	
I <sub>T(RMS)</sub>	T <sub>VJ</sub> = T <sub>VJM</sub>	29	A
I <sub>T(AV)M</sub>	T <sub>C</sub> = 85°C, 180° sine	19	A
I <sub>TSM</sub>	T <sub>VJ</sub> = 45°C V <sub>R</sub> = 0 V	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	160 A 180 A
	T <sub>VJ</sub> = T <sub>VJM</sub> V <sub>R</sub> = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	140 A 160 A
I <sup>2</sup> t	T <sub>VJ</sub> = 45°C V <sub>R</sub> = 0 V	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	128 A <sup>2</sup> s 134 A <sup>2</sup> s
	T <sub>VJ</sub> = T <sub>VJM</sub> V <sub>R</sub> = 0 V	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	100 A <sup>2</sup> s 105 A <sup>2</sup> s
(di/dt) <sub>cr</sub>	T <sub>VJ</sub> = T <sub>VJM</sub> ; f = 50 Hz; t <sub>p</sub> = 200 µs	repetitive, I <sub>T</sub> = 20 A	100 A/µs
	V <sub>D</sub> = 2/3 V <sub>DRM</sub> I <sub>G</sub> = 0.15 A di <sub>G</sub> /dt = 0.15 A/µs	non repetitive, I <sub>T</sub> = I <sub>T(AV)M</sub>	500 A/µs
(dv/dt) <sub>cr</sub>	T <sub>VJ</sub> = T <sub>VJM</sub> ; V <sub>D</sub> = 2/3 V <sub>DRM</sub> R <sub>GK</sub> = ∞; method 1 (linear voltage rise)		500 V/µs
P <sub>GM</sub>	T <sub>VJ</sub> = T <sub>VJM</sub> ; t <sub>p</sub> = 30 µs I <sub>T</sub> = I <sub>T(AV)M</sub> ; t <sub>p</sub> = 300 µs	5 W 2.5 W 0.5 W	W
P <sub>GAV</sub>			
V <sub>RGM</sub>		10 V	V
T <sub>VJ</sub>		-40 ... +125	°C
T <sub>VJM</sub>		125	°C
T <sub>stg</sub>		-40 ... 125	°C
M <sub>d</sub>	Mounting torque with screw M3; TO-220 Mounting torque with screw M3.5; TO-220	0.45 Nm 0.55 Nm	Nm
Weight	typ.	2 g	g

Data according to IEC 60747

## Features

- SCR for frequency up to 400 Hz
- International standard package
- High performance glass passivated chip
- Long-term stability of leakage current and blocking voltage
- Epoxy meets UL 94V-0

## Applications

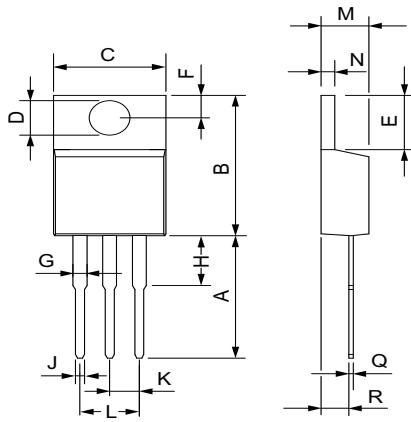
- Motor control
- Power converter
- AC power controller
- Light and temperature control
- SCR for inrush current limiting in power supplies or AC drive

## Advantages

- Space and weight savings
- Simple mounting

Symbol	Conditions	Characteristic Values	
		typ.	max.
$I_R, I_D$	$V_R = V_{RRM}; V_D = V_{DRM}; T_{VJ} = T_{VJM}$	5	mA
$V_T$	$I_T = 20 \text{ A}; T_{VJ} = 25^\circ\text{C}$	1.6	V
$V_{TO}$	For power-loss calculations only	0.85	V
$r_T$	$T_{VJ} = 125^\circ\text{C}$	27	$\text{m}\Omega$
$V_{GT}$	$V_D = 6 \text{ V}; T_{VJ} = 25^\circ\text{C}$ $T_{VJ} = -40^\circ\text{C}$	1.5	V
$I_{GT}$	$V_D = 6 \text{ V}; T_{VJ} = 25^\circ\text{C}$ $T_{VJ} = -40^\circ\text{C}$	28	mA
$I_{GD}$	$V_D = \frac{2}{3} V_{DRM}; T_{VJ} = T_{VJM}$	0.2	V
$I_{GD}$		3	mA
$I_L$	$t_p = 10 \mu\text{s}; T_{VJ} = 25^\circ\text{C}$ $I_G = 0.1 \text{ A}; dI_G/dt = 0.1 \text{ A}/\mu\text{s}$	75	mA
$I_H$	$V_D = 6 \text{ V}; R_{GK} = \infty; T_{VJ} = 25^\circ\text{C}$	50	mA
$t_{gd}$	$V_D = \frac{1}{2} V_{DRM}; T_{VJ} = 25^\circ\text{C}$ $I_G = 0.1 \text{ A}; dI_G/dt = 0.1 \text{ A}/\mu\text{s}$	2	$\mu\text{s}$
$R_{thJC}$	DC current	0.25	K/W
$R_{thJH}$	DC current		K/W
$a$	Max. acceleration; 50 Hz	50	$\text{m/s}^2$

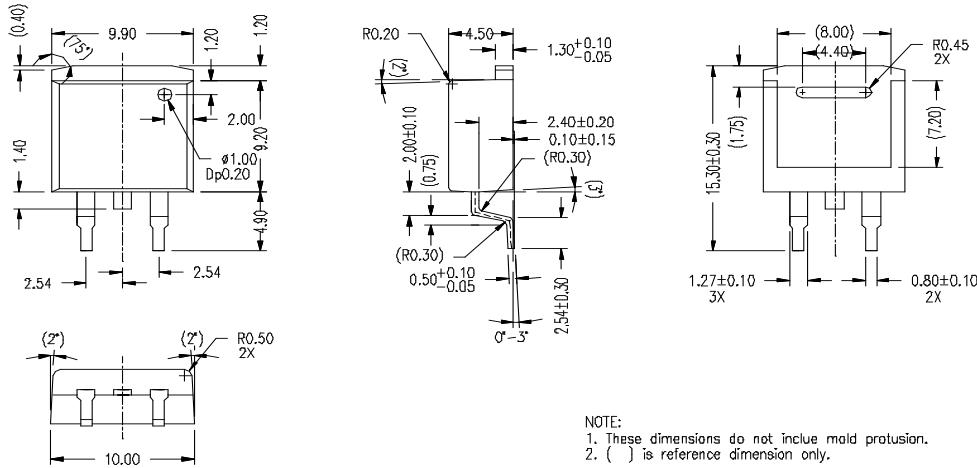
TO-220 AB



Dimensions (1 mm = 0.0394")

Dim.	Millimeter Min. Max.	Inches Min. Max.
A	12.70 13.97	0.500 0.550
B	14.73 16.00	0.580 0.630
C	9.91 10.66	0.390 0.420
D	3.54 4.08	0.139 0.161
E	5.85 6.85	0.230 0.270
F	2.54 3.18	0.100 0.125
G	1.15 1.65	0.045 0.065
H	2.79 5.84	0.110 0.230
J	0.64 1.01	0.025 0.040
K	2.54 BSC	0.100 BSC
M	4.32 4.82	0.170 0.190
N	1.14 1.39	0.045 0.055
Q	0.35 0.56	0.014 0.022
R	2.29 2.79	0.090 0.110

TO-263 AA



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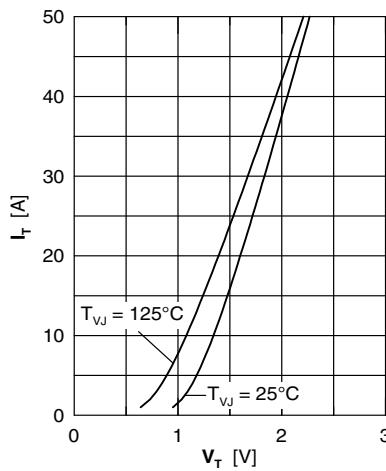


Fig. 1 Forward characteristics

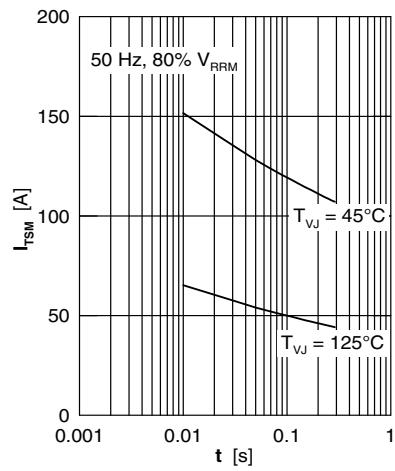
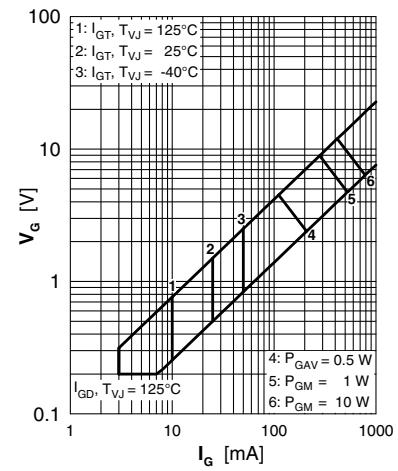
Fig. 2 Surge overload current  
 $I_{TSM}$ : crest value,  $t$ : duration

Fig. 3 Gate trigger range

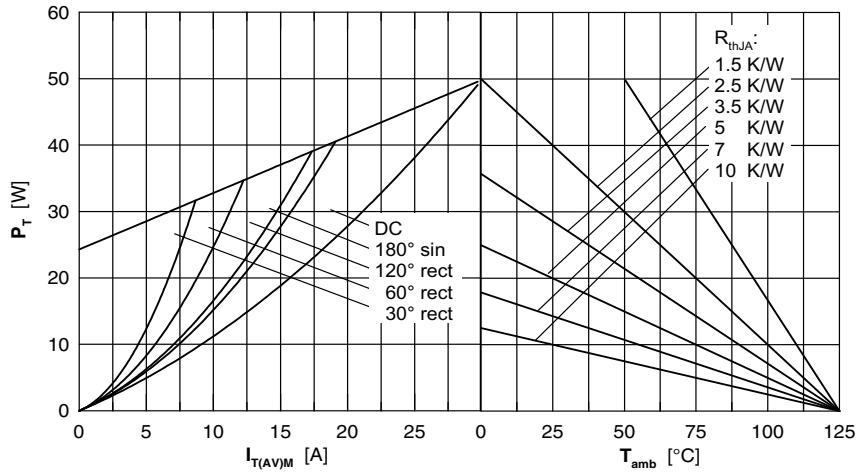


Fig. 4 Power dissipation versus forward current and ambient temperature

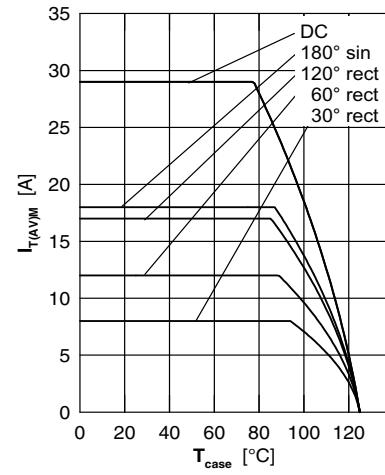


Fig. 5 Max. forward current at case temperature

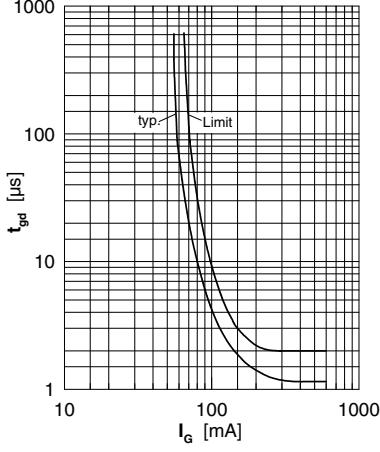


Fig. 6 Forward characteristics

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