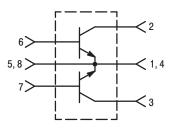
The RF Line NPN Silicon Push-Pull RF Power Transistor

Designed primarily for wideband large-signal output and driver amplifier stages in the 30 to 500 MHz frequency range.

- Specified 28 Volt, 400 MHz Characteristics Output Power = 125 W Typical Gain = 10 dB Efficiency = 55% (Typ)
- Built–In Input Impedance Matching Networks for Broadband Operation
- Push–Pull Configuration Reduces Even Numbered Harmonics
- Gold Metallization System for High Reliability
- 100% Tested for Load Mismatch
- Circuit board photomaster available upon request by contacting RF Tactical Marketing in Phoenix, AZ.



The MRF392 is two transistors in a single package with separate base and collector leads and emitters common. This arrangement provides the designer with a space saving device capable of operation in a push–pull configuration.

PUSH-PULL TRANSISTORS

MAXIMUM RATINGS

INFORMATION

ARCHIVE

Rating	Symbol	Value	Unit	
Collector–Emitter Voltage	V _{CEO}	30	Vdc	
Collector–Base Voltage	V _{CBO}	60	Vdc	
Emitter-Base Voltage	V _{EBO}	4.0	Vdc	
Collector Current — Continuous	Ι _C	16	Adc	
Total Device Dissipation @ $T_C = 25^{\circ}C$ (1) Derate above 25°C	P _D	270 1.54	Watts W/°C	
Storage Temperature Range	T _{stg}	-65 to +150	°C	
Junction Temperature	TJ	200	°C	

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	$R_{ extsf{ heta}JC}$	0.65	°C/W

NOTE:

1. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF push-pull amplifier.

REV 8

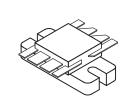
© Motorola, Inc. 1997

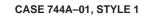
Downloaded from Elcodis.com electronic components distributor

125 W, 30 to 500 MHz CONTROLLED "Q" BROADBAND PUSH–PULL RF POWER TRANSISTOR

NPN SILICON

MRF392







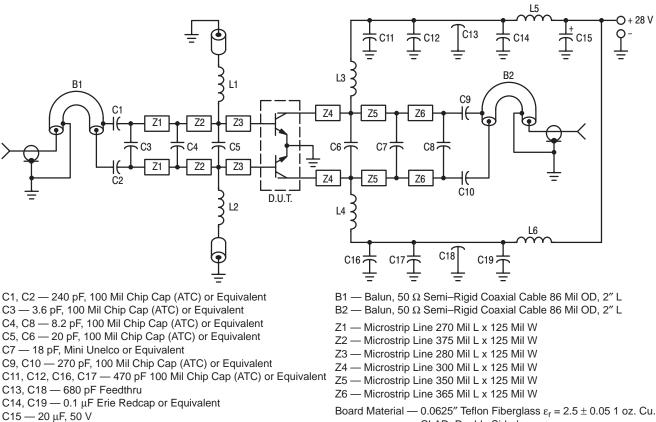
ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS (1)	· · ·		•		
Collector–Emitter Breakdown Voltage ($I_C = 50 \text{ mAdc}, I_B = 0$)	V _{(BR)CEO}	30	- I	-	Vdc
Collector–Emitter Breakdown Voltage ($I_C = 50 \text{ mAdc}, V_{BE} = 0$)	V _{(BR)CES}	60	_	_	Vdc
Emitter–Base Breakdown Voltage ($I_E = 5.0 \text{ mAdc}, I_C = 0$)	V _{(BR)EBO}	4.0	_	_	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, $I_E = 0$)	I _{CBO}	—	—	5.0	mAdc
ON CHARACTERISTICS (1)	· · ·				
DC Current Gain (I _C = 1.0 Adc, V _{CE} = 5.0 Vdc)	h _{FE}	40	60	100	_
DYNAMIC CHARACTERISTICS (1)	· · ·		•		
Output Capacitance (V_{CB} = 28 Vdc, I_E = 0, f = 1.0 MHz)	C _{ob}	_	75	95	pF
FUNCTIONAL TESTS (2) — See Figure 1	· · ·		•		
Common–Emitter Amplifier Power Gain (V _{CC} = 28 Vdc, P _{out} = 125 W, f = 400 MHz)	G _{pe}	8.0	10	-	dB
Collector Efficiency (V _{CC} = 28 Vdc, P _{out} = 125 W, f = 400 MHz)	η	50	55	-	%
Load Mismatch (V_{CC} = 28 Vdc, P_{out} = 125 W, f = 400 MHz, VSWR = 30:1, all phase angles)	ψ	No Degradation in Output Power			

NOTES:

1. Each transistor chip measured separately.

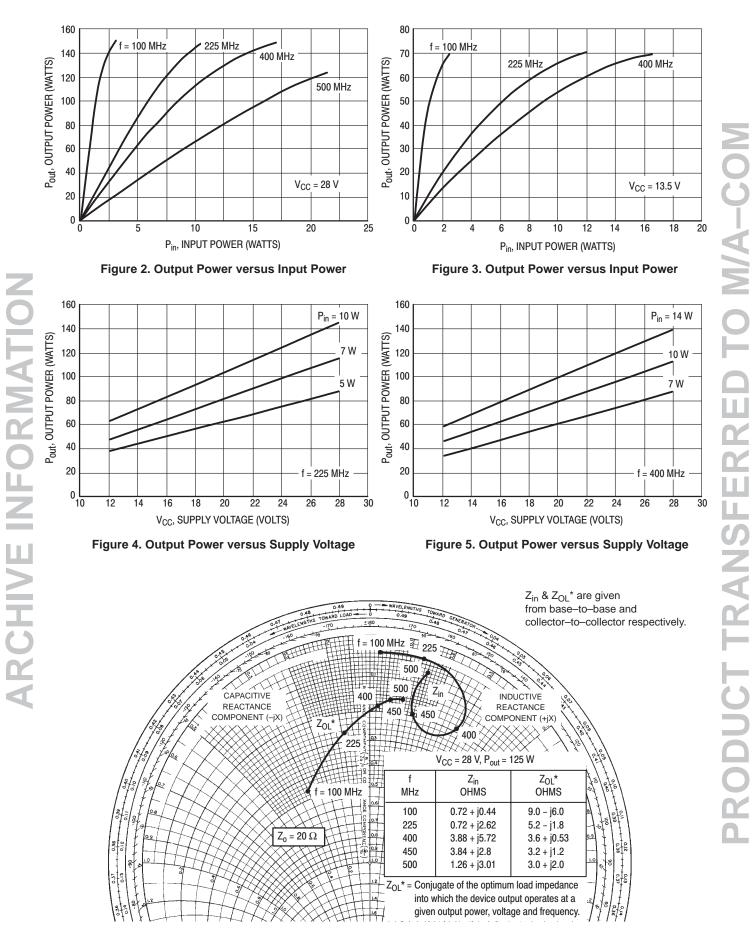
2. Both transistor chips operating in push-pull amplifier.



CLAD, Double Sided

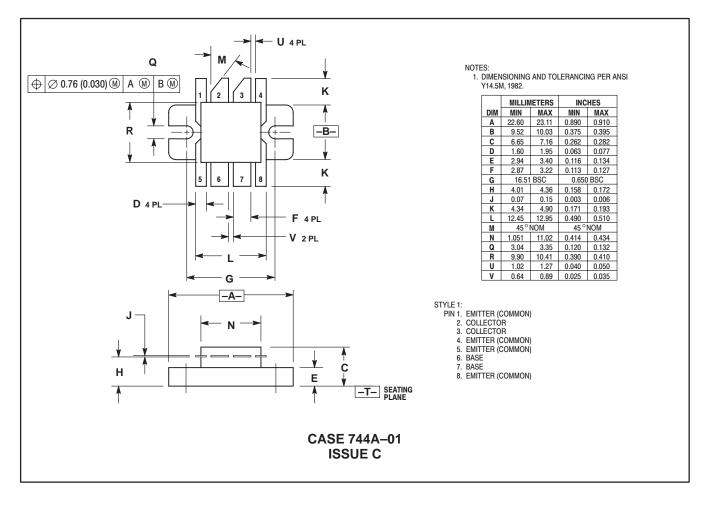
TO M/A-C(**PRODUCT TRANSFERRED**

L1, L2 — 0.15 μH Molded Choke With Ferrite Bead L3, L4 — 2–1/2 Turns #20 AWG, 0.200 ID L5, L6 — 3–1/2 Turns #18 AWG, 0.200 ID



MOTOROLA RF DEVICE DATA

PACKAGE DIMENSIONS



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