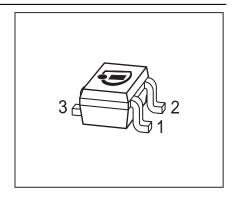


NPN Silicon RF Transistor*

- For broadband amplifiers up to 2 GHz and fast non-saturated switches at collector currents from 0.5 mA to 20 mA
- Complementary type: BFT92W (PNP)
- Pb-free (RoHS compliant) package 1)
- Qualified according AEC Q101
- * Short term description







ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Marking	Pin Configuration			Package
BFR92W	P1s	1=B	2=E	3=C	SOT323

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{\sf CEO}$	15	V
Collector-emitter voltage	V _{CES}	20	
Collector-base voltage	V_{CBO}	20	
Emitter-base voltage	V _{EBO}	2.5	
Collector current	IC	45	mA
Base current	I _B	4	
Total power dissipation ²⁾	P _{tot}	280	mW
_ <i>T</i> _S ≤ 86 °C			
Junction temperature	$ T_{j} $	150	°C
Ambient temperature	T_{A}	-65 150	
Storage temperature	$T_{ m stg}$	-65 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ³⁾	R _{thJS}	≤ 230	K/W

¹Pb-containing package may be available upon special request

 $^{{}^2}T_{\mbox{\scriptsize S}}$ is measured on the collector lead at the soldering point to the pcb

 $^{^3}$ For calculation of $R_{
m thJA}$ please refer to Application Note Thermal Resistance



Electrical Characteristics at $T_A = 25^{\circ}$ C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage	V _{(BR)CEO}	15	-	-	V
$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$					
Collector-emitter cutoff current	I _{CES}	-	-	10	μΑ
$V_{CE} = 20 \text{ V}, V_{BE} = 0$					
Collector-base cutoff current	I _{CBO}	-	-	100	nA
$V_{\text{CB}} = 10 \text{ V}, I_{\text{E}} = 0$					
Emitter-base cutoff current	I _{EBO}	-	-	100	μΑ
$V_{\rm EB} = 2.5 \rm V, I_{\rm C} = 0$					
DC current gain-	h _{FE}	70	100	140	-
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, pulse measured					



Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling	g)				
Transition frequency	f_{T}	3.5	5	-	GHz
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, f = 500 MHz					
Collector-base capacitance	C _{cb}	-	0.42	0.6	pF
$V_{\text{CB}} = 10 \text{ V}, f = 1 \text{ MHz}, V_{\text{BE}} = 0 ,$					
emitter grounded					
Collector emitter capacitance	C _{ce}	-	0.27	-	
$V_{CE} = 10 \text{ V}, f = 1 \text{ MHz}, V_{BE} = 0$,					
base grounded					
Emitter-base capacitance	C _{eb}	-	0.66	-	
$V_{EB} = 0.5 \text{ V}, f = 1 \text{ MHz}, V_{CB} = 0$,					
collector grounded					
Noise figure	F				dB
$I_{\rm C}$ = 2 mA, $V_{\rm CE}$ = 6 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
f = 900 MHz		-	1.4	-	
$I_{\rm C}$ = 2 mA, $V_{\rm CE}$ = 6 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
f = 1.8 GHz		-	2	-	
Power gain, maximum available ¹⁾	G _{ma}				
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$, $Z_{\rm L}$ = $Z_{\rm Lopt}$,					
f = 900 MHz		-	17	-	
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$, $Z_{\rm L}$ = $Z_{\rm Lopt}$,					
f = 1.8 GHz		-	11.5	-	
Transducer gain	S _{21e} ²				dB
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω ,					
f = 900 MHz		-	13.5	_	
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω ,					
f = 1.8 MHz		-	8	-	

 $^{^{1}}G_{\text{ma}} = |S_{21e}/S_{12e}| (k-(k^{2}-1)^{1/2})$



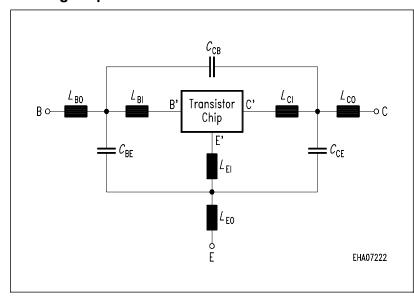
SPICE Parameter (Gummel-Poon Model, Berkley-SPICE 2G.6 Syntax):

Transistor Chip Data:

IS =	0.1213	fA	BF =	94.733	-	NF =	1.0947	-
VAF =	30	V	IKF =	0.46227	Α	ISE =	129.55	fΑ
NE =	1.9052	-	BR =	10.729	-	NR =	0.8983	-
VAR =	14.599	V	IKR =	0.01	Α	ISC =	0.75557	fΑ
NC =	1.371	-	RB =	14.998	Ω	IRB =	0.01652	mΑ
RBM =	7.8145	Ω	RE =	0.29088	-	RC =	0.13793	Ω
CJE =	10.416	fF	VJE =	0.70618	V	MJE =	0.34686	-
TF =	26.796	ps	XTF =	0.3817	-	VTF =	0.32861	V
ITF =	4.4601	mA	PTF =	0	deg	CJC =	946.47	fF
VJC =	0.84079	V	MJC =	0.4085	-	XCJC =	0.13464	-
TR =	1.2744	ns	CJS =	0	fF	VJS =	0.75	V
MJS =	0	-	XTB =	0	-	EG =	1.11	eV
XTI =	3	-	FC =	0.99545		TNOM	300	K

All parameters are ready to use, no scalling is necessary. Extracted on behalf of Infineon Technologies AG by: Institut für Mobil- und Satellitentechnik (IMST)

Package Equivalent Circuit:



L _{BI} =	0.57	nΗ
L _{BO} =	0.4	nΗ
L _{EI} =	0.43	nΗ
L _{EO} =	0.5	nΗ
L _{CI} =	0	nΗ
L _{CO} =	0.41	nΗ
C _{BE} =	61	fF
C _{CB} =	101	fF
C _{CE} =	175	fF
Valid up	to 6GHz	

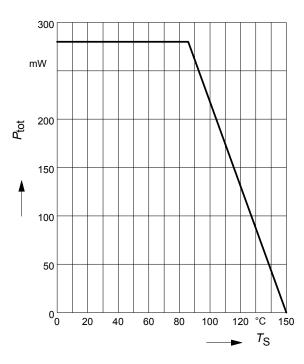
For examples and ready to use parameters please contact your local Infineon Technologies distributor or sales office to obtain a Infineon Technologies CD-ROM or see Internet: http://www.infineon.com

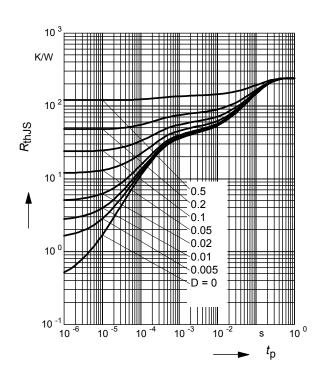
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Total power dissipation $P_{tot} = f(T_S)$

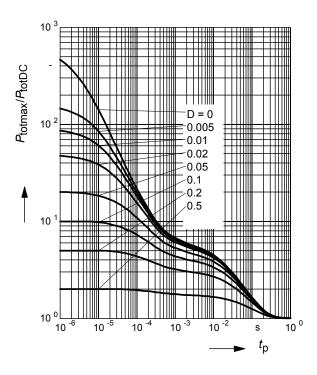
Permissible Pulse Load $R_{thJS} = f(t_p)$





Permissible Pulse Load

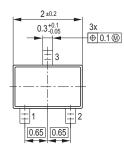
$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_{\text{p}})$$

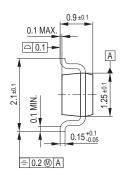




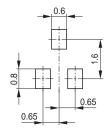
Package Outline



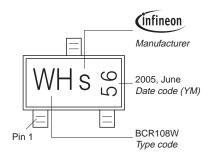




Foot Print

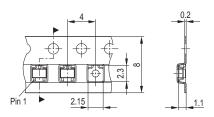


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel





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