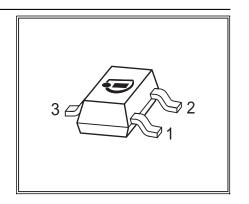


NPN Silicon RF Transistor

- For linear broadband amplifier application up to 500 MHz
- SAW filter driver in TV tuners
- Pb-free (RoHS compliant) package 1)
- Qualified according AEC Q101







Туре	Marking	Piı	n Configura	tion	Package
BF799	LKs	1 = B	2 = E	3 = C	SOT23

Maximum Ratings

Parameter	Symbol	Value	Unit	
Collector-emitter voltage	V _{CEO}	20	V	
Collector-emitter voltage	V _{CES}	30		
Collector-base voltage	V _{CBO}	30		
Emitter-base voltage	V _{EBO}	3		
Collector current			mA	
Peak collector current,	/ _{CM}	50		
Peak base current	/ _{BM}	15		
Total power dissipation	P _{tot}	280	mW	
$T_{S} \le 69 ^{\circ}\text{C}^{2}$				
Junction temperature	T _i	150	°C	
Storage temperature	$T_{\rm stg}$	-65 150		
Thermal Resistance			•	

Junction - soldering point³⁾

¹Pb-containing package may be available upon special request

K/W

≤ 290

 R_{thJS}

²T_S is measured on the collector lead at the soldering point to the pcb

 $^{^3}$ For calculation of $R_{
m thJA}$ please refer to Application Note Thermal Resistance

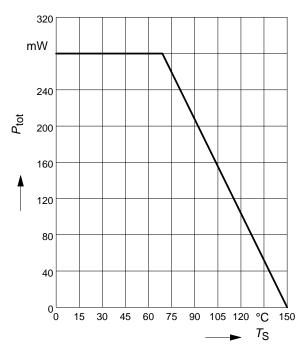


Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC characteristics					
Collector-emitter breakdown voltage	V _{(BR)CEO}	20	-	-	V
$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$					
Collector-base breakdown voltage	V _{(BR)CBO}	30	-	-	
$I_{\rm C} = 10 \ \mu A, \ I_{\rm E} = 0$					
Base-emitter breakdown voltage	$V_{(BR)EBO}$	3	-	-	
$I_{\rm E} = 10 \ \mu \text{A}, \ I_{\rm C} = 0$					
Collector-base cutoff current	/ _{CBO}	-	-	100	nA
$V_{\rm CB} = 20 \text{ V}, I_{\rm E} = 0$					
DC current gain	h _{FE}				-
$I_{\rm C} = 5 \text{ mA}, \ V_{\rm CE} = 10 \text{ V}$		35	95	-	
$I_{\rm C} = 20 \text{ mA}, \ V_{\rm CE} = 10 \text{ V}$		40	100	250	
Collector-emitter saturation voltage	V _{CEsat}	-	0.1	0.3	V
$I_{\rm C} = 20 \text{ mA}, I_{\rm B} = 2 \text{ mA}$					
Base-emitter saturation voltage	V _{BEsat}	-	-	0.95	
$I_{\rm C} = 20 \text{ mA}, I_{\rm B} = 2 \text{ mA}$					
AC characteristics					
Transition frequency	f _T				MHz
$I_{\rm C} = 5 \text{ mA}, \ V_{\rm CE} = 10 \text{ V}, \ f = 100 \text{ MHz}$		-	800	-	
$I_{\rm C} = 20 \text{ mA}, \ V_{\rm CE} = 8 \text{ V}, \ f = 100 \text{ MHz}$		-	1100	-	
Output capacitance	Cob	-	0.96	-	pF
$V_{CB} = 10 \text{ V}, I_{E} = 0 \text{ mA}, f = 1 \text{ MHz}$					
Collector-base capacitance	C _{cb}	-	0.7	-	
$V_{CB} = 10 \text{ V}, f = 1 \text{ MHz}$					
Collector-emitter capacitance	C _{ce}	-	0.28	-	
$V_{CE} = 10 \text{ V}, f = 1 \text{ MHz}$					
Noise figure	F	-	3	-	dB
$I_{\rm C} = 5 \text{ mA}, \ V_{\rm CE} = 10 \text{ V}, \ f = 100 \text{ MHz},$					
$Z_{ m S}$ = 50 Ω					
Output conductance	<i>g</i> 22e	-	60	-	μS
$I_{\rm C} = 20 \text{ mA}, \ V_{\rm CE} = 10 \text{ V}, \ f = 35 \text{ MHz}$					



Total power dissipation $P_{tot} = f(T_S)$



Permissible Pulse Load $R_{\text{thJS}} = f(t_{\text{p}})$

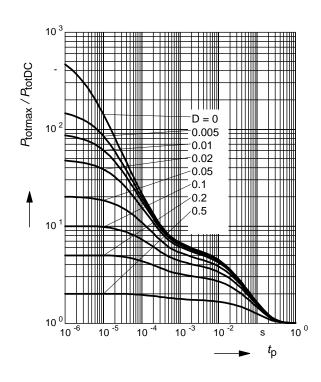
K/W 0.5 0.5 0.2 0.1 0.005 0.002 0.001 0.0005

10⁻⁵ 10⁻⁴ 10⁻³

10 ⁻²

Permissible Pulse Load

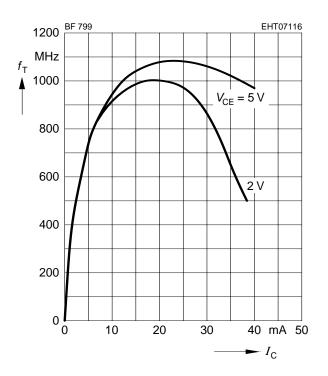
$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_{p})$$



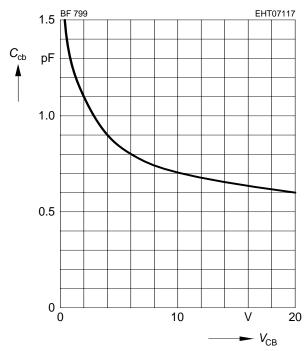


Transition frequency $f_T = f(I_C)$

f = 100MHz



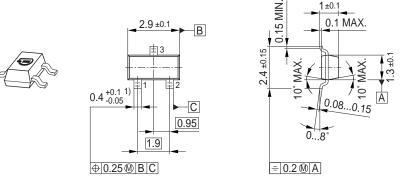
Collector-base capacitance $C_{CD} = f(V_{CB})$ f = 1 MHz



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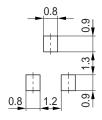


Package Outline

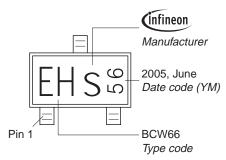


1) Lead width can be 0.6 max. in dambar area

Foot Print

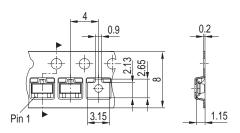


Marking Layout (Example)



Standard Packing

Reel Ø180 mm = 3.000 Pieces/Reel Reel Ø330 mm = 10.000 Pieces/Reel



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