

4V Drive Pch MOSFET

RRH075P03

●Structure

Silicon P-channel MOSFET

●Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (SOP8).

●Application

Switching

●Packaging specifications

Type	Package	Taping
	Code	TB
	Basic ordering unit (pieces)	2500
RRH075P03		○

●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit	
Drain-source voltage	V _{bss}	-30	V	
Gate-source voltage	V _{GSS}	±20	V	
Drain current	Continuous	I _D	±7.5	A
	Pulsed	I _{DP} *1	±30	A
Source current (Body Diode)	Continuous	I _S	-1.6	A
	Pulsed	I _{SP} *1	-30	A
Total power dissipation	P _D *2	2.0	W	
Channel temperature	T _{ch}	150	°C	
Range of storage temperature	T _{stg}	-55 to +150	°C	

*1 P_w ≤ 10μs, Duty cycle ≤ 1%

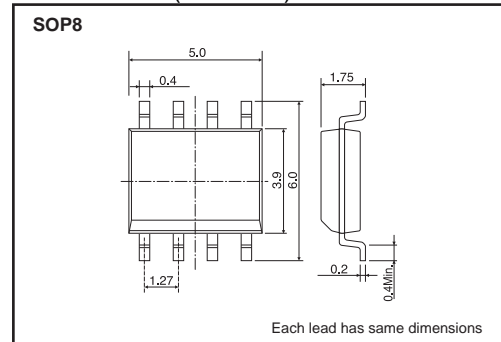
*2 Mounted on a ceramic board.

●Thermal resistance

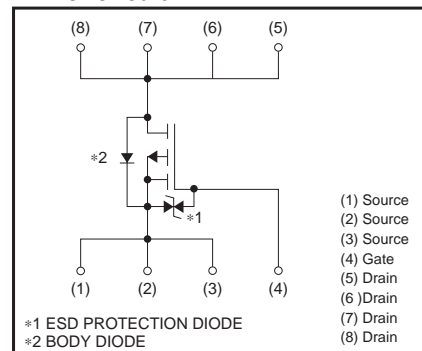
Parameter	Symbol	Limits	Unit
Channel to Ambient	R _{th (ch-a)} *	62.5	°C / W

* Mounted on a ceramic board.

●Dimensions (Unit : mm)



●Inner circuit



●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Gate-source leakage	I _{GSS}	–	–	±10	μA	V _{GS} =±20V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR)DSS}	–30	–	–	V	I _D =–1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	–	–	–1	μA	V _{DS} =–30V, V _{GS} =0V
Gate threshold voltage	V _{GS(th)}	–1.0	–	–2.5	V	V _{DS} =–10V, I _D =–1mA
Static drain-source on-state resistance	R _{DS(on)} *	–	15	21	mΩ	I _D =–7.5A, V _{GS} =–10V
		–	22	31		I _D =–4A, V _{GS} =–4.5V
		–	25	35		I _D =–4A, V _{GS} =–4.0V
Forward transfer admittance	Y _{fs} *	9	–	–	S	I _D =–7.5A, V _{DS} =–10V
Input capacitance	C _{iss}	–	1900	–	pF	V _{DS} =–10V
Output capacitance	C _{oss}	–	250	–	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	–	250	–	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	–	14	–	ns	I _D =–4A, V _{DD} = –15V
Rise time	t _r *	–	25	–	ns	V _{GS} =–10V
Turn-off delay time	t _{d(off)} *	–	100	–	ns	R _L =3.8Ω
Fall time	t _f *	–	70	–	ns	R _G =10Ω
Total gate charge	Q _g *	–	21	–	nC	I _D =–7.5A, V _{DD} = –15V V _{GS} =–5V
Gate-source charge	Q _{gs} *	–	5	–	nC	R _L =2.0Ω
Gate-drain charge	Q _{gd} *	–	7	–	nC	R _G =10Ω

*Pulsed

●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward Voltage	V _{SD} *	–	–	–1.2	V	I _S =–7.5A, V _{GS} =0V

*Pulsed

●Electrical characteristics curves

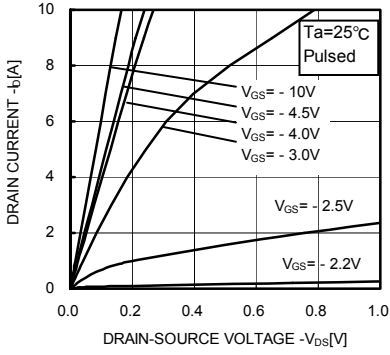


Fig.1 Typical output characteristics(I)

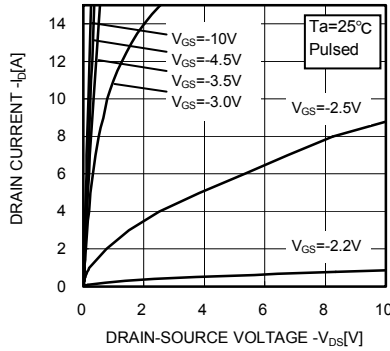


Fig.2 Typical output characteristics(II)

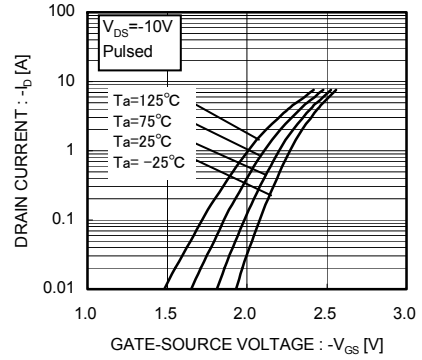


Fig.3 Typical Transfer Characteristics

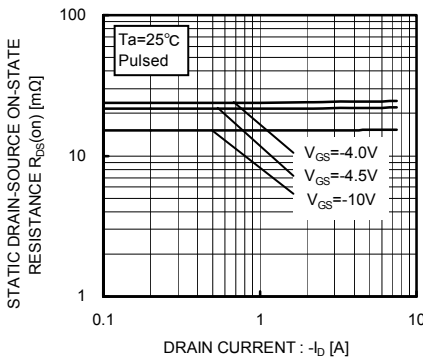


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current(I)

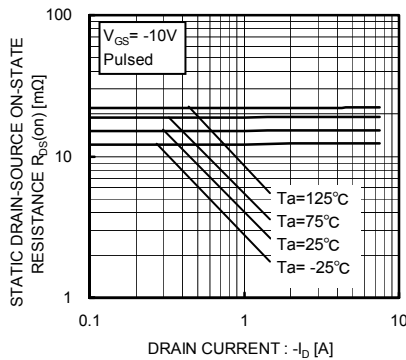


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current(II)

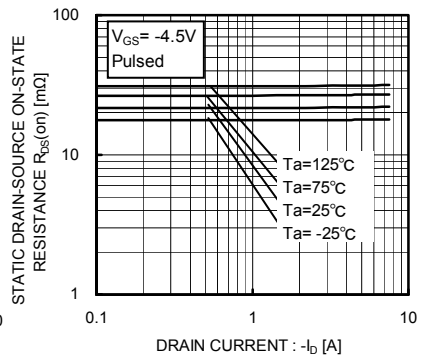


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current(III)

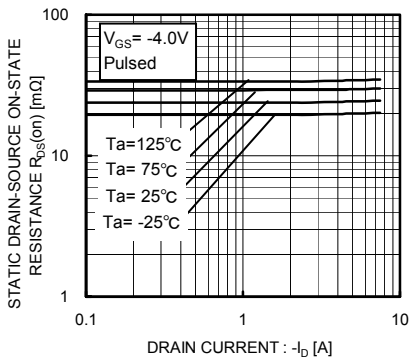


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current(IV)

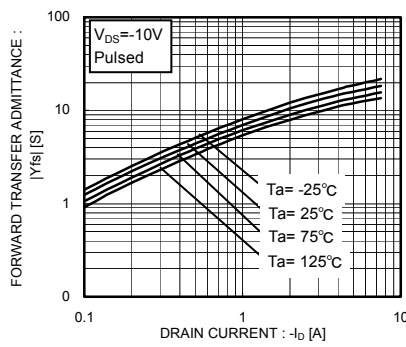


Fig.8 Forward Transfer Admittance vs. Drain Current

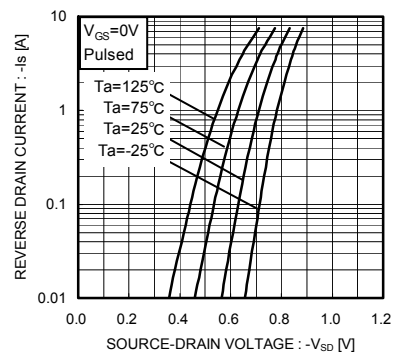


Fig.9 Reverse Drain Current vs. Source-Drain Voltage

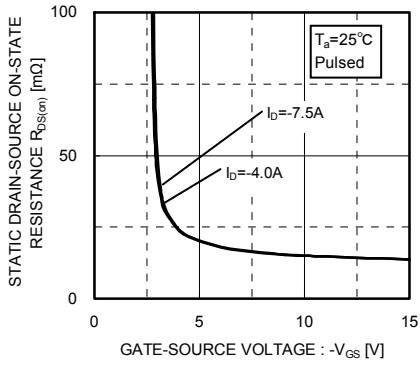


Fig.10 Static Drain-Source On-State Resistance vs. Gate Source Voltage

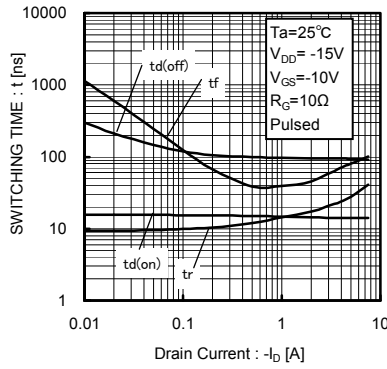


Fig.11 Switching Characteristics

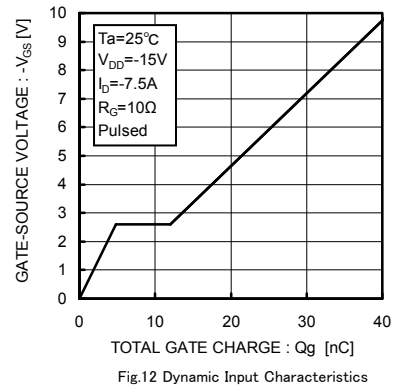


Fig.12 Dynamic Input Characteristics

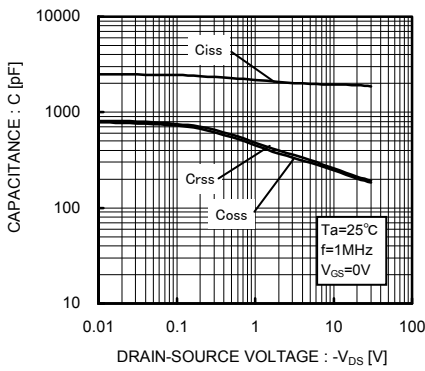


Fig.13 Typical Capacitance vs. Drain-Source Voltage

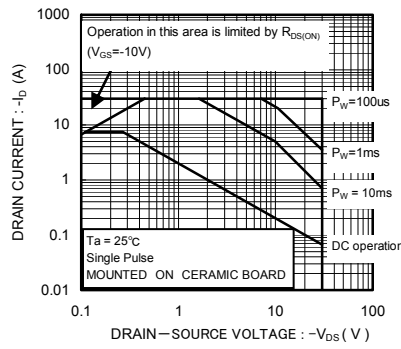


Fig.14 Maximum Safe Operating Area

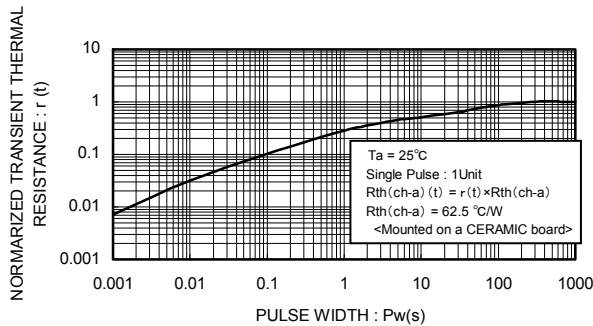


Fig.15 Normalized Transient Thermal Resistance vs. Pulse Width

●Measurement circuit

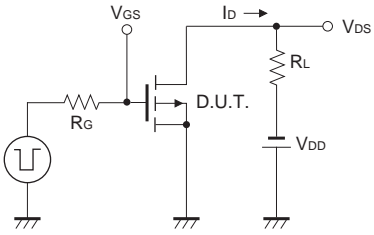


Fig.1-1 Switching Time Measurement Circuit

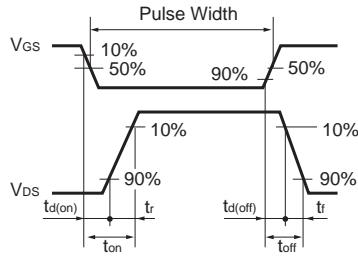


Fig.1-2 Switching Waveforms

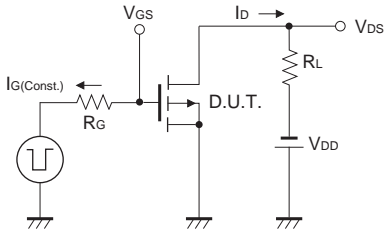


Fig.2-1 Gate Charge Measurement Circuit

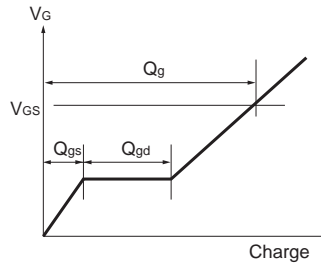


Fig.2-2 Gate Charge Waveform

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