

J304

N-Channel RF Amplifier

- This device is designed for electronic switching applications such as low ON resistance analog switching.
- Sourced from process 50.



1. Drain 2. Source 3. Gate

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings* T_C=25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DG}	Drain-Gate Voltage	30	V
V_{GS}	Gate-Source Voltage	-30	V
I _{GF}	Forward Gate Current	10	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

^{*} This ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

$\textbf{Electrical Characteristics} \ \, \textbf{T}_{\text{C}} = 25 ^{\circ} \text{C unless otherwise noted}$

Parameter	Test Condition	Min.	Тур.	Max.	Units
teristics					
Gate-Source Breakdwon Voltage	$I_G = -1.0 \mu A, V_{DS} = 0$	-30			V
Gate Reverse Current	$V_{GS} = -20V, V_{DS} = 0$			-100	pА
Gate-Source Cutoff Voltage	$V_{DS} = 15V, I_{D} = 1.0nA$	-2.0		-6.0	V
teristics					
Zero-Gate Voltage Drain Current	V _{DS} = 15V, VGS = 0	5.0		15	mA
Forward Transconductance	$V_{GS} = 0V, V_{DS} = 15V, f = 1KHz$	4500		7500	μS
Output Conductance	$V_{GS} = 0V, V_{DS} = 15V, f = 1KHz$			50	μS
	cteristics Gate-Source Breakdwon Voltage Gate Reverse Current Gate-Source Cutoff Voltage teristics Zero-Gate Voltage Drain Current Forward Transconductance				

Thermal Characteristics TA=25°C unless otherwise noted

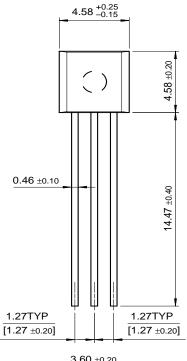
Symbol	Parameter	Max.	Units
P _D	Total Device Dissipation	350	mW
	Derate above 25°C	2.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

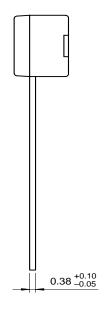
^{*} Device mounted on FR-4 PCB 1.5" × 1.6" × 0.06"

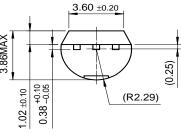
These rating are based on a maximum junction temperature of 150 degrees C.
 These are steady limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Package Dimensions

TO-92







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