

DATA SHEET

SMP1340-040LF: 0402 Surface Mount PIN Diode

Applications

- WLAN, WiMAX, cellular handsets
- Cellular infrastructure
- RFID readers
- Test instruments
- High isolation switches
- Series diode switches



Features

- Low total capacitance: 0.3 pF maximum @ 5 V
- Low-series resistance: 1.2 Ω maximum @ 10 mA
- Industry-standard 0402 footprint
- Package rated MSL1, 260 °C per JEDEC J-STD-020

NEW



Skyworks offers lead (Pb)-free, RoHS (Restriction of Hazardous Substances)-compliant packaging.

Description

The SMP1340-040LF is a surface mountable PIN diode, excellent for use in switch applications from 10 MHz to more than 3 GHz.

Maximum resistance at 10 mA is 1.2 Ω and maximum capacitance at 5 V is 0.3 pF. The combination of low capacitance, low parasitic inductance, and nominal 7 μm I-region width, makes the SMP1340-040LF useful in fast, high isolation switch applications.

Design information for high power switches may be found in the Skyworks Application Note, *Design With PIN Diodes* (document number 200312).

Table 1. SMP1340-040LF Absolute Maximum Ratings

Parameter	Symbol	Minimum	Maximum	Units
Forward current	I _F		150	mA
Reverse voltage	V _R		50	V
Dissipated power @ 25 °C	P _D		750	mW
Storage temperature	T _{STG}	-55	+200	°C
Junction temperature	T _J	-55	+175	°C
Solder interface temperature	T _S	-40	+85	°C

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Table 2. SMP1340-040LF Electrical Specifications (Note 1)
(T_S = +25 °C, Characteristic Impedance [Z₀] = 50 Ω, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Forward voltage	V _F	I _F = 10 mA		0.85		V
Reverse leakage current	I _R	V _R = 50 V			10	μA
Series resistance	R _{S1}	f = 100 MHz				
	R _{S5}	I _F = 1 mA		1.70		Ω
	R _{S10}	I _F = 5 mA I _F = 10 mA		1.00 0.85	2.00 1.20	Ω Ω
Series inductance	L _S			0.45		nH
Total capacitance	C _{T5}	V _R = 5 V, f = 1 MHz		0.21	0.30	pF
Minority carrier lifetime	T _L	I _F = 10 mA		100		ns
I-region width	W			7		μm

Note 1: Performance is guaranteed only under the conditions listed in this Table.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SMP1340-040LF are provided in Table 1. Electrical specifications are provided in Table 2.

Typical performance characteristics of the SMP1340-040LF are illustrated in Figures 1 and 2.

Package Dimensions

The PCB layout footprint for the SMP1340-040LF is provided in Figure 3. Typical case markings are shown in Figure 4. Package dimensions for the SMP1340-040LF are provided in Figure 5. Tape and reel dimensions are provided in Figure 6.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SMP1340-040LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

Typical Performance Characteristics

($T_A = 25\text{ }^\circ\text{C}$, Unless Otherwise Noted)

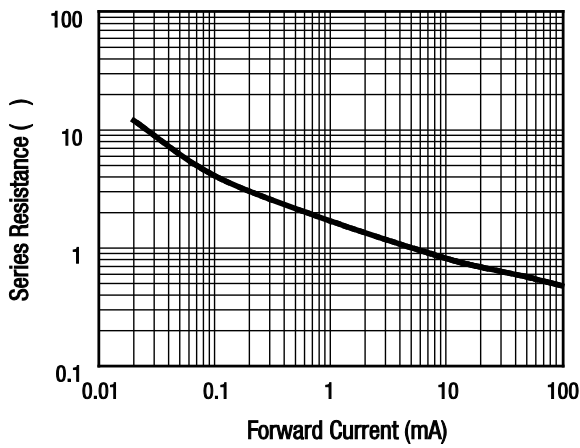


Figure 1. Series Resistance vs Current @ 100 MHz

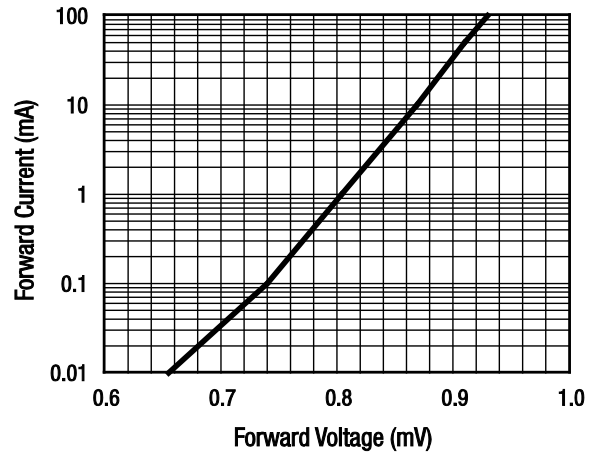
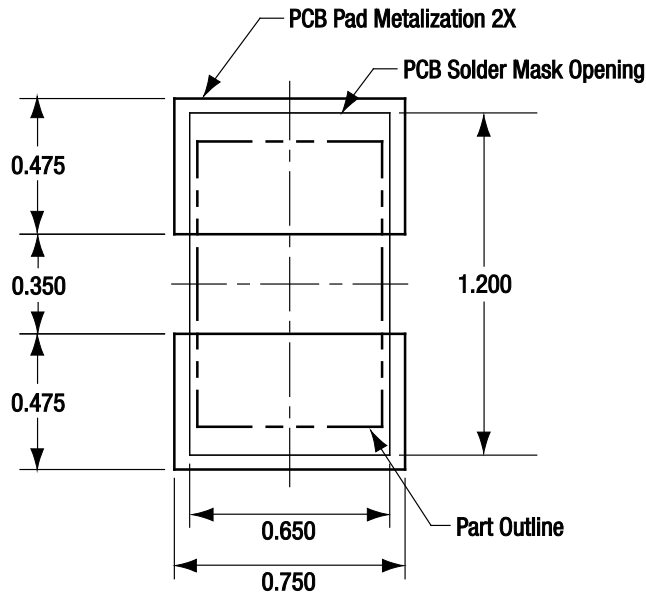


Figure 2. DC Characteristics



All measurements in millimeters

S1997

Figure 3. SMP1340-040LF PCB Layout Footprint

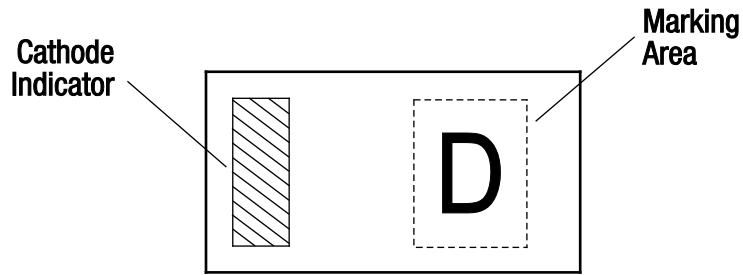
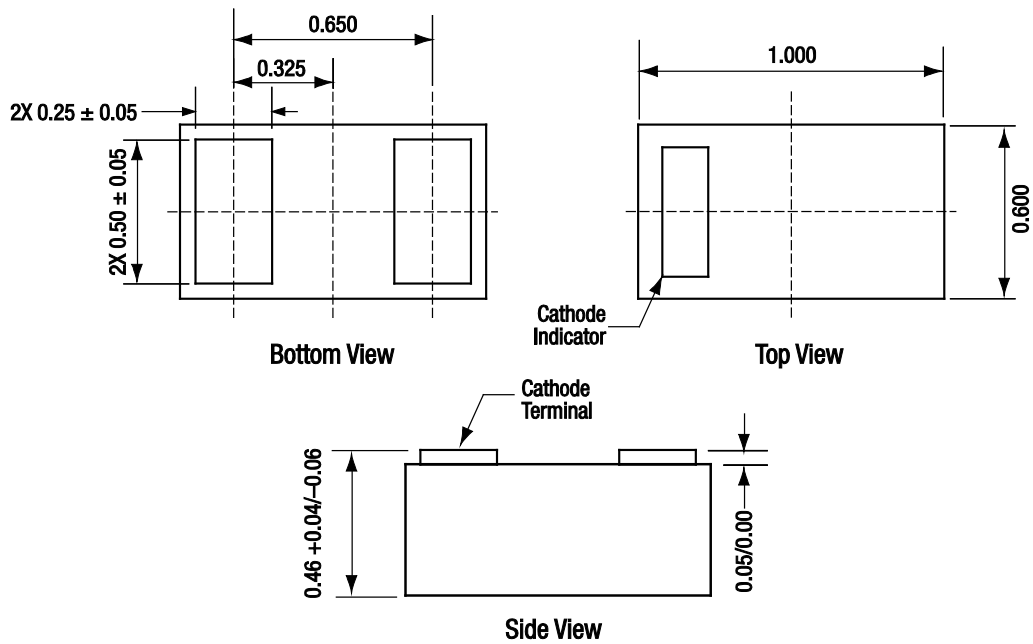


Figure 4. SMP1340-040LF Typical Case Markings (Top View)



All dimensions in millimeters

S1882

Figure 5. SMP1340-040LF Package Dimensions

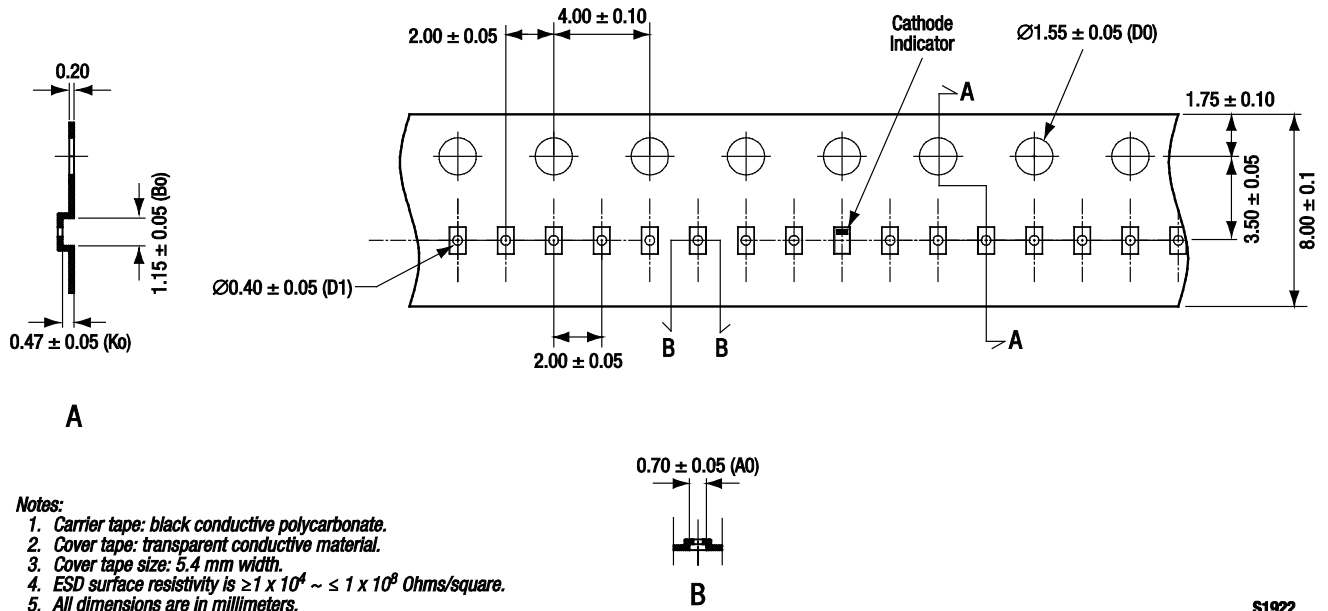


Figure 6. SMP1340-040LF Tape and Reel Dimensions

S1922

Ordering Information

Model Name	Manufacturing Part Number
SMP1340-040LF Surface Mount PIN Diode	SMP1340-040LF

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