

Micro Commercial Components



Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311 Phone: (818) 701-4933 (818) 701-4939 Fax:

SI2301

Features

- -20V,-2.8A, R_{DS(ON)}=120m Ω @V_{GS}=-4.5V $R_{DS(ON)}$ =150m $\Omega @V_{GS}$ =-2.5V
- High dense cell design for extremely low R_{DS(ON)}
- Rugged and reliable
- High Speed Switching
- SOT-23 Package
- Marking Code: S1
- Epoxy meets UL 94 V-0 flammability rating

Internal Block Diagram

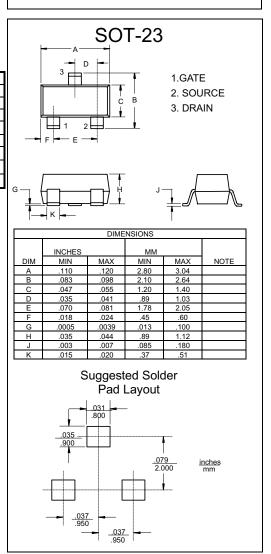
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Moisture Sensitivity Level 1 Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter	Rating	Unit
V _{DS}	Drain-source Voltage	-20	V
ID	Drain Current-Continuous	-2.8	А
IDM	Drain Current-Pulsed ^a	-10	А
V _{GS}	Gate-source Voltage	±8	V
PD	Total Power Dissipation	1.25	W
R _{☉JA}	Thermal Resistance Junction to Ambient ^b	100	°C/W
TJ	Operating Junction Temperature	-55 to +150	°C
T _{STG}	Storage Temperature	-55 to +150	°C

P-Channel Enhancement Mode Field Effect Transistor



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Revision: A

2011/01/01



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Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

$V_{GS} = 0V, I_D = -250\mu A$ $V_{DS} = -20V, V_{GS} = 0V$ $V_{GS} = 8V, V_{DS} = 0V$ $V_{GS} = -8V, V_{DS} = 0V$ $V_{GS} = -8V, V_{DS} = 0V$ $V_{GS} = -8V, V_{DS} = 0V$ $V_{GS} = -4.5V, I_D = -2.8A$ $V_{DS} = -2.5V, I_D = -2.8A$		80	-1 100 -100	V µA nA nA
$V_{DS} = -20V, V_{GS} = 0V$ $V_{GS} = 8V, V_{DS} = 0V$ $V_{GS} = -8V, V_{DS} = 0V$ $V_{GS} = -8V, V_{DS} = 0V$ $V_{GS} = -8V, V_{DS} = -250\mu$ $V_{GS} = -4.5V, I_{D} = -2.8A$ $V_{GS} = -2.5V, I_{D} = -2.0A$	A -0.45	80	100	μA nA nA
$V_{DS} = -20V, V_{GS} = 0V$ $V_{GS} = 8V, V_{DS} = 0V$ $V_{GS} = -8V, V_{DS} = 0V$ $V_{GS} = -8V, V_{DS} = 0V$ $V_{GS} = -8V, V_{DS} = -250\mu$ $V_{GS} = -4.5V, I_{D} = -2.8A$ $V_{GS} = -2.5V, I_{D} = -2.0A$		80	100	nA nA
$\begin{array}{c c} R & V_{GS} = -8V, V_{DS} = 0V \\ \hline \\ & V_{GS} = V_{DS}, I_D = -250\mu \lambda \\ \hline \\ & V_{GS} = -4.5V, I_D = -2.8A \\ \hline \\ & V_{GS} = -2.5V, I_D = -2.0A \end{array}$		80		nA
$V_{GS} = -8V, V_{DS} = 0V$ $V_{GS} = V_{DS}, I_D = -250\mu \lambda$ $V_{GS} = -4.5V, I_D = -2.8A$ $V_{GS} = -2.5V, I_D = -2.0A$		80	-100	
$V_{GS} = V_{DS}, I_D = -250 \mu A$ $V_{GS} = -4.5V, I_D = -2.8A$ $V_{GS} = -2.5V, I_D = -2.0A$		80		V
$V_{GS} = -4.5V, I_D = -2.8A$ $V_{GS} = -2.5V, I_D = -2.0A$		80		V
$V_{GS} = -4.5V, I_D = -2.8A$ $V_{GS} = -2.5V, I_D = -2.0A$		80		v
V _{GS} = -2.5V, I _D = -2.0A			120	mΩ
		110	150	mΩ
		8		S
	-1			L
		880		pF
$V_{DS} = -6V, V_{GS} = 0V,$ f = 1.0 MHz		270		pF
1 = 1.0 WHZ		175		pF
		11	20	ns
		5	10	ns
$V_{GS} = -4.5V, R_{GEN} = 6\Omega$		32	65	ns
		23	45	ns
		11	14.5	nC
		1.5		nC
VGS 4.0V		2.1		nC
un Ratings				
			-0.75	A
V _{GS} = 0V, I _S = -0.75A			-1.2	V
	$V_{DD} = -6V, I_D = -1A, V_{GS} = -4.5V, R_{GEN} = 6\Omega$	$V_{DD} = -6V, I_D = -1A, V_{GS} = -4.5V, R_{GEN} = 6\Omega$	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $

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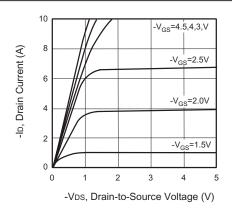


Figure 1. Output Characteristics

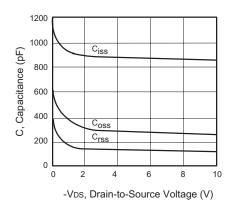


Figure 3. Capacitance

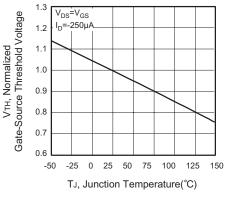
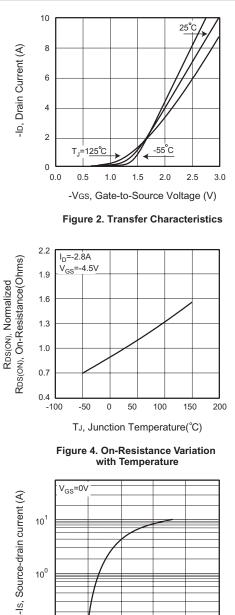


Figure 5. Gate Threshold Variation with Temperature



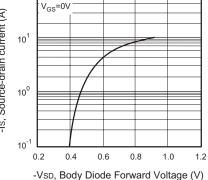


Figure 6. Body Diode Forward Voltage Variation with Source Current

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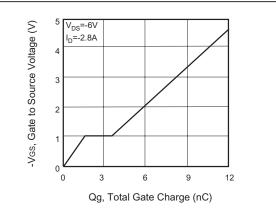


Figure 7. Gate Charge

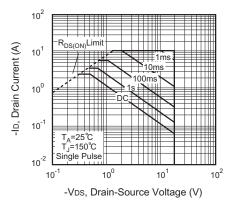


Figure 8. Maximum Safe Operating Area

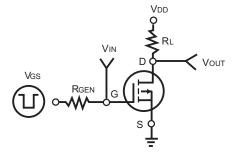


Figure 9. Switching Test Circuit

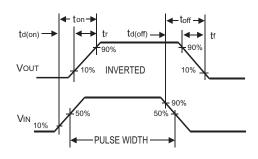


Figure 10. Switching Waveforms

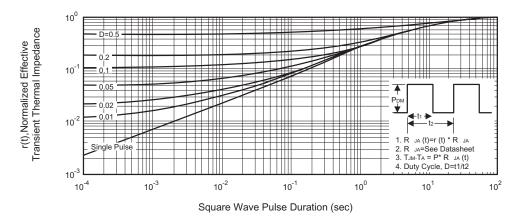


Figure 11. Normalized Thermal Transient Impedance Curve

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Ordering Information :

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

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