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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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FS70SM-2

High-Speed Switching Use Nch Power MOS FET

REJ03G1431-0200

(Previous: MEJ02G0110-0101)

Rev.2.00 Aug 07, 2006

Features

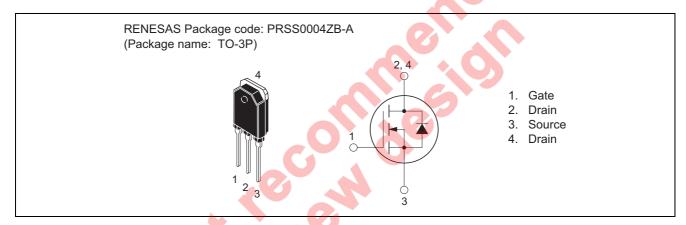
Drive voltage : 10 VV_{DSS} : 100 V

• $r_{DS(ON) (max)}$: 20 m Ω

• I_D: 70 A

• Integrated Fast Recovery Diode (TYP.): 120 ns

Outline



Applications

Motor control, Lamp control, Solenoid control, DC-DC converters, etc.

Maximum Ratings

 $(Tc = 25^{\circ}C)$

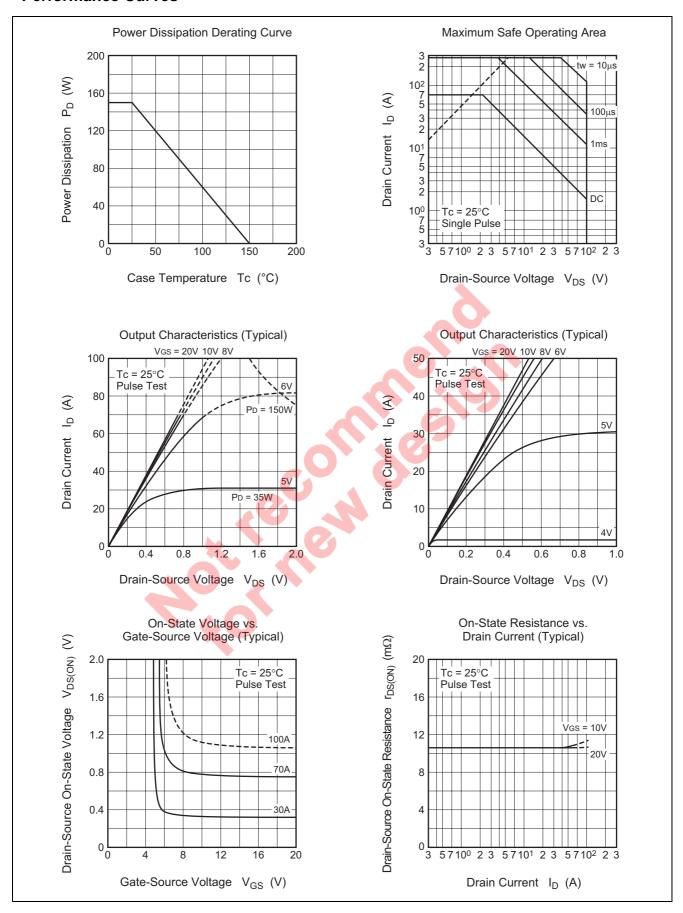
Parameter	Symbol	Ratings	Unit	Conditions
Orain-source voltage V _{DSS}		100	V	V _{GS} = 0 V
Gate-source voltage	V_{GSS}	±20	V	V _{DS} = 0 V
Drain current	I_D	70	Α	
Drain current (Pulsed)	I _{DM}	280	Α	
Avalanche drain current (Pulsed)	I _{DA}	70	Α	L = 100 μH
Source current	Is	70	Α	
Source current (Pulsed)	I _{SM}	280	Α	
Maximum power dissipation	P_D	150	W	
Channel temperature	Tch	- 55 to +150	°C	
Storage temperature	Tstg	- 55 to +150	°C	
Mass	_	4.8	g	Typical value

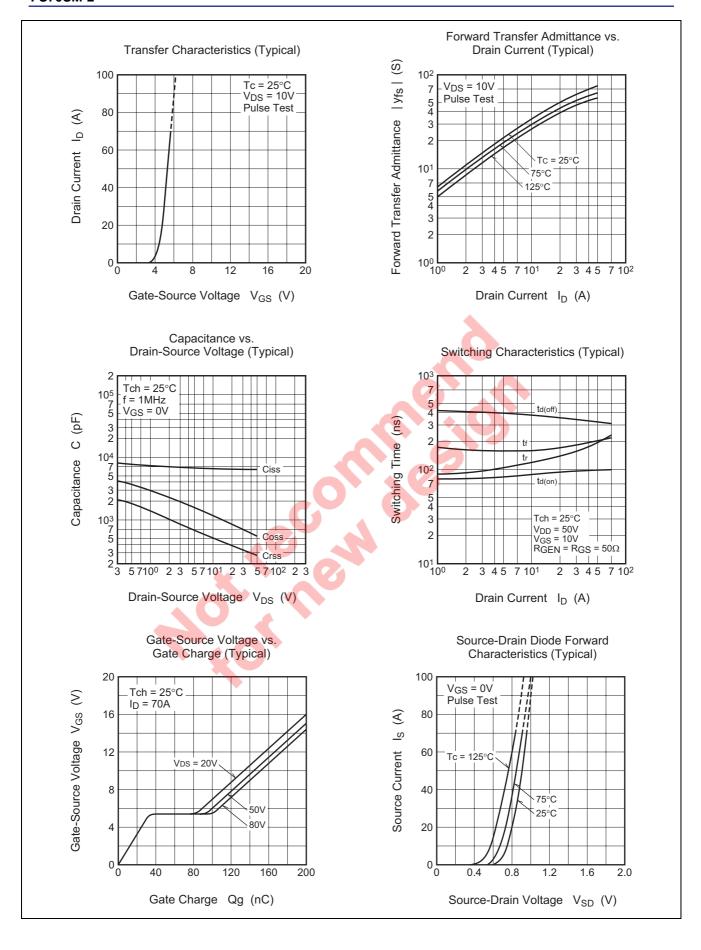
Electrical Characteristics

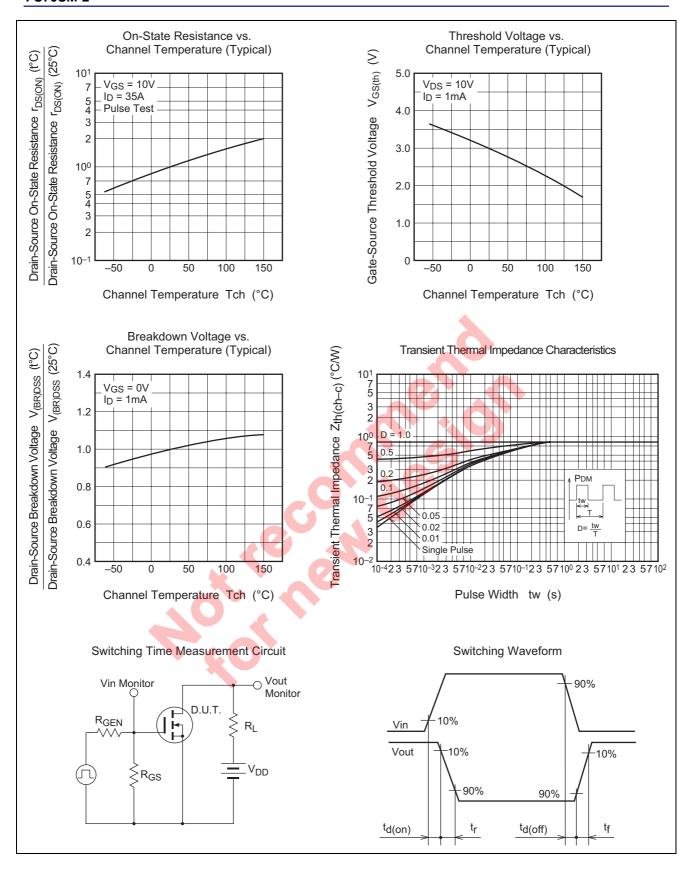
 $(Tch = 25^{\circ}C)$

Parameter	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain-source breakdown voltage	V _{(BR)DSS}	100	_	_	V	$I_D = 1 \text{ mA}, V_{GS} = 0 \text{ V}$
Gate-source leakage current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$
Drain-source leakage current	I _{DSS}		_	0.1	mA	V _{DS} = 100 V, V _{GS} = 0 V
Gate-source threshold voltage	V _{GS(th)}	2.0	3.0	4.0	V	I _D = 1 mA, V _{DS} = 10 V
Drain-source on-state resistance	r _{DS(ON)}	_	14	20	mΩ	$I_D = 35 \text{ A}, V_{GS} = 10 \text{ V}$
Drain-source on-state voltage	V _{DS(ON)}	_	0.49	0.7	V	I _D = 35 A, V _{GS} = 10 V
Forward transfer admittance	yfs	_	53	_	S	I _D = 35 A, V _{DS} = 10 V
Input capacitance	Ciss		6540	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V},$
Output capacitance	Coss		1150	_	pF	f = 1MHz
Reverse transfer capacitance	Crss		500	_	pF	
Turn-on delay time	t _{d(on)}	_	95	_	ns	$V_{DD} = 50 \text{ V}, I_D = 35 \text{ A},$
Rise time	t _r	_	175	_	ns	V _{GS} = 10 V,
Turn-off delay time	t _{d(off)}	_	330	_	ns	$R_{GEN} = R_{GS} = 50 \Omega$
Fall time	t _f		190	_	ns	
Source-drain voltage	V_{SD}		1.0	1.5	V	I _S = 35 A, V _{GS} = 0 V
Thermal resistance	R _{th(ch-c)}		_	0.83	°C/W	Channel to case
Reverse recovery time	t _{rr}	_	120		ns	$I_S = 70 \text{ A}, d_{is}/d_t = -100 \text{ A/}\mu\text{s}$
B			31			

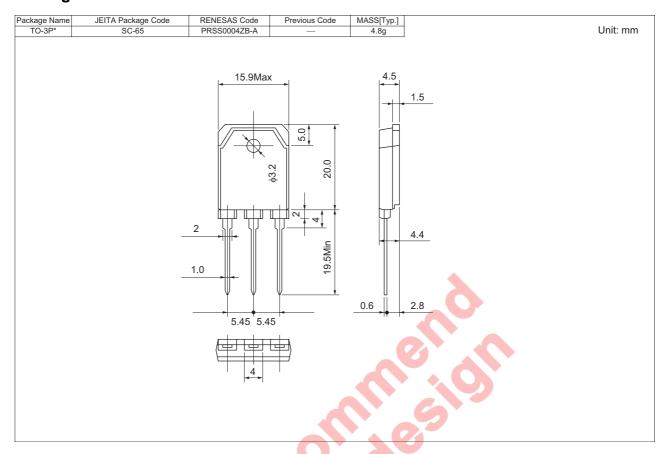
Performance Curves







Package Dimensions



Order Code

Lead form	Standard packi <mark>ng</mark>	Quantity	Standard order code	Standard order code example
Straight type	Static electricity prevention bag	20	Type name	FS70SM-2
Lead form	Plastic Magazine (Tube)	30	Type name – Lead forming code	FS70SM-2-A8

Note: Please confirm the specification about the shipping in detail.

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