TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSIII)

2SK3017

DC-DC Converter, Relay Drive and Motor Drive Applications

• Low drain-source ON resistance : $R_{DS(ON)} = 1.05 \Omega$ (typ.)

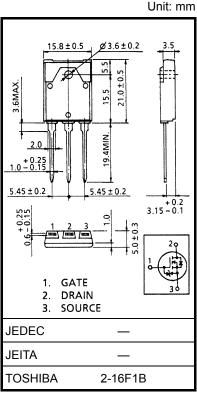
• High forward transfer admittance : $|Y_{fS}| = 7.0 \text{ S (typ.)}$

• Low leakage current : $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 720 \text{ V)}$

• Enhancement mode : $V_{th} = 2.0 \text{ to } 4.0 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

| Characteris | stics | Symbol | Rating | Unit |
|-------------------------|------------------------|------------------|------------|------|
| Drain-source voltage | | V_{DSS} | 900 | V |
| Drain-gate voltage (Ro | _{SS} = 20 kΩ) | V_{DGR} | 900 | V |
| Gate-source voltage | | V _{GSS} | ±30 | V |
| Drain current | DC (Note 1) | ΙD | 8.5 | Α |
| | Pulse (Note 1) | I _{DP} | 25.5 | Α |
| Drain power dissipation | n (Tc = 25°C) | P_{D} | 90 | W |
| Single pulse avalanche | e energy (Note 2) | E _{AS} | 966 | mJ |
| Avalanche current | | I _{AR} | 8.5 | Α |
| Repetitive avalanche e | nergy (Note 3) | E _{AR} | 9 | mJ |
| Channel temperature | | T _{ch} | 150 | °C |
| Storage temperature ra | ange | T _{stg} | -55 to 150 | °C |



Weight: 5.8 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

| Characteristics | Symbol | Max | Unit |
|--|------------------------|------|--------|
| Thermal resistance, channel to case | R _{th (ch-c)} | 1.39 | °C / W |
| Thermal resistance, channel to ambient | R _{th (ch-a)} | 41.6 | °C / W |

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 24.5 mH, R_G = 25 Ω , I_{AR} = 8.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

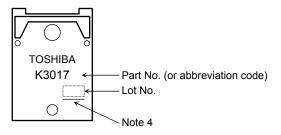
Electrical Characteristics (Ta = 25°C)

| Charac | eteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|-----------------|----------------------|---|-----|------|------|---------|
| Gate leakage cu | rrent | I _{GSS} | V _{GS} = ±30 V, V _{DS} = 0 V | | _ | ±10 | μΑ |
| Gate-source bre | eakdown voltage | V (BR) GSS | I _G = ±10 μA, V _{DS} = 0 V | ±30 | _ | _ | V |
| Drain cut-off cu | rrent | I _{DSS} | V _{DS} = 720 V, V _{GS} = 0 V | _ | _ | 100 | μΑ |
| Drain-source br | eakdown voltage | V (BR) DSS | I _D = 10 mA, V _{GS} = 0 V | 900 | _ | _ | V |
| Gate threshold v | oltage | V_{th} | V _{DS} = 10 V, I _D = 1 mA | 2.0 | _ | 4.0 | V |
| Drain-source O | N resistance | R _{DS} (ON) | V _{GS} = 10 V, I _D = 4 A | _ | 1.05 | 1.25 | Ω |
| Forward transfer | admittance | Y _{fs} | V _{DS} = 15 V, I _D = 4 A | 3.5 | 7.0 | _ | S |
| Input capacitano | е | C _{iss} | | | 2150 | _ | pF |
| Reverse transfer capacitance | | C _{rss} | V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz | _ | 35 | _ | |
| Output capacitance | | Coss | | _ | 220 | _ | |
| Switching time | Rise time | t _r | $V_{GS} \stackrel{10 \text{ V}}{\circ} V_{OUT}$ $V_{GS} \stackrel{10 \text{ V}}{\circ} V_{OUT}$ $R_{L} = 100 \Omega$ $V_{DD} = 400 \text{ V}$ | _ | 25 | _ | |
| | Turn-on time | t _{on} | | _ | 60 | _ | ne |
| | Fall time | t _f | | l | 25 | ı | ns |
| | Turn-off time | t _{off} | Duty $\leq 1\%$, $t_{\rm W} = 10 \mu{\rm s}$ | | 120 | | |
| Total gate charge (Gate-source plus gate-drain) | | Qg | V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 8 A | | 70 | _ | |
| Gate-source charge | | Q _{gs} | | | 37 | _ | nC - |
| Gate-drain ("miller") charge | | Q _{gd} | | | 33 | _ | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|------------------|--|-----|------|------|------|
| Continuous drain reverse current (Note 1) | I _{DR} | _ | _ | _ | 8.5 | Α |
| Pulse drain reverse current (Note 1) | I _{DRP} | _ | _ | _ | 25.5 | Α |
| Forward voltage (diode) | V_{DSF} | I _{DR} = 8.5 A, V _{GS} = 0 V | _ | _ | -1.9 | V |
| Reverse recovery time | t _{rr} | I _{DR} = 8.5 A, V _{GS} = 0 V | 1 | 1300 | 1 | ns |
| Reverse recovery charge | Q _{rr} | dl _{DR} / dt = 100 Å / μs | _ | 14.5 | _ | μC |

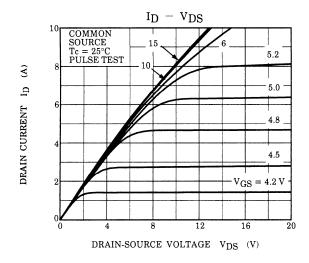
Marking

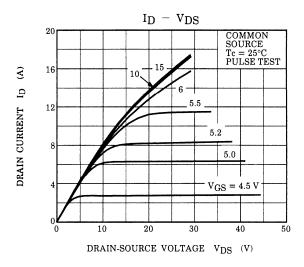


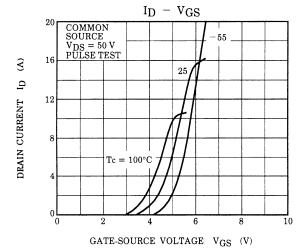
Note 4: A line under a Lot No. identifies the indication of product Labels.

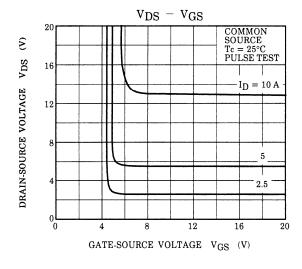
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

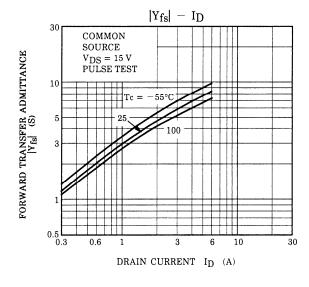
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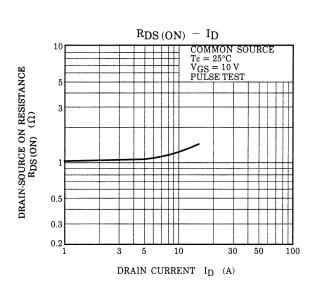


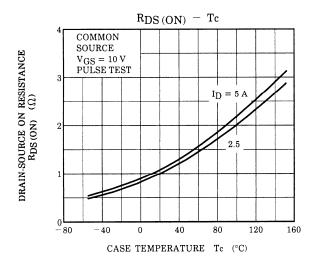


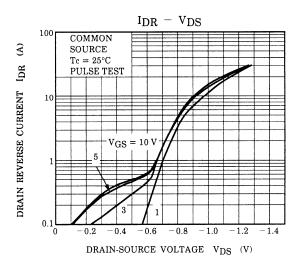


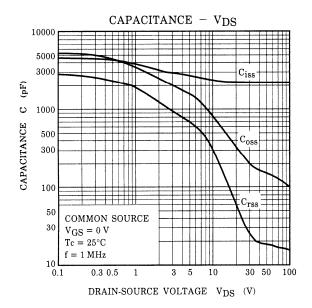


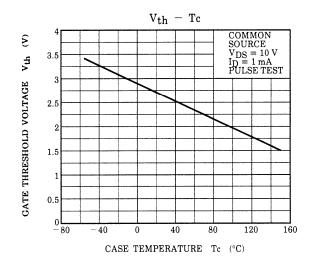


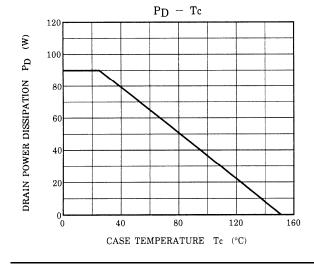


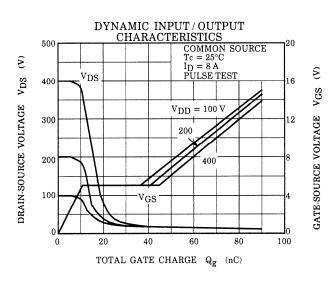




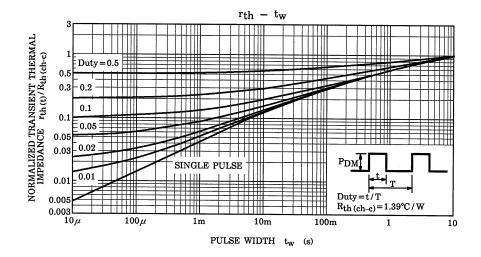


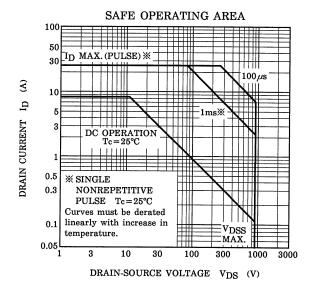


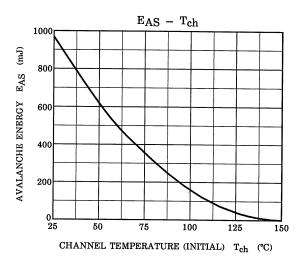


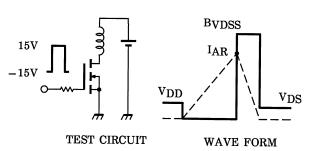


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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 24.5~mH \end{aligned} \qquad E_{AS} &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right) \end{aligned}$$

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