TOSHIBA Field Effect Transistor Silicon N Channel MOS Type  $(\pi\text{-MOSII}^{5})$ 

# 2SK1359

### DC-DC Converter and Motor Drive Applications

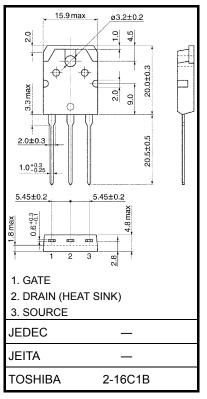
Unit: mm

• Low drain-source ON resistance :  $RDS (ON) = 3.0 \Omega (typ.)$ • High forward transfer admittance :  $|Y_{fs}| = 2.0 S (typ.)$ • Low leakage current :  $IDSS = 300 \mu A (max) (VDS = 800 V)$ 

• Enhancement mode :  $V_{th} = 1.5 \text{ to } 3.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$ 

## Absolute Maximum Ratings (Ta = 25°C)

Characteris	etics	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	1000	V	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	1000	V	
Gate-source voltage		$V_{GSS}$	±30	V	
Drain current	DC (Note 1)	ΙD	5	А	
	Pulse (Note 1)	I <sub>DP</sub>	15		
Drain power dissipation (Tc = 25°C)		$P_{D}$	125	W	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.0	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

This transistor is an electrostatic-sensitive device.

Please handle with caution.

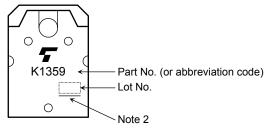
#### **Electrical Characteristics (Ta = 25°C)**

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	eakage current $I_{GSS}$ $V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$		_	_	±50	nA	
		I <sub>DSS</sub>	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = 0 V	_	_	300	μA
Drain-source br	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	1000	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	-	3.5	V
Drain-source O	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2 A	_	3.0	3.8	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 2 A	1.0	2.0	_	S
Input capacitano	ce	C <sub>iss</sub>		_	700	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0V, f = 1 MHz		55	_	pF
Output capacitance		Coss		_	100	_	
Switching time	Rise time	t <sub>r</sub>	V <sub>GS</sub> <sub>0V</sub> V <sub>OUT</sub> <sub>R<sub>L</sub></sub> =200Ω	_	18	_	
	Turn-on time	t <sub>on</sub>			30	_	20
	Fall time	t <sub>f</sub>		_	12	_	ns ns
	Turn-off time	t <sub>off</sub>	$V_{\mathrm{DD}} = 400 \mathrm{V}$ $\mathrm{Duty} \leq 1\%, \ t_W = 10 \mu \mathrm{s}$	_	70	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	60	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 4 \text{ A}$	_	35	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>	]		25	_	

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	15	Α
Forward voltage (diode)	$V_{DSF}$	I <sub>DR</sub> = 4 A, V <sub>GS</sub> = 0 V	-		-1.9	V

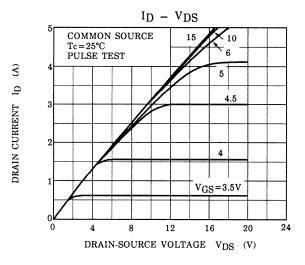
#### Marking

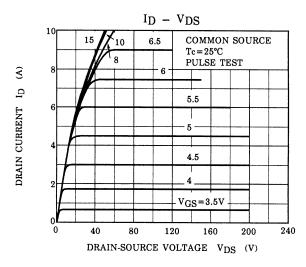


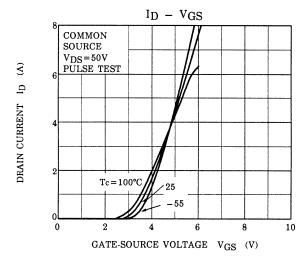
Note 2: A line under a Lot No. identifies the indication of product Labels.

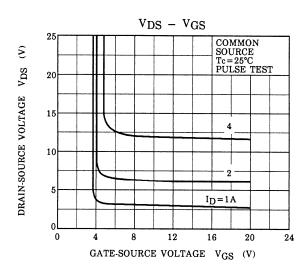
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

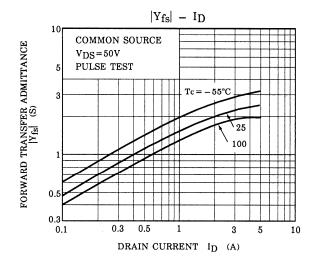
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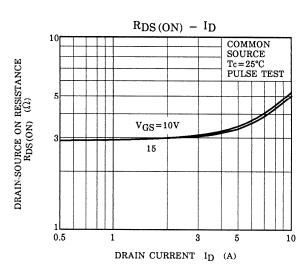




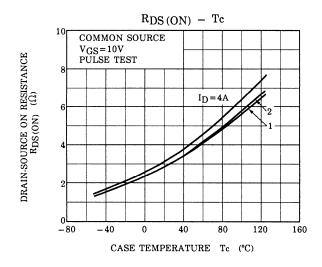


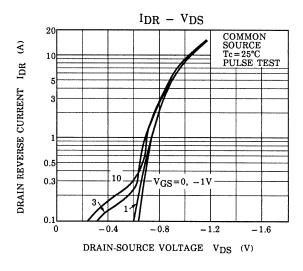


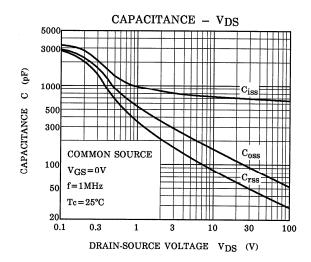


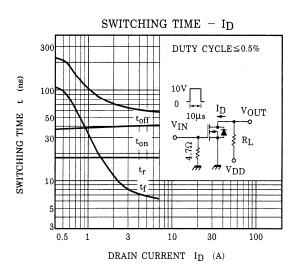


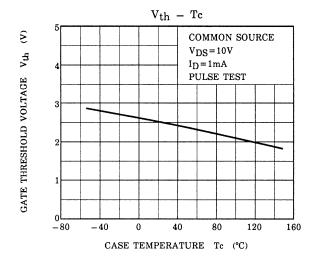
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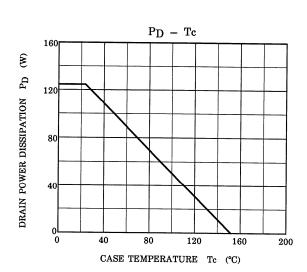


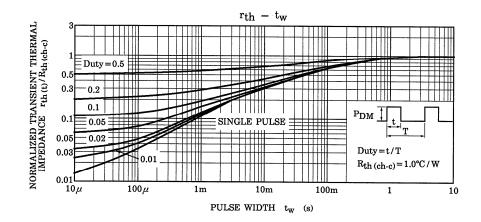


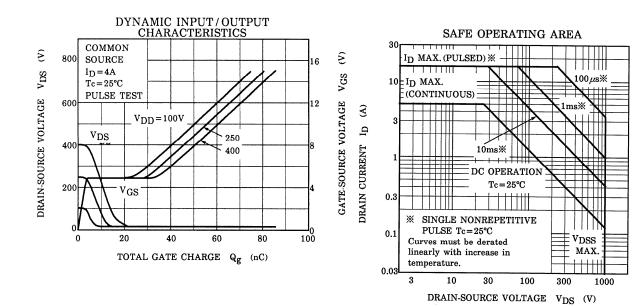












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