TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

# **TK15X60U**

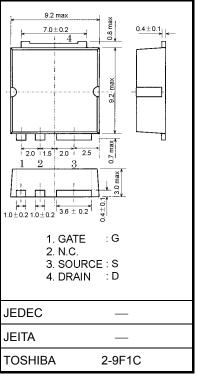
### **Switching Regulator Applications**

Unit: mm

- Low drain-source ON resistance: RDS (ON) = 0.24  $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 8.5 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = 100 \mu A (max) (V_{DS} = 600 V)$
- Enhancement-mode:  $V_{th} = 3.0 \text{ to } 5.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	600	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	15		
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	30	А	
Drain power dissipation (Tc = 25°C)		PD	125	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	81	mJ	
Avalanche current		I <sub>AR</sub>	11	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	12.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 0.74 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

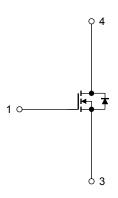
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.0	°C/W

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 1.17 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 11 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



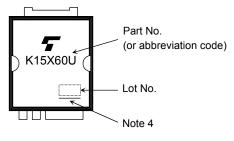
## **Electrical Characteristics (Ta = 25°C)**

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curr	ent	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off curre	nt	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source brea	kdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_	_	V
Gate threshold vo	Itage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	3.0	_	5.0	٧
Drain-source ON	resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.5 A		0.24	0.30	Ω
Forward transfer a	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 7.5 A	2.1	8.5		S
Input capacitance		C <sub>iss</sub>			950		
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		47		pF
Output capacitance		C <sub>oss</sub>			2300		
Switching time	Rise time	t <sub>r</sub>	$\begin{array}{c c} 10 \text{ V} \\ \text{VGS} \\ 0 \text{ V} \\ \hline \\ 50  \Omega \\ \end{array} \begin{array}{c} \text{I}_D = 7.5 \text{ A} \\ \text{V}_{OUT} \\ \hline \\ \text{V}_{DD} \approx 300 \text{ V} \\ \end{array}$ Duty $\leq$ 1%, $t_W = 10  \mu\text{s}$	_	37	_	ns
	Turn-ON time	t <sub>on</sub>			80		
	Fall time	t <sub>f</sub>			8		
	Turn-OFF time	t <sub>off</sub>		_	105	_	
Total gate charge		Qg		_	17	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$	_	10	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	7	_	

## **Source-Drain Ratings and Characteristics (Ta = 25°C)**

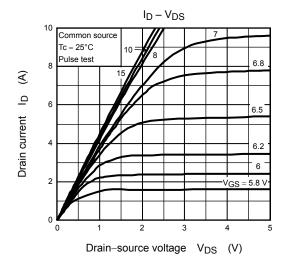
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	15	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	30	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 15 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 15 \text{ A}, V_{GS} = 0 \text{ V},$	_	530	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	9.0	_	μС

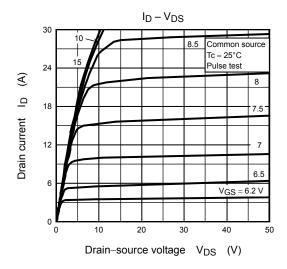
## Marking

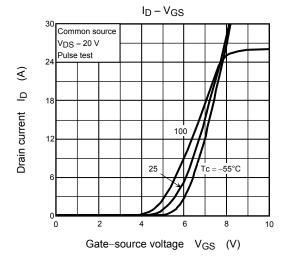


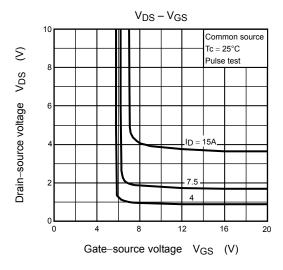
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

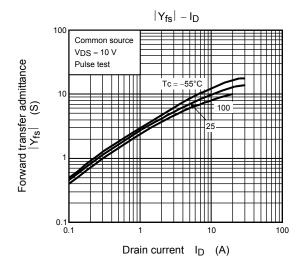
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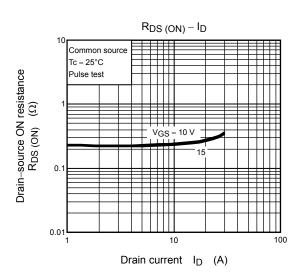


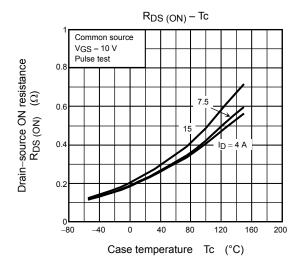


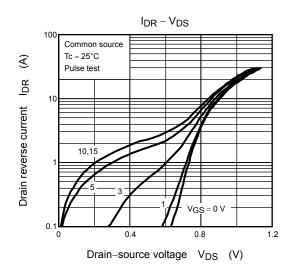


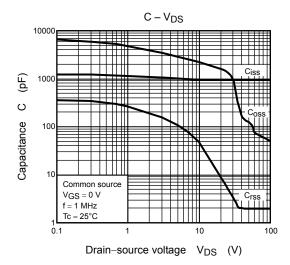


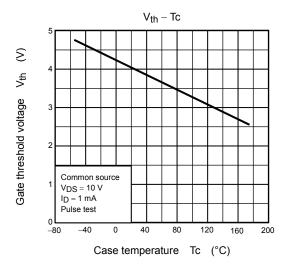


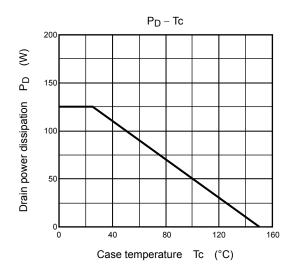


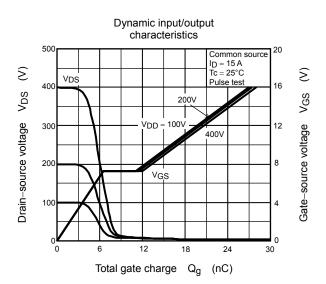


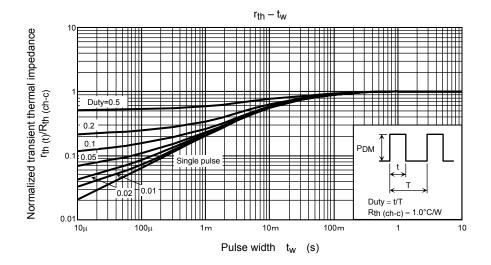


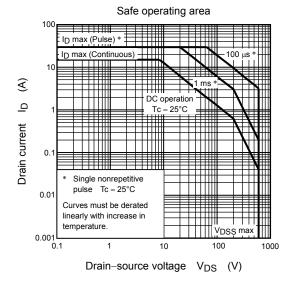


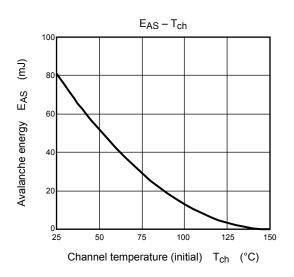


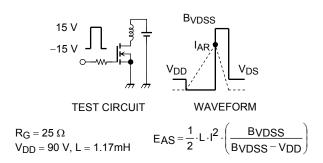












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