



P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
- 60	0.015 at V _{GS} = - 10 V	- 50 ^d		
	0.020 at V _{GS} = - 4.5 V	- 50 ^d		

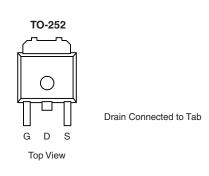
FEATURES

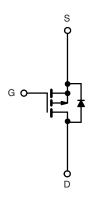
- · Halogen-free
- TrenchFET® Power MOSFET



APPLICATIONS

· Load Switch





Ordering Information: SUD50P06-15-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	- 60	V	
Gate-Source Voltage		V_{GS}	± 20	V	
Continuous Drain Current (T _J = 175 °C)	T _C = 25 °C	1-	- 50 ^d	А	
	T _C = 125 °C	I _D	- 27.5		
Pulsed Drain Current		I _{DM}	- 80	A	
Avalanche Current		I _{AS}	- 50		
Single Pulse Avalanche Energy ^a	L = 0.1 mH	E _{AS}	125	mJ	
Dower Dissipation	T _C = 25 °C	Pn	113 ^c	— w	
Power Dissipation	T _A = 25 °C	םים	2.5 ^{b, c}		
Operating Junction and Storage Temperature Range	e	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Junction-to-Ambient ^b	t ≤ 10 s	- R _{thJA}	15	18	°C/W
Junction-to-Ambient	Steady State		40	50	
Junction-to-Case		R_{thJC}	0.82	1.1	

Notes:

- a. Duty cycle \leq 1 %.
- b. When Mounted on 1" square PCB (FR-4 material).
- c. See SOA curve for voltage derating.
- d. Package limited.

SUD50P06-15

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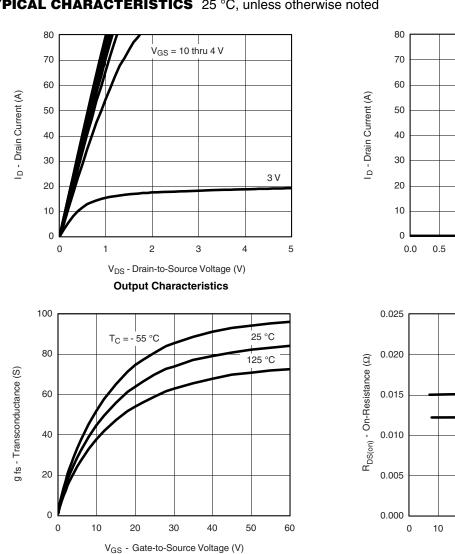
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		V _{DS} = - 60 V, V _{GS} = 0 V			- 1		
	I _{DSS}	V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 125 °C			- 50	μΑ	
		V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 150 °C			- 100	1	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 50			Α	
Drain-Source On-State Resistance ^a		V _{GS} = - 10 V, I _D = - 17 A		0.012	0.015	Ω	
		V _{GS} = - 10 V, I _D = - 50 A, T _J = 125 °C			0.025		
	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 50 A, T _J = 150 °C			0.028		
		V _{GS} = - 4.5 V, I _D = - 14 A			0.020		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 17 A		61		S	
Dynamic ^b	•			•			
Input Capacitance	C _{iss}			4950		pF	
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = - 25 V, f = 1 MHz		480			
Reverse Transfer Capacitance	C _{rss}			405			
Total Gate Charge ^c	Qg			110	165	nC	
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -50 \text{ A}$		19			
Gate-Drain Charge ^c	Q_{gd}			28			
Turn-On Delay Time ^c	t _{d(on)}			15	23		
Rise Time ^c	t _r	$V_{DD} = -30 \text{ V}, R_L = 0.6 \Omega$ $I_D \cong -50 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 6 \Omega$		70	105	ns	
Turn-Off Delay Time ^c	t _{d(off)}			175	260		
Fall Time ^c	t _f			175	260		
Source-Drain Diode Ratings and Cha	aracteristics -	Γ _C = 25 °C ^b			<u> </u>		
Continuous Current	Is				- 50		
Pulsed Current	I _{SM}				- 80	Α	
Forward Voltage ^a	V _{SD}	I _F = - 50 A, V _{GS} = 0 V		- 1.0	- 1.6	٧	
Reverse Recovery Time	t _{rr}	I _F = - 50 A, dI/dt = 100 A/μs		45	70	ns	

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

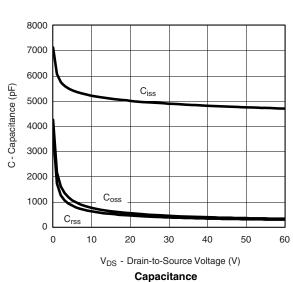
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

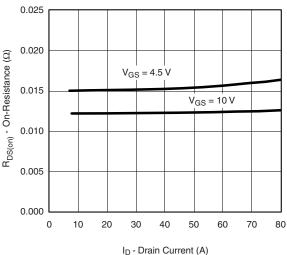


Transconductance

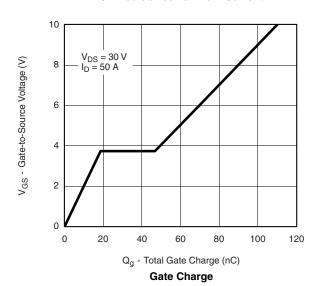


 $T_C = 125 \, ^{\circ}C$ 25 °C 55 °C 1.0 1.5 2.0 2.5 3.0 3.5 4.0 V_{GS} - Gate-to-Source Voltage (V)

Transfer Characteristics



On-Resistance vs. Drain Current

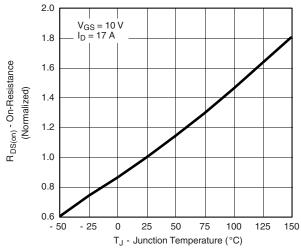


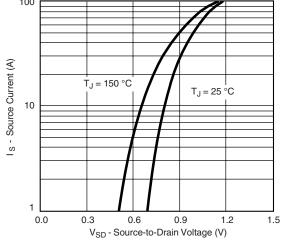
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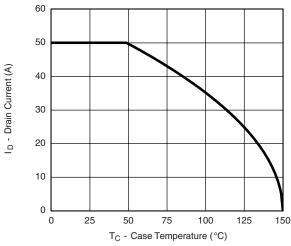


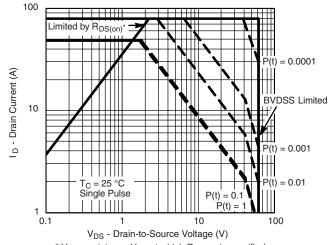


On-Resistance vs. Junction Temperature

Source-Drain Diode Forward Voltage

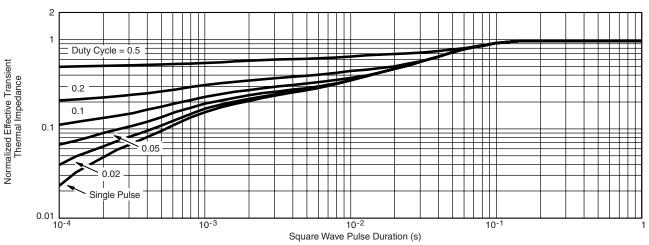
THERMAL RATINGS





Drain Current vs. Case Temperature

* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified **Safe Operating Area**



Normalized Thermal Transient Impedance, Junction-to-Case

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