MJD44H11 — NPN Epitaxial Silicon Transistor

March 2009

I-PAK

D-PAK

1.Base 2.Collector 3.Emitter

Absolute Maximum Ratings $T_a = 25^{\circ}C$ unless otherwise noted

NPN Epitaxial Silicon Transistor

Load Formed for Surface Mount Application (No Suffix)

• General Purpose Power and Switching Such as Output or Driver Stages in Applications

Symbol	Parameter	Value	Units	
V _{CEO}	Collector-Emitter Voltage	80	V	
V _{EBO}	Emitter-Base Voltage	5	V	
I _C	Collector Current (DC)	8	А	
I _{CP}	Collector-Current (Pulse)	16	А	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 65 ~ 150 °C		

Thermal Characteristics $T_a = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Max.	Units	
P _D	Total Device Dissipation $T_{c} = 25^{\circ}C$ $T_{a} = 25^{\circ}C$	20 1.75	W	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	6.25	°C/W	
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient	71.4	°C/W	

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MJD44H11

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D-PAK for Surface Mount Applications

Low Collector Emitter Saturation Voltage

Straight Lead (I-PAK, "- I" Suffix)
Electrically Similar to Popular MJE44H

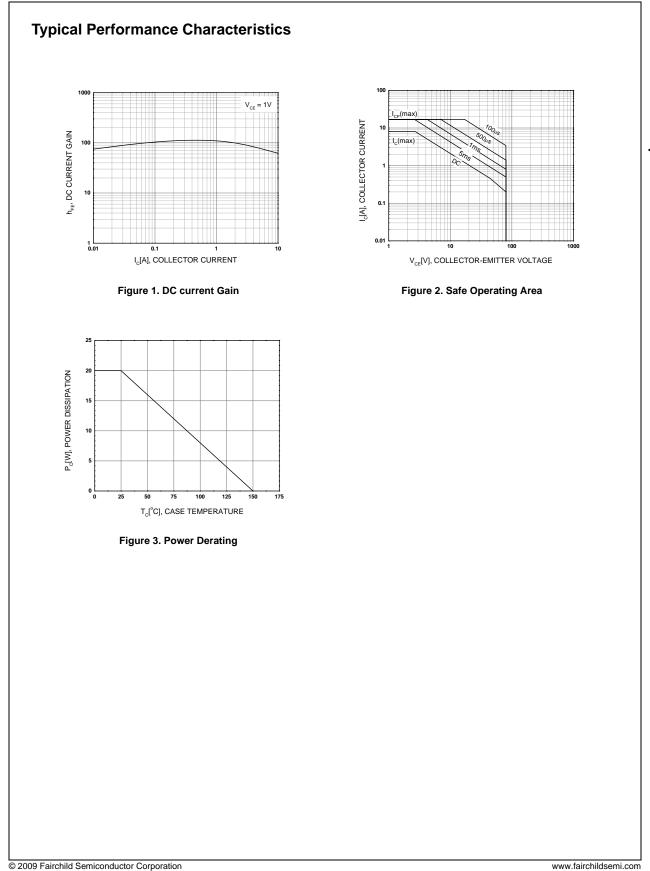
Fast Switching Speeds

MJD44H11 —
NPN
Epitaxial
Silicon
Transistor

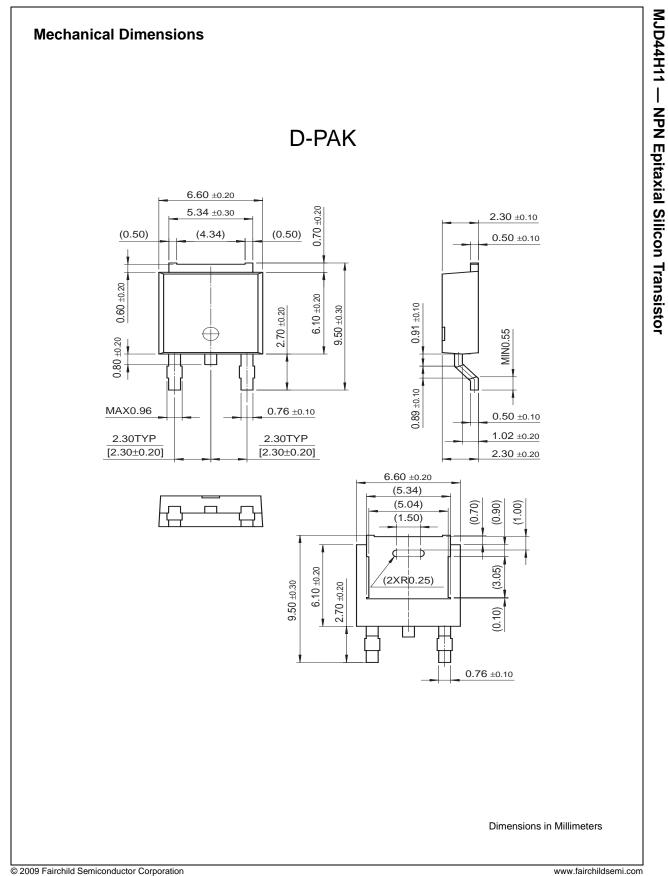
Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{CEO} (sus)	*Collector-Emitter Sustaining Voltage	I _C = 30mA, I _B = 0	80			V
I _{CEO}	Collector Cut-off Current	$V_{CE} = 80V, I_B = 0$			10	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{BE} = 5V, I_{C} = 0$			50	μΑ
h _{FE}	*DC Current Gain	$V_{CE} = 1V, I_C = 2A$ $V_{CE} = 1V, I_C = 4A$	60 40			
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	I _C = 8A, I _B = 0.4A			1	V
V _{BE} (on)	*Base-Emitter On Voltage	I _C = 8A, I _B = 0.8A			1.5	V
f _T	Current Gain Bandwidth Product	V _{CE} = 10V, I _C = 0.5A		50		MHz
C _{ob}	Output Capacitance	V _{CB} =10V, f = 1MHz		130		pF
t _{ON}	Turn On Time			300		ns
t _{STG}	Storage Time	$I_{C} = 5A$ $I_{B1} = -I_{B2} = 0.5A$		500		ns
t _F	Fall Time			140		ns

* Pulse Test: PW≤300µs, Duty Cycle≤2%

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