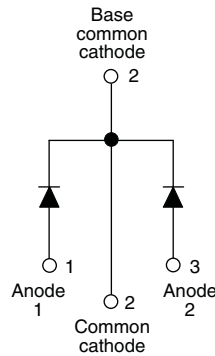


## Ultrafast Rectifier, 2 x 15 A FRED Pt®



TO-247AC



### FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level



**RoHS**  
COMPLIANT

### DESCRIPTION/APPLICATIONS

FRED Pt® series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

### PRODUCT SUMMARY

Package	TO-247AC
$I_{F(AV)}$	2 x 15 A
$V_R$	400 V
$V_F$ at $I_F$	1.25 V
$t_{rr}$ typ.	See Recovery table
$T_J$ max.	175 °C
Diode variation	Common cathode

### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		400	V
Average rectified forward current	$I_{F(AV)}$	per leg	15	A
		total device	Rated $V_R$ , $T_C = 149$ °C	
Non-repetitive peak surge current per leg	$I_{FSM}$	$T_C = 25$ °C	200	
Peak repetitive forward current per leg	$I_{FRM}$	Rated $V_R$ , $T_C = 149$ °C, square wave, 20 kHz	30	
Operating junction and storage temperatures	$T_J, T_{Stg}$		- 65 to 175	°C

### ELECTRICAL SPECIFICATIONS ( $T_J = 25$ °C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100$ $\mu$ A	400	-	-	V
Forward voltage	$V_F$	$I_F = 15$ A	-	1.17	1.25	
		$I_F = 15$ A, $T_J = 150$ °C	-	0.93	1.12	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	0.3	10	$\mu$ A
		$T_J = 150$ °C, $V_R = V_R$ rated	-	30	500	
Junction capacitance	$C_T$	$V_R = 400$ V	-	28	-	pF
Series inductance	$L_S$	Measured lead to lead 5 mm from package body	-	12	-	nH

DYNAMIC RECOVERY CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 1 A, di <sub>F</sub> /dt = 50 A/μs, V <sub>R</sub> = 30 V	-	36	60	ns	
		T <sub>J</sub> = 25 °C	-	46	-		
		T <sub>J</sub> = 125 °C	-	80	-		
Peak recovery current	I <sub>RRM</sub>	I <sub>F</sub> = 15 A di <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 200 V	T <sub>J</sub> = 25 °C	-	3.6	-	A
			T <sub>J</sub> = 125 °C	-	8.7	-	
Reverse recovery charge	Q <sub>rr</sub>	I <sub>F</sub> = 15 A di <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 200 V	T <sub>J</sub> = 25 °C	-	84	-	nC
			T <sub>J</sub> = 125 °C	-	345	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	0.8	1.5	°C/W
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	40	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.4	-	
Weight			-	6.0	-	g
			-	0.21	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC	30CPU04			

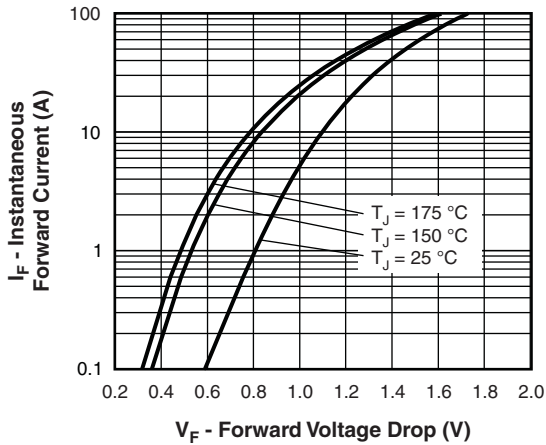


Fig. 1 - Typical Forward Voltage Drop Characteristics

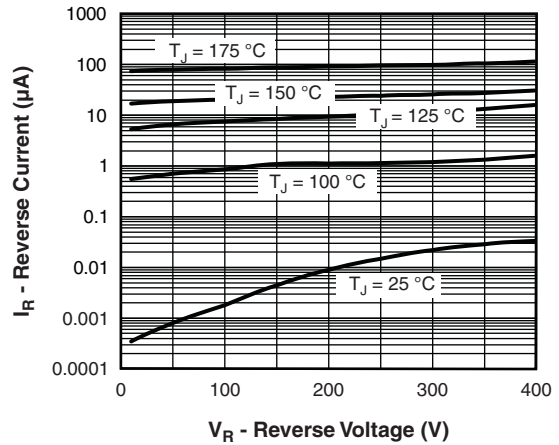


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

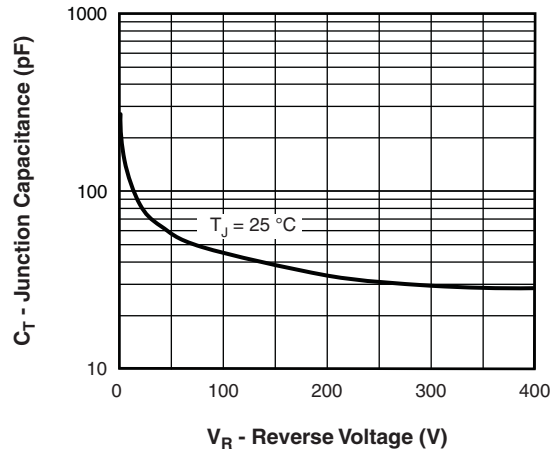
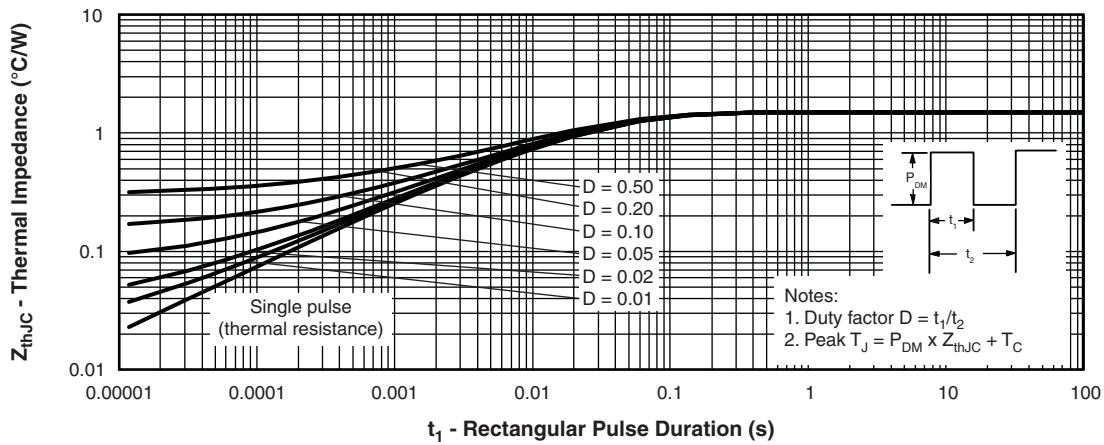


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


 Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

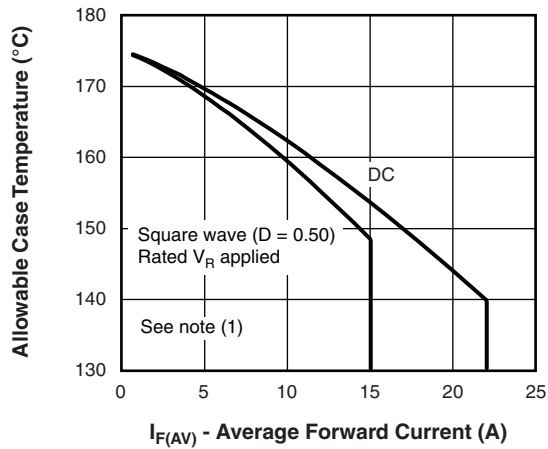


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

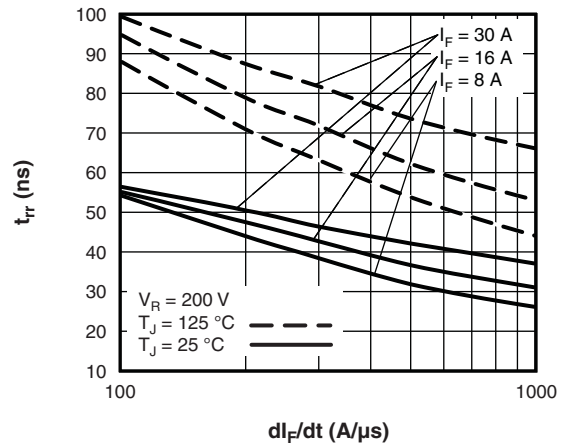


Fig. 7 - Typical Reverse Recovery Time vs.  $di_F/dt$

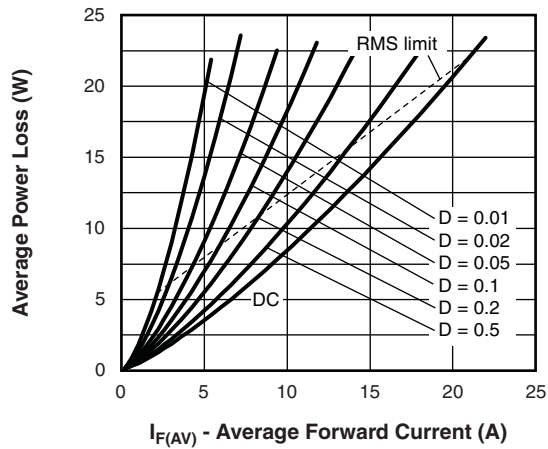


Fig. 6 - Forward Power Loss Characteristics

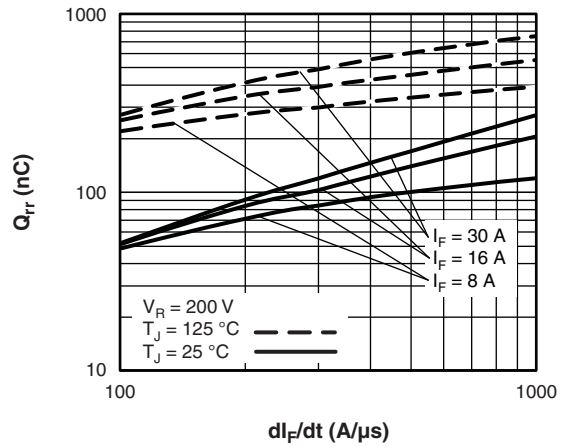


Fig. 8 - Typical Stored Charge vs.  $di_F/dt$

**Note**

- (1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  
 $Pd$  = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  
 $Pd_{REV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = Rated  $V_R$

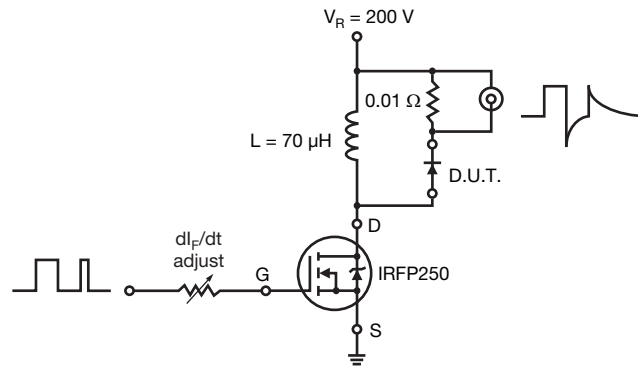
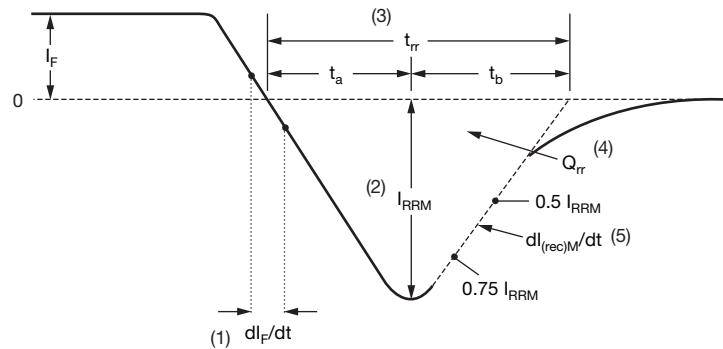


Fig. 9 - Reverse Recovery Parameter Test Circuit



- |   |   |
|---|---|
| <p>(1) <math>dI_F/dt</math> - rate of change of current through zero crossing</p> <p>(2) <math>I_{RRM}</math> - peak reverse recovery current</p> <p>(3) <math>t_{rr}</math> - reverse recovery time measured from zero crossing point of negative going <math>I_F</math> to point where a line passing through <math>0.75 I_{RRM}</math> and <math>0.50 I_{RRM}</math> extrapolated to zero current.</p> | <p>(4) <math>Q_{rr}</math> - area under curve defined by <math>t_{rr}</math> and <math>I_{RRM}</math></p> $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$ <p>(5) <math>dI_{(rec)M}/dt</math> - peak rate of change of current during <math>t_b</math> portion of <math>t_{rr}</math></p> |
|---|---|

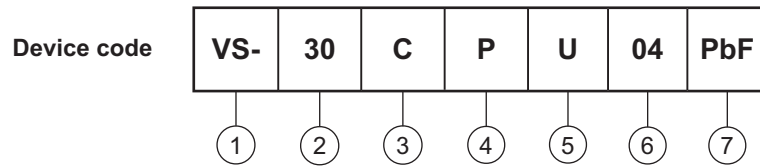
Fig. 10 - Reverse Recovery Waveform and Definitions

# VS-30CPU04PbF



Vishay Semiconductors Ultrafast Rectifier, 2 x 15 A FRED Pt®

## ORDERING INFORMATION TABLE



- 1** - Vishay Semiconductors product
- 2** - Current rating (30 = 30 A)
- 3** - Common cathode
- 4** - TO-247AC
- 5** - Ultrafast recovery
- 6** - Voltage rating (04 = 400 V)
- 7** - PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95223">www.vishay.com/doc?95223</a>
Part marking information	<a href="http://www.vishay.com/doc?95226">www.vishay.com/doc?95226</a>



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