

#### LM158-LM258-LM358

#### Low power dual operational amplifiers

#### **Features**

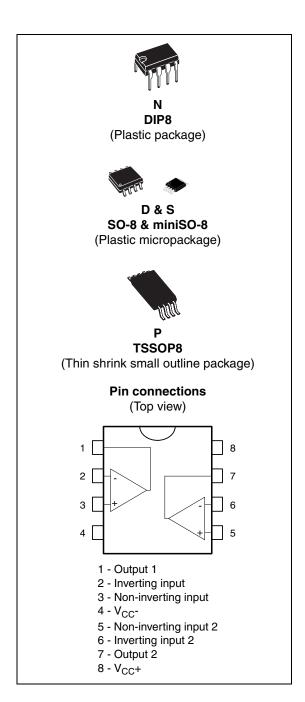
- Internally frequency-compensated
- Large DC voltage gain: 100 dB
- Wide bandwidth (unity gain): 1.1 MHz (temperature compensated)
- Very low supply current per operator essentially independent of supply voltage
- Low input bias current: 20 nA (temperature compensated)
- Low input offset voltage: 2 mV
- Low input offset current: 2 nA
- Input common-mode voltage range includes negative rails
- Differential input voltage range equal to the power supply voltage
- Large output voltage swing 0 V to (V<sub>CC</sub><sup>+</sup> 1.5V)

#### Description

These circuits consist of two independent, highgain, internally frequency-compensated op-amps, which are specifically designed to operate from a single power supply over a wide range of voltages. The low-power supply drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, DC gain blocks and all the conventional op-amp circuits, which can now be more easily implemented in single power supply systems. For example, these circuits can be directly supplied with the standard +5 V, which is used in logic systems and will easily provide the required interface electronics with no additional power supply.

In linear mode, the input common-mode voltage range includes ground and the output voltage can also swing to ground, even though operated from only a single power supply voltage.



#### 2 Absolute maximum ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	LM158,A	LM258,A	LM358,A	Unit
V <sub>CC</sub>	Supply voltage	+/-16 or 32			V
Vi	Input voltage		32		V
V <sub>id</sub>	Differential input voltage		32		V
	Output short-circuit duration (1)		Infinite		
I <sub>in</sub>	Input current (2)	5 mA in DC or 50 mA in AC (duty cycle = 10%, T=1s)			mA
T <sub>oper</sub>	Operating free-air temperature range	-55 to +125	-40 to +105	0 to +70	°C
T <sub>stg</sub>	Storage temperature range	-65 to +150			°C
T <sub>j</sub>	Maximum junction temperature	150			°C
R <sub>thja</sub>	Thermal resistance junction to ambient <sup>(3)</sup> SO-8 MiniSO-8 TSSOP8 DIP8	125 190 120 85		°C/W	
R <sub>thjc</sub>	Thermal resistance junction to case <sup>(3)</sup> SO-8 MiniSO-8 TSSOP8 DIP8	40 39 37 41		°C/W	
	HBM: human body model <sup>(4)</sup>		300		V
ESD	MM: machine model <sup>(5)</sup>		200		V
	CDM: charged device model <sup>(6)</sup>	1.5			kV

- 1. Short-circuits from the output to  $V_{CC}$  can cause excessive heating if  $V_{CC} > 15$  V. The maximum output current is approximately 40 mA independent of the magnitude of  $V_{CC}$ . Destructive dissipation can result from simultaneous short circuits on all amplifiers.
- 2. This input current only exists when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistor becoming forward-biased and thereby acting as input diode clamp. In addition to this diode action, there is NPN parasitic action on the IC chip. This transistor action can cause the output voltages of the Op-amps to go to the V<sub>CC</sub> voltage level (or to ground for a large overdrive) for the time during which an input is driven negative.
  This is not destructive and normal output is restored for input voltages above -0.3 V.
- 3. Short-circuits can cause excessive heating and destructive dissipation. Rth are typical values.
- 4. Human body model: a 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 kΩ resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.
- 5. Machine model: a 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω). This is done for all couples of connected pin combinations while the other pins are floating.
- 6. Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to the ground through only one pin. This is done for all pins.

# 3 Operating conditions

Table 2. Operating conditions

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Supply voltage	3 to 30	V
V <sub>icm</sub>	Common mode input voltage range <sup>(1)</sup>	$V_{CC}^{-}$ -0.3 to $V_{CC}^{+}$ -1.5	V
T <sub>oper</sub>	Operating free air temperature range LM158 LM258 LM358	-55 to +125 -40 to +105 0 to +70	°C

When used in comparator, the functionality is guaranteed as long as at least one input remains within the operating common mode voltage range.

### 4 Electrical characteristics

Table 3. Electrical characteristics for  $V_{CC}^+$  = +5 V,  $V_{CC}^-$  = Ground,  $V_o$  = 1.4 V,  $T_{amb}$  = +25°C (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit
V <sub>io</sub>	Input offset voltage <sup>(1)</sup> LM158A LM258A, LM358A LM158, LM258 LM358		1 2	2 3 5 7	mV
	T <sub>min</sub> ≤T <sub>amb</sub> ≤T <sub>max</sub> LM158A, LM258A, LM358A LM158, LM258 LM358			4 7 9	
DV <sub>io</sub>	Input offset voltage drift LM158A, LM258A, LM358A LM158, LM258, LM358		7 7	15 30	μV/°C
l <sub>io</sub>	Input offset current LM158A, LM258A, LM358A LM158, LM258, LM358 $T_{min} \leq T_{amb} \leq T_{max}$ LM158A, LM258A, LM358A LM158, LM258, LM358		2 2	10 30 30 40	nA
DI <sub>io</sub>	Input offset current drift LM158A, LM258A, LM358A LM158, LM258, LM358		10 10	200 300	pA/°C
l <sub>ib</sub>	Input bias current $^{(2)}$ LM158A, LM258A, LM358A LM158, LM258, LM358 $T_{min} \le T_{amb} \le T_{max}$ LM158A, LM258A, LM358A LM158, LM258, LM358		20 20	50 150 100 200	nA
A <sub>vd</sub>	Large signal voltage gain $V_{CC}^+$ = +15 V, $R_L$ = 2 k $\Omega$ , $V_o$ = 1.4 V to 11.4 V $T_{min} \le T_{amb} \le T_{max}$	50 25	100		V/mV
SVR	Supply voltage rejection ratio $V_{CC}^+ = 5 \text{ V to } 30 \text{ V}, R_S \le 10 \text{ k}\Omega$ $T_{min} \le T_{amb} \le T_{max}$	65 65	100		dB
I <sub>CC</sub>	Supply current, all amp, no load $T_{min} \leq T_{amb} \leq T_{max} \ V_{CC}^{+} = +5 \ V$ $T_{min} \leq T_{amb} \leq T_{max} \ V_{CC}^{+} = +30 \ V$		0.7	1.2 2	mA
V <sub>icm</sub>	Input common mode voltage range $V_{CC}^+$ = +30 $V_{min}^{(3)}$ $V_{min} \le V_{max}$	0 0		V <sub>CC</sub> <sup>+</sup> -1.5 V <sub>CC</sub> <sup>+</sup> -2	V

Table 3. Electrical characteristics for  $V_{CC}^+ = +5 \text{ V}$ ,  $V_{CC}^- = \text{Ground}$ ,  $V_o = 1.4 \text{ V}$ ,  $T_{amb} = +25^{\circ}\text{C}$  (unless otherwise specified) (continued)

Symbol	Parameter	Min.	Тур.	Max.	Unit
CMR	Common mode rejection ratio $\begin{aligned} R_s \leq &10 \text{ k}\Omega \\ T_{min} \leq &T_{amb} \end{array} \leq &T_{max} \end{aligned}$	70 60	85		dB
I <sub>source</sub>	Output current source $V_{CC}^+$ = +15 V, $V_0$ = +2 V, $V_{id}$ = +1 V	20	40	60	mA
I <sub>sink</sub>	Output sink current $V_{CC}^+ = +15 \text{ V}, V_o = +2 \text{ V}, V_{id} = -1 \text{ V} $ $V_{CC}^+ = +15 \text{ V}, V_o = +0.2 \text{ V}, V_{id} = -1 \text{ V}$	10 12	20 50		mA μA
V <sub>OH</sub>	High level output voltage $\begin{aligned} R_L &= 2 \text{ k}\Omega, \text{ V}_{CC}{}^+ = 30 \text{ V} \\ T_{min} &\leq T_{amb} \leq T_{max} \\ R_L &= 10 \text{ k}\Omega, \text{ V}_{CC}{}^+ = 30 \text{ V} \\ T_{min} &\leq T_{amb} \leq T_{max} \end{aligned}$	26 26 27 27	27 28		V
V <sub>OL</sub>	Low level output voltage $\begin{aligned} R_L &= 10 \text{ k}\Omega \\ T_{min} &\leq T_{amb} \ \leq T_{max} \end{aligned}$		5	20 20	mV
SR	Slew rate $V_{CC}^+ = 15 \text{ V}, V_i = 0.5 \text{ to } 3 \text{ V}, R_L = 2 \text{ k}\Omega, C_L = 100 \text{ pF, unity gain}$	0.3	0.6		V/µs
GBP	Gain bandwidth product $V_{CC}^+$ = 30 V, f = 100 kHz, $V_{in}$ = 10 mV, $R_L$ = 2 k $\Omega$ , $C_L$ = 100 pF	0.7	1.1		MHz
THD	Total harmonic distortion $f = 1 \text{ kHz}, A_v = 20 \text{ dB}, R_L = 2 \text{ k}\Omega, V_o = 2 \text{ V}_{pp},$ $C_L = 100 \text{ pF}, V_O = 2 \text{ V}_{pp}$		0.02		%
e <sub>n</sub>	Equivalent input noise voltage $f = 1 \text{ kHz}, R_s = 100 \ \Omega, \ V_{CC}^+ = 30 \text{ V}$		55		<u>nV</u> √Hz
V <sub>o1</sub> /V <sub>o2</sub>	Channel separation <sup>(4)</sup> 1 kHz $\leq$ f $\leq$ 20 kHz		120		dB

<sup>1.</sup>  $V_0 = 1.4 \text{ V}, R_s = 0 \Omega, 5 \text{ V} < V_{CC}^+ < 30 \text{ V}, 0 < V_{ic} < V_{CC}^+ - 1.5 \text{ V}$ 

<sup>2.</sup> The direction of the input current is out of the IC. This current is essentially constant, independent of the state of the output so there is no change in the load on the input lines.

<sup>3.</sup> The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3 V.

The upper end of the common-mode voltage range is V<sub>CC</sub><sup>+</sup> - 1.5 V, but either or both inputs can go to +32 V without damage.

<sup>4.</sup> Due to the proximity of external components, ensure that stray capacitance between these external parts does not cause coupling. Typically, this can be detected because this type of capacitance increases at higher frequencies.

## 6.1 DIP8 package information

Figure 27. DIP8 package mechanical drawing

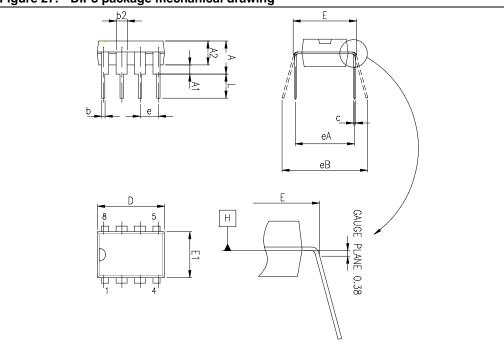


Table 4. DIP8 package mechanical data

	Dimensions					
Ref.	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			5.33			0.210
A1	0.38			0.015		
A2	2.92	3.30	4.95	0.115	0.130	0.195
b	0.36	0.46	0.56	0.014	0.018	0.022
b2	1.14	1.52	1.78	0.045	0.060	0.070
С	0.20	0.25	0.36	0.008	0.010	0.014
D	9.02	9.27	10.16	0.355	0.365	0.400
Е	7.62	7.87	8.26	0.300	0.310	0.325
E1	6.10	6.35	7.11	0.240	0.250	0.280
е		2.54			0.100	
eA		7.62			0.300	
eB			10.92			0.430
L	2.92	3.30	3.81	0.115	0.130	0.150

## 7 Ordering information

Table 8. Order codes

Order code	Temperature range	Package	Packaging	Marking
LM158N		DIP8	Tube	LM158N
LM158D LM158DT	-55°C, +125°C	SO-8	Tube ou tone 9 year	158
LM158YD <sup>(1)</sup> LM158YDT <sup>(1)</sup>		SO-8 Automotive grade	Tube or tape & reel	158Y
LM258AN LM258N	-40°C, +105°C	DIP8	Tube	LM258A LM258N
LM258AD LM258ADT		SO-8	Tube outtone 9 year	258A
LM258AYD <sup>(1)</sup> LM258AYDT <sup>(1)</sup>		SO-8 Automotive grade	Tube or tape & reel	258AY
LM258D LM258DT		SO-8	Tube or tape & reel	258
LM258YD <sup>(1)</sup> LM258YDT <sup>(1)</sup>		SO-8 Automotive grade	Tube of tape & reer	258Y
LM258PT LM258APT		TSSOP8	Tana 9 masi	258 258A
LM258YPT <sup>(2)</sup> LM258AYPT <sup>(2)</sup>		TSSOP8 Automotive grade	Tape & reel	258Y 258AY
LM258AST LM258ST		MiniSO-8	Tape & reel	K408 K416
LM358N LM358AN		DIP8	Tube	LM358N LM358AN
LM358D LM358DT		SO-8		358
LM358YD <sup>(1)</sup> LM358YDT <sup>(1)</sup>		SO-8 Automotive grade	Tube or tape & reel	358Y
LM358AD LM358ADT	0°C, +70°C	SO-8		358A
LM358PT LM358APT		TSSOP8	Tono 9 real	358 358A
LM358YPT <sup>(2)</sup> LM358AYPT <sup>(2)</sup>		TSSOP8 Automotive grade	Tape & reel	358Y 358AY
LM358ST LM358AST		MiniSO-8	Tape & reel	K405 K404

Qualified and characterized according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 & Q 002 or equivalent.

<sup>2.</sup> Qualification and characterization according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 & Q 002 or equivalent are on-going.