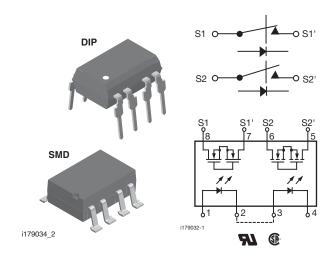


Vishay Semiconductors

Dual 1 Form A/B, C Solid State Relay



DESCRIPTION

The LH1512 relays contain normally open and normally closed switches that can be used independently as a 1 form A and 1 form B relay, or when used together, as a 1 form C relay. The relays are constructed as a mult.-chip hybrid device. Actuation control is via an infrared LED. The output switch is a combination of a photodiode array with MOSFET switches and control circuity.

FEATURES

- Current limit protection
- Isolation test voltage 3750 V_{RMS}
- Typical R_{ON} 10 Ω
- Load voltage 200 V
- Load current 200 mA
- High surge capability
- Clean bounce free switching
- Low power consumption
- SMD lead available on tape and reel
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- · General telecom switching
- On/off hook control
- Ring delay
- Dial pulse
- Ground start
- Ground fault protection
- Instrumentation
- Industrial controls

AGENCY APPROVALS

UL1577: file no. E52744 system code H, double protection

CSA: certification no. 093751

ORDERING INFORMATION				
L H 1 5 1 2 B PART NUMBER ELECTR. VARIATION	# # T R PACKAGE TAPE AND REEL 7.62 mm			
PACKAGE	UL, CSA			
SMD-8, tubes	LH1512BAC			
SMD-8, tape and reel	LH1512BACTR			
DIP-8, tubes	LH1512BB			

LH1512BAC, LH1512BACTR, LH1512BB

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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
LED continuous forward current		I _F	50	mA	
LED reverse voltage	I _R ≤ 10 μA	V _R	5	V	
OUTPUT					
DC or peak AC load voltage	I _L ≤ 50 μA	V _L	200	V	
Continuous DC load current (form C operation)		IL	200	mA	
Peak load current, form A	t = 100 ms	I _P	(2)		
Peak load current (single shot), form B		I _P	400	mA	
SSR					
Ambient operating temperature range		T _{amb}	- 40 to + 85	°C	
Storage temperature range		T _{stg}	- 40 to + 125	°C	
Pin soldering temperature (3)	t = 10 s max.	T _{sld}	260	°C	
Input to output isolation test voltage	$t = 1 \text{ s, } I_{ISO} = 10 \mu\text{A max.}$	V _{ISO}	3750	V _{RMS}	
Pole-to-pole isolation voltage (S1 to S2) (1) (dry air, dust free, at sea level)			1600	V	
Output power dissipation (continuous)		P _{diss}	600	mW	

- Notes

 Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

 Breakdown occurs between the output pins external to the package.

 Refer to current limit performance application note for a discussion on relay operation during transient currents.

 Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	INPUT					
LED forward current switch turn-on (NO)	$I_L = 100 \text{ mA}, t = 10 \text{ ms}$	I _{Fon}		0.6	2	mA
LED forward current switch turn-off (NO)	$V_{L} = \pm 150 \text{ V}$	I _{Foff}	0.2	0.5		mA
LED forward current switch turn-on (NC)	$I_L = 100 \text{ mA}, t = 10 \text{ ms}$	I _{Fon}	0.2	0.9		mA
LED forward current switch turn-off (NC)	$V_{L} = \pm 150 \text{ V}$	I _{Foff}		1	2	mA
LED forward voltage	I _F = 10 mA	V _F	1.15	1.26	1.45	V
OUTPUT						
On-resistance: (NO, NC)	$I_F = 5 \text{ mA (NO)}, I_F = 0 \text{ (NC)}, I_L = 50 \text{ mA (NC)}$	R _{ON}		10	15	Ω
Off-resistance: (NO)	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	0.35	5000		GΩ
Off-resistance: (NC)	$I_F = 5 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	0.1	1.4		GΩ
Current limit: (NO)	$I_F = 5 \text{ mA}, t = 5 \text{ ms}, V_L = \pm 5 \text{ V}$	I _{LMT}	270	360	460	mA
Off-state leakage current: (NO)	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Io		0.02	1000	nA
Off-state leakage current: (NC)	$I_F = 5 \text{ mA}, V_L = \pm 100 \text{ V}$	lo		0.07	1	μΑ
Off-state leakage current: (NO, NC)	$I_F = 0$ mA (NO), $I_F = 5$ mA, $V_L = \pm 200$ V	Io			1	μΑ
Output capacitance: (NO)	$I_F = 0 \text{ mA}, V_L = 50 \text{ V}$	Co		60		pF
Output capacitance: (NC)	$I_F = 5 \text{ mA}, V_L = 50 \text{ V}$	Co		60		pF
TRANSFER						
Capacitance (input to output)	V _{ISO} = 1 V	C _{IO}	•	3		pF

Note
 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time (NO)	$I_F = 10 \text{ mA}, I_L = 50 \text{ mA}$	t _{on}		1.4	3	ms
Turn-on time (NC)	$I_F = 10 \text{ mA}, I_L = 50 \text{ mA}$	t _{on}		1.2	3	ms
Turn-off time (NO)	$I_F = 10 \text{ mA}, I_L = 50 \text{ mA}$	t _{off}		0.7	3	ms
Turn-off time (NC)	$I_F = 10 \text{ mA}, I_L = 50 \text{ mA}$	t _{off}		2	3	ms

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For technical questions, contact: optocoupleranswers@vishay.com

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Dual 1 Form A/B, C Solid State Relay Vishay Semiconductors

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

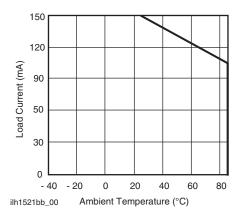


Fig. 1 - Recommended Operating Conditions

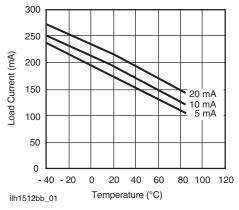


Fig. 2 - Form A Typical Load Current vs. Temperature

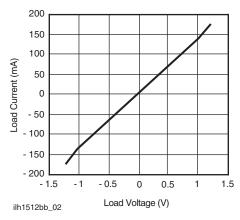


Fig. 3 - Form A Typical Load Current vs. Load Voltage

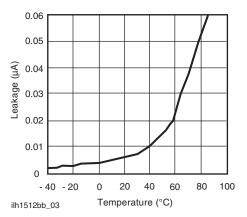


Fig. 4 - Typical Leakage vs. Temperature (Measured across Pin 5 and 6 or 7 and 8)

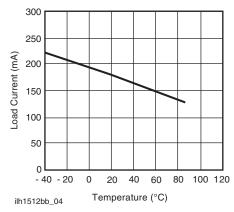


Fig. 5 - Form B Typical Load Current vs. Temperature

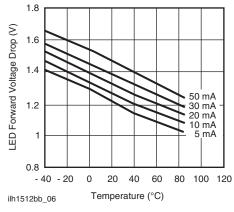


Fig. 6 - Typical LED Forward Voltage Drop vs. Temperature

LH1512BAC, LH1512BACTR, LH1512BB

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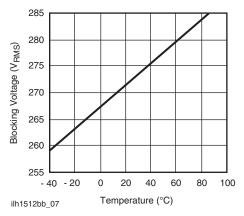


Fig. 7 - Form A Typical Blocking Voltage vs. Temperature

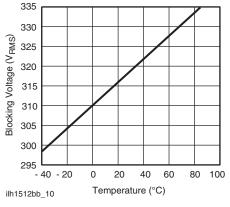


Fig. 10 - Form B Typical Blocking Voltage vs. Temperature

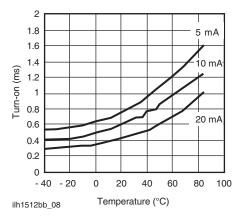


Fig. 8 - Form A Typical Turn-On vs. Temperature

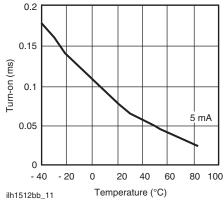


Fig. 11 - Form B Typical Turn-On vs. Temperature

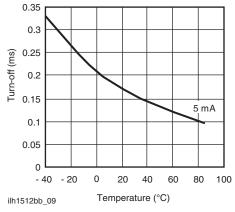


Fig. 9 - Form A Typical Turn-Off vs. Temperature

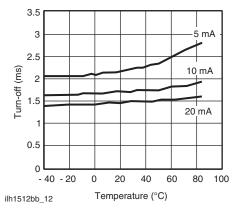


Fig. 12 - Form B Typical Turn-Off vs. Temperature



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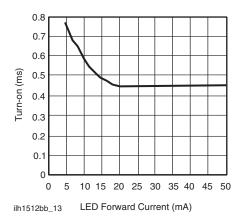


Fig. 13 - Form A Typical Turn-On vs. LED Forward Current

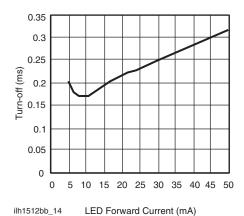


Fig. 14 - Form A Typical Turn-Off vs. LED Forward Current

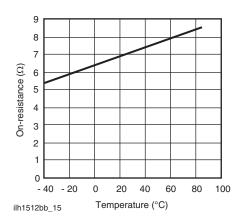


Fig. 15 - Form A Typical On-Resistance vs. Temperature

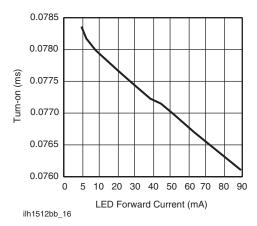


Fig. 16 - Form B Typical Turn-On vs. LED Forward Current

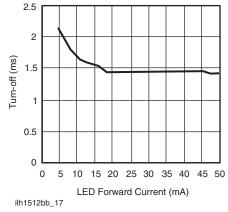


Fig. 17 - Form B Typical Turn-Off vs. LED Forward Current

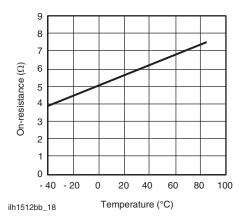


Fig. 18 - Form B Typical On-Resistance vs. Temperature

LH1512BAC, LH1512BACTR, LH1512BB

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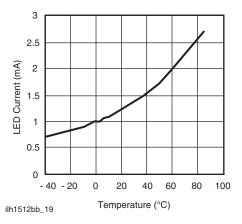


Fig. 19 - Form A Typical I_F for Switch Operation vs. Temperature

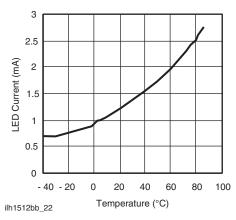


Fig. 22 - Form B Typical I_{F} for Switch Dropout vs. Temperature

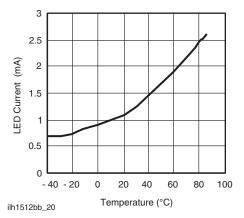


Fig. 20 - Form A Typical I_{F} for Switch Dropout vs. Temperature

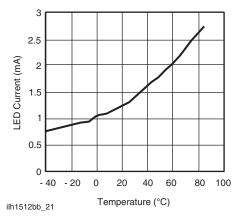


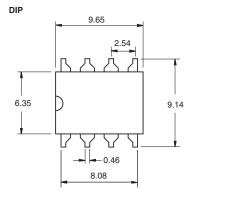
Fig. 21 - Form B Typical I_{F} for Switch Operation vs. Temperature

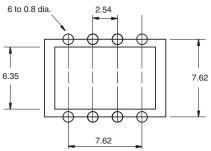




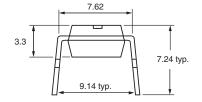
Dual 1 Form A/B, C Solid State Relay Vishay Semiconductors

PACKAGE DIMENSIONS in millimeters



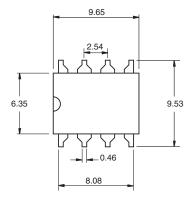


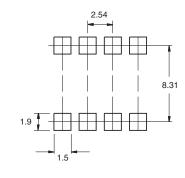




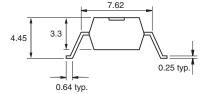
SMD

i178017









i178018

PACKAGE MARKING (example)



Note

• Tape and reel suffix (TR) is not part of the package marking.

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