## LM2675

SIMPLE SWITCHER ${ }^{\circledR}$ Power Converter High Efficiency 1A Step-Down Voltage Regulator

## General Description

The LM2675 series of regulators are monolithic integrated circuits built with a LMDMOS process. These regulators provide all the active functions for a step-down (buck) switching regulator, capable of driving a 1A load current with excellent line and load regulation. These devices are available in fixed output voltages of $3.3 \mathrm{~V}, 5.0 \mathrm{~V}, 12 \mathrm{~V}$, and an adjustable output version.
Requiring a minimum number of external components, these regulators are simple to use and include patented internal frequency compensation (Patent Nos. 5,382,918 and $5,514,947$ ) and a fixed frequency oscillator.
The LM2675 series operates at a switching frequency of 260 kHz , thus allowing smaller sized filter components than what would be needed with lower frequency switching regulators. Because of its very high efficiency ( $>90 \%$ ), the copper traces on the printed circuit board are the only heat sinking needed.
A family of standard inductors for use with the LM2675 are available from several different manufacturers. This feature greatly simplifies the design of switch-mode power supplies using these advanced ICs. Also included in the datasheet are selector guides for diodes and capacitors designed to work in switch-mode power supplies.
Other features include a guaranteed $\pm 1.5 \%$ tolerance on output voltage within specified input voltages and output load conditions, and $\pm 10 \%$ on the oscillator frequency. External shutdown is included, featuring typically $50 \mu \mathrm{~A}$ stand-by current. The output switch includes current limiting, as well as thermal shutdown for full protection under fault conditions.

To simplify the LM2675 buck regulator design procedure, there exists computer design software, LM267X Made
Simple version 6.0.

## Features

- Efficiency up to $96 \%$
- Available in SO-8, 8-pin DIP and LLP packages
- Computer Design Software LM267X Made Simple (version 6.0)
- Simple and easy to design with
- Requires only 5 external components
- Uses readily available standard inductors
- $3.3 \mathrm{~V}, 5.0 \mathrm{~V}, 12 \mathrm{~V}$, and adjustable output versions
- Adjustable version output voltage range: 1.21 V to 37 V
- $\pm 1.5 \%$ max output voltage tolerance over line and load conditions
- Guaranteed 1A output load current
- $0.25 \Omega$ DMOS Output Switch
- Wide input voltage range: 8 V to 40 V
- 260 kHz fixed frequency internal oscillator
- TTL shutdown capability, low power standby mode
- Thermal shutdown and current limit protection


## Typical Applications

- Simple High Efficiency (>90\%) Step-Down (Buck) Regulator
- Efficient Pre-Regulator for Linear Regulators
- Positive-to-Negative Converter


## Connection Diagrams



* No Connections
**Connect to Pins 11, 12 on PCB


## LLP Package

# 8-Lead Package Top View <br>  <br> See NSC Package Drawing Number MO8A/N08E 

## Package Marking and Ordering Information

TABLE 1.

| Output Voltage | Order Information | Package Marking | Supplied as: |
| :---: | :---: | :---: | :---: |
| 16 Lead LLP |  |  |  |
| 12 | LM2675LD-12 | S000DB | 1000 Units on Tape and Reel |
| 12 | LM2675LDX-12 | S000DB | 4500 Units on Tape and Reel |
| 3.3 | LM2675LD-3.3 | S000EB | 1000 Units on Tape and Reel |
| 3.3 | LM2675LDX-3.3 | S000EB | 4500 Units on Tape and Reel |
| 5.0 | LM2675LD-5.0 | S000FB | 1000 Units on Tape and Reel |
| 5.0 | LM2675LDX-5.0 | S000FB | 4500 Units on Tape and Reel |
| ADJ | LM2675LD-ADJ | S000GB | 1000 Units on Tape and Reel |
| ADJ | LM2675LDX-ADJ | S000GB | 4500 Units on Tape and Reel |
| SO-8 |  |  |  |
| 12 | LM2675M-12 | 2675M-12 | Shipped in Anti-Static Rails |
| 12 | LM2675MX-12 | 2675M-12 | 2500 Units on Tape and Reel |
| 3.3 | LM2675M-3.3 | 2675M-3.3 | Shipped in Anti-Static Rails |
| 3.3 | LM2675MX-3.3 | 2675M-3.3 | 2500 Units on Tape and Reel |
| 5.0 | LM2675M-5.0 | 2675M-5.0 | Shipped in Anti-Static Rails |
| 5.0 | LM2675MX-5.0 | 2675M-5.0 | 2500 Units on Tape and Reel |
| ADJ | LM2675M-ADJ | 2675M-ADJ | Shipped in Anti-Static Rails |
| ADJ | LM2675MX-ADJ | 2675M-ADJ | 2500 Units on Tape and Reel |
| DIP |  |  |  |
| 12 | LM2675N-12 | LM2675N-12 | Shipped in Anti-Static Rails |
| 3.3 | LM2675N-3.3 | LM2675N-3.3 | Shipped in Anti-Static Rails |
| 5.0 | LM2675N-5.0 | LM2675N-5.0 | Shipped in Anti-Static Rails |
| ADJ | LM2675N-ADJ | LM2675N-ADJ | Shipped in Anti-Static Rails |

## Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

| Supply Voltage | 45 V | Storage Temperature Range | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| :---: | :---: | :---: | :---: |
| ON/OFF Pin Voltage | $-0.1 \mathrm{~V} \leq \mathrm{V}_{\mathrm{SH}} \leq 6 \mathrm{~V}$ | Lead Temperature |  |
| Switch Voltage to Ground | -1V | M Package |  |
| Boost Pin Voltage | $\mathrm{V}_{\mathrm{Sw}}+8 \mathrm{~V}$ | Vapor Phase (60s) | $+215^{\circ} \mathrm{C}$ |
| Feedback Pin Voltage | $-0.3 \mathrm{~V} \leq \mathrm{V}_{\mathrm{FB}} \leq 14 \mathrm{~V}$ | Infrared (15s) | $+220^{\circ} \mathrm{C}$ |
| ESD Susceptibility |  | N Package (Soldering, 10s) | $+260^{\circ} \mathrm{C}$ |
| Human Body Model (Note 2) | 2 kV | LLP Package (See AN-1187) |  |
| Power Dissipation | Internally Limited | Maximum Junction Temperature | $+150^{\circ} \mathrm{C}$ |

## Operating Ratings

Supply Voltage Junction Temperature Range $\quad 6.5 \mathrm{~V}$ to $40 \mathrm{~V} \quad-40^{\circ} \mathrm{C} \leq \mathrm{T}_{J} \leq+125^{\circ} \mathrm{C}$

## Electrical Characteristics

LM2675-3.3 Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with bold type face apply over full Operating Temperature Range.

| Symbol | Parameter | Conditions | Typical (Note 4) | Min (Note 5) | $\begin{gathered} \text { Max } \\ (\text { Note 5) } \end{gathered}$ | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SYSTEM PARAMETERS Test Circuit Figure 2 (Note 3) |  |  |  |  |  |  |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $\mathrm{V}_{\text {IN }}=8 \mathrm{~V}$ to $40 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=20 \mathrm{~mA}$ to 1 A | 3.3 | 3.251/3.201 | 3.350/3.399 | V |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $\mathrm{V}_{\text {IN }}=6.5 \mathrm{~V}$ to $40 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=20 \mathrm{~mA}$ to 500 mA | 3.3 | 3.251/3.201 | 3.350/3.399 | V |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=1 \mathrm{~A}$ | 86 |  |  | \% |

## LM2675-5.0

| Symbol | Parameter | Conditions | Typical <br> (Note 4) | $\begin{gathered} \text { Min } \\ (\text { Note } 5) \\ \hline \end{gathered}$ | Max <br> (Note 5) | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SYSTEM PARAMETERS Test Circuit Figure 2 (Note 3) |  |  |  |  |  |  |
| $\mathrm{V}_{\text {Out }}$ | Output Voltage | $\mathrm{V}_{\text {IN }}=8 \mathrm{~V}$ to $40 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=20 \mathrm{~mA}$ to 1 A | 5.0 | 4.925/4.850 | 5.075/5.150 | V |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $\mathrm{V}_{\text {IN }}=6.5 \mathrm{~V}$ to 40 V , $\mathrm{I}_{\text {LOAD }}=20 \mathrm{~mA}$ to 500 mA | 5.0 | 4.925/4.850 | 5.075/5.150 | V |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=1 \mathrm{~A}$ | 90 |  |  | \% |
| LM2675-12 |  |  |  |  |  |  |
| Symbol | Parameter | Conditions | Typical (Note 4) | Min (Note 5) | Max (Note 5) | Units |
| SYSTEM PARAMETERS Test Circuit Figure 2 (Note 3) |  |  |  |  |  |  |
| $\mathrm{V}_{\text {OUt }}$ | Output Voltage | $\mathrm{V}_{\text {IN }}=15 \mathrm{~V}$ to 40 V , $\mathrm{I}_{\text {LOAD }}=20 \mathrm{~mA}$ to 1 A | 12 | 11.82/11.64 | 12.18/12.36 | V |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=24 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=1 \mathrm{~A}$ | 94 |  |  | \% |
| LM2675-ADJ |  |  |  |  |  |  |
| Symbol | Parameter | Conditions |  | Min (Note 5) | Max (Note 5) | Units |
| SYSTEM PARAMETERS Test Circuit Figure 3 (Note 3) |  |  |  |  |  |  |
| $\mathrm{V}_{\mathrm{FB}}$ | Feedback Voltage | $\mathrm{V}_{\mathrm{IN}}=8 \mathrm{~V}$ to 40 V , $\mathrm{I}_{\text {LOAD }}=20 \mathrm{~mA}$ to 1 A <br> $\mathrm{V}_{\text {Out }}$ Programmed for 5 V <br> (see Circuit of Figure 3) | 1.210 | 1.192/1.174 | 1.228/1.246 | V |
| $\mathrm{V}_{\text {FB }}$ | Feedback Voltage | $\mathrm{V}_{\mathrm{IN}}=6.5 \mathrm{~V}$ to 40 V , $\mathrm{I}_{\text {LOAD }}=20 \mathrm{~mA}$ to 500 mA $\mathrm{V}_{\text {Out }}$ Programmed for 5 V <br> (see Circuit of Figure 3) | 1.210 | 1.192/1.174 | 1.228/1.246 | V |

LM2675-ADJ (Continued)

| Symbol | Parameter | Conditions | Typ <br> $($ Note 4) | Min <br> (Note 5) | Max <br> (Note 5) | Units |
| :--- | :--- | :--- | :---: | :---: | :---: | :---: |
| $\eta$ | Efficiency | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=1 \mathrm{~A}$ | 90 |  |  | $\%$ |

## All Output Voltage Versions

Specifications with standard type face are for $T_{J}=25^{\circ} \mathrm{C}$, and those with bold type face apply over full Operating Temperature Range. Unless otherwise specified, $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}$ for the $3.3 \mathrm{~V}, 5 \mathrm{~V}$, and Adjustable versions and $\mathrm{V}_{\mathrm{IN}}=24 \mathrm{~V}$ for the 12 V version, and $\mathrm{I}_{\text {LOAD }}=100 \mathrm{~mA}$.

| Symbol | Parameters | Conditions | Typ | Min | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| DEVICE PARAMETERS |  |  |  |  |  |  |
| $\mathrm{I}_{\mathrm{Q}}$ | Quiescent Current | $\mathrm{V}_{\text {FEEDBACK }}=8 \mathrm{~V}$ <br> For 3.3V, 5.0V, and ADJ Versions | 2.5 |  | 3.6 | mA |
|  |  | $\mathrm{V}_{\text {FEEDBACK }}=15 \mathrm{~V}$ <br> For 12V Versions | 2.5 |  |  | mA |
| $\mathrm{I}_{\text {STBY }}$ | Standby Quiescent Current | ON/OFF Pin $=0 \mathrm{~V}$ | 50 |  | 100/150 | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\mathrm{CL}}$ | Current Limit |  | 1.55 | 1.25/1.2 | 2.1/2.2 | A |
| $\mathrm{I}_{\mathrm{L}}$ | Output Leakage Current | $\begin{aligned} & \mathrm{V}_{\text {IN }}=40 \mathrm{~V}, \text { ON } / \overline{\text { OFF }} \text { Pin }=0 \mathrm{~V} \\ & \mathrm{~V}_{\text {SWITCH }}=0 \mathrm{~V} \end{aligned}$ | 1 |  | 25 | $\mu \mathrm{A}$ |
|  |  | $\mathrm{V}_{\text {SWITCH }}=-1 \mathrm{~V}$, ON/ $\overline{\text { OFF }}$ Pin $=0 \mathrm{~V}$ | 6 |  | 15 | mA |
| $\mathrm{R}_{\text {DS(ON) }}$ | Switch On-Resistance | $\mathrm{I}_{\text {SWITCH }}=1 \mathrm{~A}$ | 0.25 |  | 0.30/0.50 | $\Omega$ |
| $\mathrm{f}_{0}$ | Oscillator Frequency | Measured at Switch Pin | 260 | 225 | 275 | kHz |
| D | Maximum Duty Cycle |  | 95 |  |  | \% |
|  | Minimum Duty Cycle |  | 0 |  |  | \% |
| $\mathrm{I}_{\text {BIAS }}$ | Feedback Bias Current | $\begin{aligned} & \mathrm{V}_{\text {FEEDBACK }}=1.3 \mathrm{~V} \\ & \text { ADJ Version Only } \end{aligned}$ | 85 |  |  | nA |
| $\mathrm{V}_{\mathrm{S} / \mathrm{D}}$ | $\begin{aligned} & \text { ON/OFF Pin } \\ & \text { Voltage Thesholds } \end{aligned}$ |  | 1.4 | 0.8 | 2.0 | V |
| $\mathrm{I}_{\text {S/D }}$ | ON/OFF Pin Current | ON/OFF Pin = 0V | 20 | 7 | 37 | $\mu \mathrm{A}$ |
| $\theta_{\mathrm{JA}}$ | Thermal Resistance | N Package, Junction to Ambient (Note 6) M Package, Junction to Ambient (Note 6) | $\begin{gathered} 95 \\ 105 \end{gathered}$ |  |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but device parameter specifications may not be guaranteed under these conditions. For guaranteed specifications and test conditions, see the Electrical Characteristics.
Note 2: The human body model is a 100 pF capacitor discharged through a $1.5 \mathrm{k} \Omega$ resistor into each pin.
Note 3: External components such as the catch diode, inductor, input and output capacitors, and voltage programming resistors can affect switching regulator performance. When the LM2675 is used as shown in Figures 2, 3 test circuits, system performance will be as specified by the system parameters section of the Electrical Characteristics.
Note 4: Typical numbers are at $25^{\circ} \mathrm{C}$ and represent the most likely norm.
Note 5: All limits guaranteed at room temperature (standard type face) and at temperature extremes (bold type face). All room temperature limits are $100 \%$ production tested. All limits at temperature extremes are guaranteed via correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).
Note 6: Junction to ambient thermal resistance with approximately 1 square inch of printed circuit board copper surrounding the leads. Additional copper area will lower thermal resistance further. See Application Information section in the application note accompanying this datasheet and the thermal model in LM267X Made Simple software (version 6.0). The value $\theta_{J-A}$ for the LLP (LD) package is specifically dependent on PCB trace area, trace material, and the number of layers and thermal vias. For improved thermal resistance and power dissipation for the LLP package, refer to Application Note AN-1187.

Physical Dimensions inches (millimeters)
unless otherwise noted


RECOMMENDED LAND PATTERN


CONTROLLING DIMENSION IS MILLIMETER
VALUES IN [ ] ARE INCHES
M08A (Rev K)
8-Lead ( 0.150 " Wide) Molded Small Outline Package, JEDEC Order Number LM2675M-3.3, LM2675M-5.0,

LM2675M-12 or LM2675M-ADJ NS Package Number M08A

