

### FEATURES

- Easy to use**
- Higher performance than discrete design**
- Single-supply and dual-supply operation**
- Rail-to-rail output swing**
- Input voltage range extends 150 mV below ground (single supply)**
- Low power, 550  $\mu$ A maximum supply current**
- Gain set with one external resistor**
- Gain range: 1 (no resistor) to 1000**
- High accuracy dc performance**
  - 0.10% gain accuracy ( $G = 1$ )**
  - 0.35% gain accuracy ( $G > 1$ )**
  - 10 ppm maximum gain drift ( $G = 1$ )**
  - 200  $\mu$ V maximum input offset voltage (AD623A)**
  - 2  $\mu$ V/ $^{\circ}$ C maximum input offset drift (AD623A)**
  - 100  $\mu$ V maximum input offset voltage (AD623B)**
  - 1  $\mu$ V/ $^{\circ}$ C maximum input offset drift (AD623B)**
  - 25 nA maximum input bias current**
- Noise: 35 nV/ $\sqrt{\text{Hz}}$  RTI noise @ 1 kHz ( $G = 1$ )**
- Excellent ac specifications**
  - 90 dB minimum CMRR ( $G = 10$ ); 70 dB minimum CMRR ( $G = 1$ ) at 60 Hz, 1 k $\Omega$  source imbalance**
  - 800 kHz bandwidth ( $G = 1$ )**
  - 20  $\mu$ s settling time to 0.01% ( $G = 10$ )**

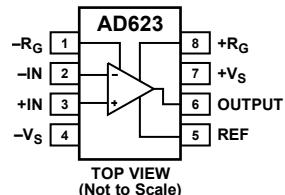
### APPLICATIONS

- Low power medical instrumentation**
- Transducer interfaces**
- Thermocouple amplifiers**
- Industrial process controls**
- Difference amplifiers**
- Low power data acquisition**

### GENERAL DESCRIPTION

The AD623 is an integrated single-supply instrumentation amplifier that delivers rail-to-rail output swing on a 3 V to 12 V supply. The AD623 offers superior user flexibility by allowing single gain set resistor programming and by conforming to the 8-lead industry standard pinout configuration. With no external resistor, the AD623 is configured for unity gain ( $G = 1$ ), and with an external resistor, the AD623 can be programmed for gains up to 1000.

### CONNECTION DIAGRAM



00778-001

Figure 1. 8-Lead PDIP (N), SOIC (R), and MSOP (RM) Packages

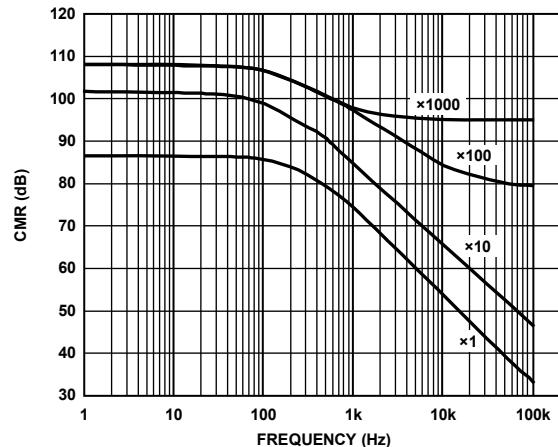


Figure 2. CMR vs. Frequency, 5 Vs, 0 Vs

The AD623 holds errors to a minimum by providing superior ac CMRR that increases with increasing gain. Line noise, as well as line harmonics, are rejected because the CMRR remains constant up to 200 Hz. The AD623 has a wide input common-mode range and can amplify signals that have a common-mode voltage 150 mV below ground. Although the design of the AD623 was optimized to operate from a single supply, the AD623 still provides superior performance when operated from a dual voltage supply ( $\pm 2.5$  V to  $\pm 6.0$  V).

Low power consumption (1.5 mW at 3 V), wide supply voltage range, and rail-to-rail output swing make the AD623 ideal for battery-powered applications. The rail-to-rail output stage maximizes the dynamic range when operating from low supply voltages. The AD623 replaces discrete instrumentation amplifier designs and offers superior linearity, temperature stability, and reliability in a minimum of space.

Rev. D

Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties that may result from its use. Specifications subject to change without notice. No license is granted by implication or otherwise under any patent or patent rights of Analog Devices. Trademarks and registered trademarks are the property of their respective owners.

## SPECIFICATIONS

### SINGLE SUPPLY

Typical @ 25°C single supply,  $V_s = 5$  V, and  $R_L = 10$  kΩ, unless otherwise noted.

Table 1.

Parameter	Conditions	AD623A			AD623ARM			AD623B			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
GAIN	$G = 1 + (100 \text{ k}/R_G)$										
Gain Range		1		1000	1		1000	1		1000	
Gain Error <sup>1</sup>	$G_1 V_{\text{OUT}} = 0.05 \text{ V to } 3.5 \text{ V}$ $G > 1 V_{\text{OUT}} = 0.05 \text{ V to } 4.5 \text{ V}$										
G = 1			0.03	0.10		0.03	0.10		0.03	0.05	%
G = 10			0.10	0.35		0.10	0.35		0.10	0.35	%
G = 100			0.10	0.35		0.10	0.35		0.10	0.35	%
G = 1000			0.10	0.35		0.10	0.35		0.10	0.35	%
Nonlinearity	$G_1 V_{\text{OUT}} = 0.05 \text{ V to } 3.5 \text{ V}$ $G > 1 V_{\text{OUT}} = 0.05 \text{ V to } 4.5 \text{ V}$										
G = 1 to 1000			50			50			50		ppm
Gain vs. Temperature			5	10		5	10		5	10	ppm/°C
G = 1			50			50			50		ppm/°C
G > 1 <sup>1</sup>											
VOLTAGE OFFSET		Total RTI error = $V_{\text{OSI}} + V_{\text{Oso}}/G$									
Input Offset, $V_{\text{OSI}}$			25	200		200	500		25	100	μV
Over Temperature				350			650			160	μV
Average Tempco			0.1	2		0.1	2		0.1	1	μV/°C
Output Offset, $V_{\text{Oso}}$			200	1000		500	2000		200	500	μV
Over Temperature				1500			2600			1100	μV
Average Tempco			2.5	10		2.5	10		2.5	10	μV/°C
Offset Referred to the Input vs. Supply (PSR)											
G = 1			80	100		80	100		80	100	dB
G = 10			100	120		100	120		100	120	dB
G = 100			120	140		120	140		120	140	dB
G = 1000			120	140		120	140		120	140	dB
INPUT CURRENT											
Input Bias Current			17	25		17	25		17	25	nA
Over Temperature				27.5			27.5			27.5	nA
Average Tempco			25			25			25		pA/°C
Input Offset Current			0.25	2		0.25	2		0.25	2	nA
Over Temperature				2.5			2.5			2.5	nA
Average Tempco			5			5			5		pA/°C

# AD623

Parameter	Conditions	AD623A			AD623ARM			AD623B			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
INPUT											
Input Impedance		2  2			2  2			2  2			GΩ  pF
Differential		2  2			2  2			2  2			GΩ  pF
Common-Mode											V
Input Voltage Range <sup>2</sup>	V <sub>S</sub> = 3 V to 12 V	(-V <sub>S</sub> ) - 0.15		(+V <sub>S</sub> ) - 1.5	(-V <sub>S</sub> ) - 0.15		(+V <sub>S</sub> ) - 1.5	(-V <sub>S</sub> ) - 0.15		(+V <sub>S</sub> ) - 1.5	
Common-Mode Rejection at 60 Hz with 1 kΩ Source Imbalance											
G = 1	V <sub>CM</sub> = 0 V to 3 V	70	80		70	80		77	86		dB
G = 10	V <sub>CM</sub> = 0 V to 3 V	90	100		90	100		94	100		dB
G = 100	V <sub>CM</sub> = 0 V to 3 V	105	110		105	110		105	110		dB
G = 1000	V <sub>CM</sub> = 0 V to 3 V	105	110		105	110		105	110		dB
OUTPUT											
Output Swing	R <sub>L</sub> = 10 kΩ	0.01	(+V <sub>S</sub> ) - 0.5		0.01	(+V <sub>S</sub> ) - 0.5		0.01	(+V <sub>S</sub> ) - 0.5		V
	R <sub>L</sub> = 100 kΩ	0.01	(+V <sub>S</sub> ) - 0.15		0.01	(+V <sub>S</sub> ) - 0.15		0.01	(+V <sub>S</sub> ) - 0.15		V
DYNAMIC RESPONSE											
Small Signal –3 dB Bandwidth											
G = 1		800			800			800			kHz
G = 10		100			100			100			kHz
G = 100		10			10			10			kHz
G = 1000		2			2			2			kHz
Slew Rate		0.3			0.3			0.3			V/μs
Settling Time to 0.01%	V <sub>S</sub> = 5 V										
G = 1	Step size: 3.5 V	30			30			30			μs
G = 10	Step size: 4 V, V <sub>CM</sub> = 1.8 V	20			20			20			μs

<sup>1</sup> Does not include effects of external resistor, R<sub>G</sub>.

<sup>2</sup> One input grounded. G = 1.

## DUAL SUPPLIES

Typical @ 25°C dual supply, V<sub>S</sub> = ±5 V, and R<sub>L</sub> = 10 kΩ, unless otherwise noted.

Table 2.

Parameter	Conditions	AD623A			AD623ARM			AD623B			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
GAIN	G = 1 + (100 k/R <sub>G</sub> )										
Gain Range	1		1000		1		1000	1		1000	
Gain Error <sup>1</sup>	G1 V <sub>OUT</sub> = -4.8 V to +3.5 V G > 1 V <sub>OUT</sub> = 0.05 V to 4.5 V										
G = 1		0.03	0.10		0.03	0.10		0.03	0.05		%
G = 10		0.10	0.35		0.10	0.35		0.10	0.35		%
G = 100		0.10	0.35		0.10	0.35		0.10	0.35		%
G = 1000		0.10	0.35		0.10	0.35		0.10	0.35		%

Parameter	Conditions	AD623A			AD623ARM			AD623B			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
Nonlinearity	G1 $V_{OUT} = -4.8\text{V}$ to $+3.5\text{V}$ G > 1 $V_{OUT} = -4.8\text{V}$ to $+4.5\text{V}$										
G = 1 to 1000 Gain vs. Temperature G = 1 G > 1 <sup>1</sup>		50			50			50			ppm
		5	10		5	10		5	10		ppm/ $^{\circ}\text{C}$
		50			50			50			ppm/ $^{\circ}\text{C}$
VOLTAGE OFFSET	Total RTI error = $V_{OSI} + V_{oso}/G$										
Input Offset, $V_{OSI}$ Over Temperature Average Tempco		25	200		200	500		25	100		$\mu\text{V}$
			350			650			160		$\mu\text{V}$
Output Offset, $V_{oso}$ Over Temperature Average Tempco		0.1	2		0.1	2		0.1	1		$\mu\text{V}/^{\circ}\text{C}$
Offset Referred to the Input vs. Supply (PSR)		200	1000		500	2000		200	500		$\mu\text{V}$
G = 1 G = 10 G = 100 G = 1000		1500			2600			1100			$\mu\text{V}$
		2.5	10		2.5	10		2.5	10		$\mu\text{V}/^{\circ}\text{C}$
INPUT CURRENT											
Input Bias Current Over Temperature Average Tempco		80	100		80	100		80	100		$\text{dB}$
Input Offset Current Over Temperature Average Tempco		100	120		100	120		100	120		$\text{dB}$
		120	140		120	140		120	140		$\text{dB}$
		120	140		120	140		120	140		$\text{dB}$
INPUT											
Input Impedance Differential Common-Mode		17	25		17	25		17	25		$\text{nA}$
Input Voltage Range <sup>2</sup>	$V_s = +2.5\text{V}$ to $\pm 6\text{V}$	2 2		2 2	2 2		2 2	2 2			$\text{G}\Omega  \text{pF}$
Common-Mode Rejection at 60 Hz with 1 k $\Omega$ Source Imbalance		2 2		2 2	2 2		2 2	2 2			$\text{G}\Omega  \text{pF}$
G = 1 G = 10 G = 100 G = 1000	$V_{CM} = +3.5\text{V}$ to $-5.15\text{V}$	0.15	1.5	0.15	1.5	0.15	1.5	0.15	1.5		V
		70	80		70	80		77	86		$\text{dB}$
		90	100		90	100		94	100		$\text{dB}$
		105	110		105	110		105	110		$\text{dB}$
		105	110		105	110		105	110		$\text{dB}$
OUTPUT											
Output Swing	$R_L = 10\text{ k}\Omega$ , $V_s = \pm 5\text{V}$	( $-V_s$ ) + 0.2	( $+V_s$ ) - 0.5	( $-V_s$ ) + 0.2	( $+V_s$ ) - 0.5	( $-V_s$ ) + 0.2	( $+V_s$ ) - 0.5	( $-V_s$ ) + 0.2	( $+V_s$ ) - 0.5		V
	$R_L = 100\text{ k}\Omega$	( $-V_s$ ) + 0.05	( $+V_s$ ) - 0.15	( $-V_s$ ) + 0.05	( $+V_s$ ) - 0.15	( $-V_s$ ) + 0.05	( $+V_s$ ) - 0.15	( $-V_s$ ) + 0.05	( $+V_s$ ) - 0.15		V

# AD623

Parameter	Conditions	AD623A			AD623ARM			AD623B			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
DYNAMIC RESPONSE											
Small Signal –3 dB Bandwidth											
G = 1			800			800			800		kHz
G = 10			100			100			100		kHz
G = 100			10			10			10		kHz
G = 1000			2			2			2		kHz
Slew Rate			0.3			0.3			0.3		V/μs
Settling Time to 0.01%	V <sub>S</sub> = ±5 V, 5 V step										
G = 1			30			30			30		μs
G = 10			20			20			20		μs

<sup>1</sup> Does not include effects of external resistor, R<sub>G</sub>.

<sup>2</sup> One input grounded. G = 1.

## BOTH DUAL AND SINGLE SUPPLIES

Table 3.

Parameter	Conditions	AD623A			AD623ARM			AD623B			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
NOISE											
Voltage Noise, 1 kHz	Total RTI noise = $\sqrt{(e_{ni})^2 + (e_{no}/G)^2}$										
Input, Voltage Noise, e <sub>ni</sub>		35			35			35			nV/Hz
Output, Voltage Noise, e <sub>no</sub>		50			50			50			nV/Hz
RTI, 0.1 Hz to 10 Hz											
G = 1		3.0			3.0			3.0			μV p-p
G = 1000		1.5			1.5			1.5			μV p-p
Current Noise	f = 1 kHz	100			100			100			fA/√Hz
0.1 Hz to 10 Hz		1.5			1.5			1.5			pA p-p
REFERENCE INPUT											
R <sub>IN</sub>		100 ± 20%			100 ± 20%			100 ± 20%			kΩ
I <sub>IN</sub>	V <sub>IN+</sub> , V <sub>REF</sub> = 0 V	50	60		50	60		50	60		μA
Voltage Range		−V <sub>S</sub>	+V <sub>S</sub>		−V <sub>S</sub>	+V <sub>S</sub>		−V <sub>S</sub>	+V <sub>S</sub>		V
Gain to Output		1 ± 0.0002			1 ± 0.0002			1 ± 0.0002			V
POWER SUPPLY											
Operating Range	Dual supply	±2.5	±6		±2.5	±6		±2.5	±6		V
	Single supply	2.7	12		2.7	12		2.7	12		V
Quiescent Current	Dual supply	375	550		375	550		375	550		μA
	Single supply	305	480		305	480		305	480		μA
Over Temperature		625			625			625			μA
TEMPERATURE RANGE											
For Specified Performance		−40	+85		−40	+85		−40	+85		°C

## ABSOLUTE MAXIMUM RATINGS

Table 4.

Parameter	Rating
Supply Voltage	$\pm 6$ V
Internal Power Dissipation <sup>1</sup>	650 mW
Differential Input Voltage	$\pm 6$ V
Output Short-Circuit Duration	Indefinite
Storage Temperature Range	-65°C to +125°C
Operating Temperature Range	-40°C to +85°C
Lead Temperature (Soldering, 10 sec)	300°C

<sup>1</sup> Specification is for device in free air:

8-Lead PDIP Package:  $\theta_{JA} = 95^\circ\text{C}/\text{W}$

8-Lead SOIC Package:  $\theta_{JA} = 155^\circ\text{C}/\text{W}$

8-Lead MSOP Package:  $\theta_{JA} = 200^\circ\text{C}/\text{W}$ .

Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

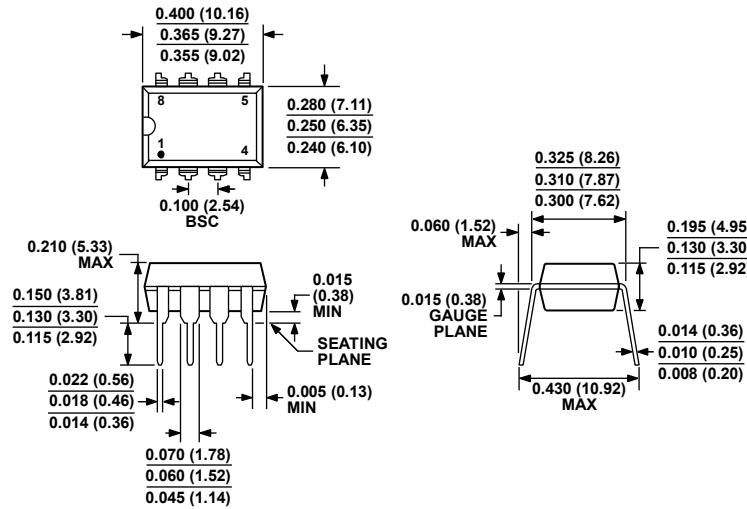
### ESD CAUTION



#### ESD (electrostatic discharge) sensitive device.

Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

## OUTLINE DIMENSIONS



COMPLIANT TO JEDEC STANDARDS MS-001

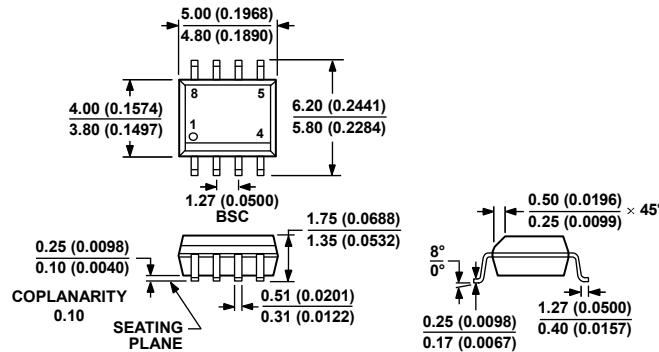
CONTROLLING DIMENSIONS ARE IN INCHES; MILLIMETER DIMENSIONS  
 (IN PARENTHESES) ARE ROUNDED-OFF INCH EQUIVALENTS FOR  
 REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN.  
 CORNER LEADS MAY BE CONFIGURED AS WHOLE OR HALF LEADS.

070505-A

Figure 56. 8-Lead Plastic Dual In-Line Package [PDIP]

Narrow Body (N-8)

Dimensions shown in inches and (millimeters)



COMPLIANT TO JEDEC STANDARDS MS-012-AA

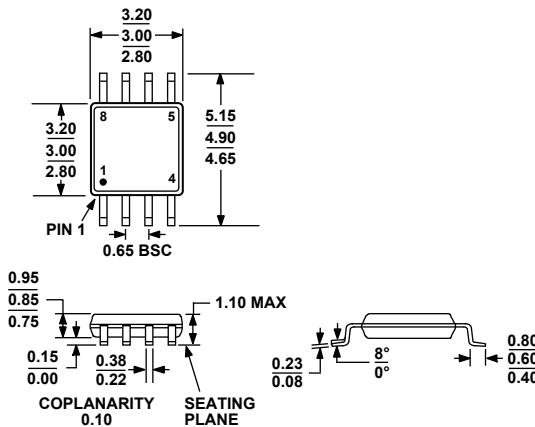
CONTROLLING DIMENSIONS ARE IN MILLIMETERS; INCH DIMENSIONS  
 (IN PARENTHESES) ARE ROUNDED-OFF MILLIMETER EQUIVALENTS FOR  
 REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN.

012407-A

Figure 57. 8-Lead Standard Small Outline Package [SOIC\_N]

Narrow Body (R-8)

Dimensions shown in millimeters and (inches)



COMPLIANT TO JEDEC STANDARDS MO-187-AA

Figure 58. 8-Lead Mini Small Outline Package [MSOP]  
(RM-8)  
Dimensions shown in millimeters

## ORDERING GUIDE

Model	Temperature Range	Package Description	Package Option	Branding
AD623AN	-40°C to +85°C	8-Lead Plastic Dual In-Line Package [PDIP]	N-8	
AD623ANZ <sup>1</sup>	-40°C to +85°C	8-Lead Plastic Dual In-Line Package [PDIP]	N-8	
AD623AR	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N]	R-8	
AD623AR-REEL	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N], 13" Tape and Reel	R-8	
AD623AR-REEL7	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N], 7" Tape and Reel	R-8	
AD623ARZ <sup>1</sup>	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N]	R-8	
AD623ARZ-R7 <sup>1</sup>	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N], 7" Tape and Reel	R-8	
AD623ARZ-RL <sup>1</sup>	-40°C to +85°C	8-Lead SOIC, 13" Tape and Reel	R-8	
AD623ARM	-40°C to +85°C	8-Lead Mini Small Outline Package [MSOP]	RM-8	JOA
AD623ARM-REEL	-40°C to +85°C	8-Lead Mini Small Outline Package [MSOP], 13" Tape and Reel	RM-8	JOA
AD623ARM-REEL7	-40°C to +85°C	8-Lead Mini Small Outline Package [MSOP], 7" Tape and Reel	RM-8	JOA
AD623ARMZ <sup>1</sup>	-40°C to +85°C	8-Lead Mini Small Outline Package [MSOP]	RM-8	JOA
AD623ARMZ-REEL <sup>1</sup>	-40°C to +85°C	8-Lead Mini Small Outline Package [MSOP], 13" Tape and Reel	RM-8	JOA
AD623ARMZ-REEL7 <sup>1</sup>	-40°C to +85°C	8-Lead Mini Small Outline Package [MSOP], 7" Tape and Reel	RM-8	JOA
AD623BN	-40°C to +85°C	8-Lead Plastic Dual In-Line Package [PDIP]	N-8	
AD623BNZ <sup>1</sup>	-40°C to +85°C	8-Lead Plastic Dual In-Line Package [PDIP]	N-8	
AD623BR	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N]	R-8	
AD623BR-REEL	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N], 13" Tape and Reel	R-8	
AD623BR-REEL7	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N], 7" Tape and Reel	R-8	
AD623BRZ <sup>1</sup>	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N]	R-8	
AD623BRZ-R7 <sup>1</sup>	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N], 7" Tape and Reel	R-8	
AD623BRZ-RL <sup>1</sup>	-40°C to +85°C	8-Lead Standard Small Outline Package [SOIC_N], 13" Tape and Reel	R-8	
EVAL-INAMP-62RZ <sup>1</sup>		Evaluation Board		

<sup>1</sup> Z = RoHS Compliant Part.