Page 1 of 2 **Design Resources** 



>> Sign Up for PSoC Newsletter

Actions

± download

± download

± download

**±** download

>> Downloads | Documentation | English | 日本 | 中国版



Cypress provides on-line sampling services to help qualified customers expedite new product

designs. Cypress reserves the right to limit the distribution of free samples for non-qualified

REQUEST SAMPLE

enterprises.

Cypress Home
Products
Applications
Design Resources◀
C-Compiler License
Design Support
Online Store
Technical Support
<b>About Cypress</b>
Investor Relations
Press
Careers

CY3632 WirelessUSB LS(TM) Development Kit (Version 1.6)

**Developer Kits** 





Help Us Improve



Online Sample Request Form

Get Information	]
PACKAGING	
Overview	

In-House Packaging Process Capability

□ Lead-Free Packaging

Last Updated 23 Sep, 2005

CY3632.jpg
130Kb, JPG File

Downloads

File Information

144Kb, PDF File CY3632\_1\_6\_Setup.zip 120Mb, ZIP File

Release Notes\_1\_6.pdf

WirelessUSB LS DVK Tutorial.pdf 2Mb, PDF File



The CY3632 WirelessUSB LS Development Kit\* supports the WirelessUSB LS family of devices. The development kit provides all the materials and documents needed to *cut the cord* on wired applications, including two radio modules that plug directly into prototyping platform boards, comprehensive WirelessUSB(TM) protocol code, and a WirelessUSB Listener Tool to analyze WirelessUSB RF transmissions. Comprehensive documentation, including the firmware design notes, application notes, data sheets, bill of materials, schematics, and gerber files for the WirelessUSB LS radio modules, is provided on a CD-ROM.

The kit comes pre-configured as a WirelessUSB LS mouse pointer. Within minutes, you'll be experiencing the true "plug and play" quality of WirelessUSB and viewing WirelessUSB packets  $\ \ \,$ 

\*PROCLAIMER: No RF expertise is required to use this kit or develop with WirelessUSB. Cutting the cord has never been easier.

## Hardware Description:

The CY3632 WirelessUSB LS Development Kit contains two base platform prototyping boards with a preprogrammed Cypress enCoRe(TM) USB low-speed controller and a Cypress PSoC(TM) microcontroller mounted to either board. Two vertical WirelessUSB LS RF radio modules plug directly into each prototyping board. The kit also features our WirelessUSB Listener Tool that enables the developer to analyze wireless data between the host and the peripheral devices. A power cord, cables, connectors, batteries, and a chip kit with preprogrammed and blank enCoRe and PSoC devices for development are also provided.

## Revisions

There have been some significant enhancements and additions to the WirelessUSB LS Development Kit v1.6 over previous kit revisions. The feature set of the WirelessUSB LS DVK firmware has been expanded with the new tutorials and certification tool. Cypress has made the

Page 2 of 2 **Design Resources** 

features of previously supported applications even more robust.

The WirelessUSB LS DVK firmware now includes the following features:

- Tutorials that introduce basic radio functionality, including Transmit, Receive, Protocol, Multi Byte.
- Support for EMC Tester tool for Telec, FCC, and ETSI certifications.
   PSoC Designer 4.2 Service Pack 2 BETA. Previous versions of PSoC Designer are no longer
- · Protocol optimizations and bug fixes

# **Features Summary**

- Radio Support: WirelessUSB LS radio-system-on-a-chip

- Radio Support: WirelessUSB LS radio-system-on-a-chip
  Throughput: 64 kbps
  Directionality: 2-way (with ACK and retransmission)
  Devices per Bridge Receiver: 1:1 and 2:1 (for simultaneous keypad and mouse support)
  Peripheral Support: Keypad, Mouse, Gamepad, and RS-232
  Binding: Basic (gamepad only), Automatic

- Error Detection (Corrupt Packets): Yes
   Error Correction: Yes, up to 8 bits per packet
   Interference Avoidance Algorithm: Yes, automatically selects new channel if channel is corrupt (with automatic bind)
- WirelessUSB Listener Tool: Supports future firmware updates
   Wireless USB Certification Tool: Telec, FCC, and ETSI certifications
   Tutorials: Transmit, Receive, Protocol, Multi Byte

#### Notes:

Version 1.6 of the CY3632 was released with PSoC Designer v4.2 Service Pack 2 BETA, which is contained in the .zip file below. For customers desiring to use Service Pack 2 final, please refer to our Knowledge Base article "How do I port DVK v1.6 PSoC Firmware to PSoC Designer v4.2 + SP2 final? (20690)"

Item ID: DK10054

Related Products: WirelessUSB, PSoC Mixed-Signal Array, USB Low-Speed Peripherals

 $\textbf{Related Part Numbers:} \ \ \underline{CYWUSB6934}, \ \underline{CYWUSB6932}, \ \underline{CY8C27643}, \ \underline{CY7C637xx}$ 

Related Applications: Head Unit and Multimedia Controller

### Need more help?

- Search KnowledgeBase™
  Create a Case
- · Contact your Sales Office

Contact Us | Privacy | Terms & Conditions | Online Store | Site Map | 日本語 | 中文 | Help Us Improve © Copyright 1995-2006. Cypress Semiconductor Corporation. All rights reserved.