



Parameters	Ratings	Units
Load Voltage	400	V _p
Load Current	150	mA
Max R _{ON}	22	Ω

Features

- 3750V_{rms} Input/Output Isolation
- Small 8-Pin Package
- Low Drive Power Requirements (TTL/CMOS Compatible)
- High Reliability
- Arc-Free With No Snubbing Circuits
- VDE Compatible
- FCC Compatible
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable
- Surface Mount and Tape Reel Version Available.

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

PAA110 is a 400V, 150mA, 22Ω, dual normally open (1-Form-A) optically isolated Solid State Relay. Its efficient MOSFET switches and photovoltaic die use Clare's patented OptoMOS architecture to provide 3750V_{rms} of input to output isolation. The optically coupled output is controlled by a highly efficient GaAIAs infrared LED.

This performance leader provides high peak load voltage handling capability and improved peak load current handling.

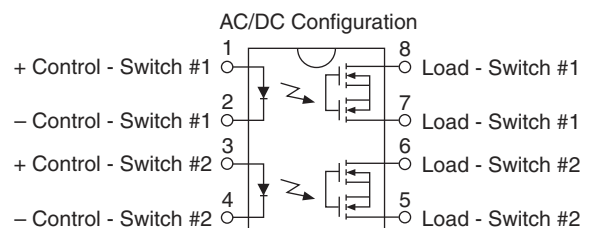
Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component:
TUV Certificate B 09 07 49410 004

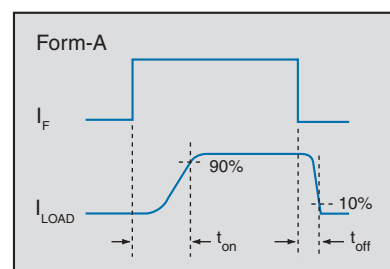
Ordering Information

Part #	Description
PAA110	8-Pin DIP (50/Tube)
PAA110P	8-Pin Flatpack (50/Tube)
PAA110PTR	8-Pin Flatpack (1000/Reel)
PAA110S	8-Pin Surface Mount (50/Tube)
PAA110STR	8-Pin Surface Mount (1000/Reel)

Pin Configuration



Switching Characteristics of Normally Open Devices



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	400	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

² Derate linearly 6.67 mW / °C

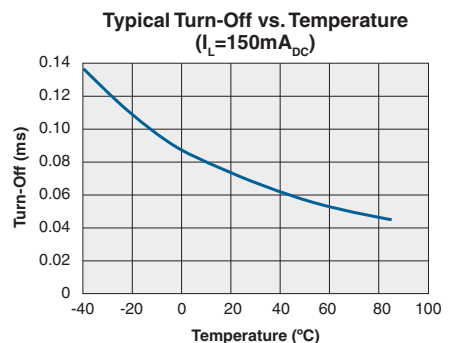
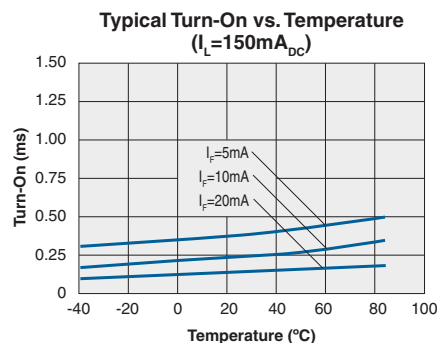
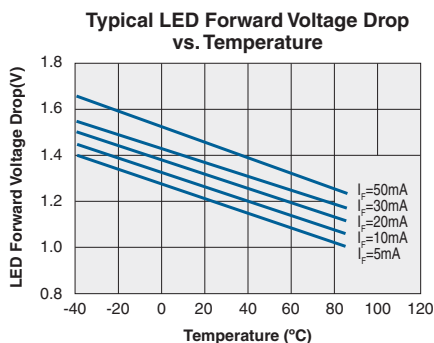
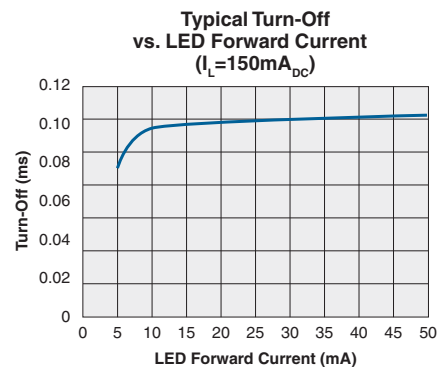
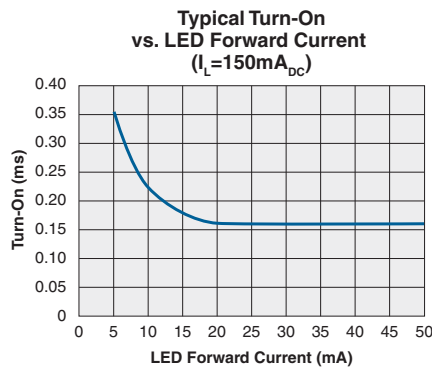
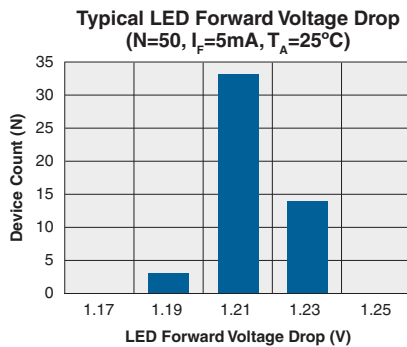
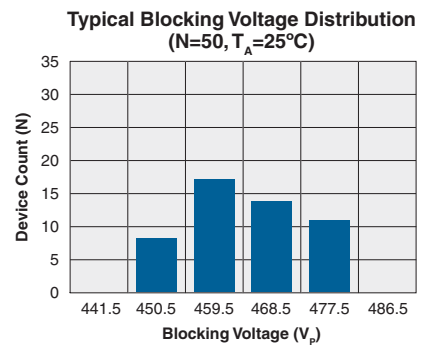
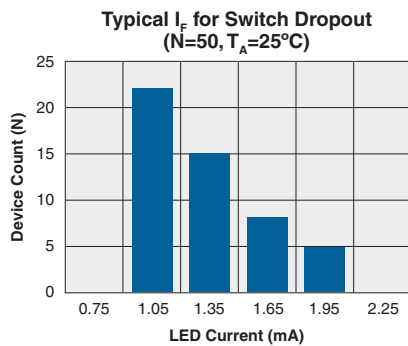
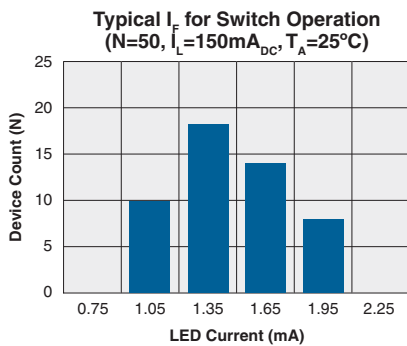
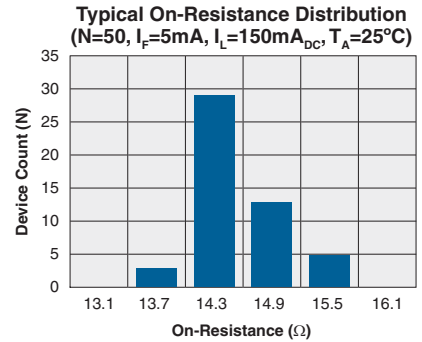
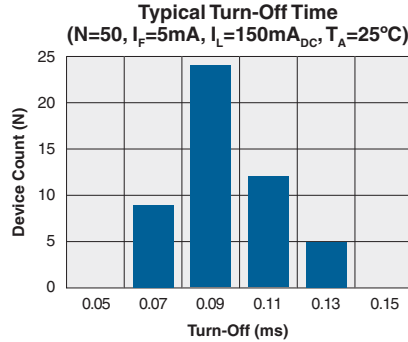
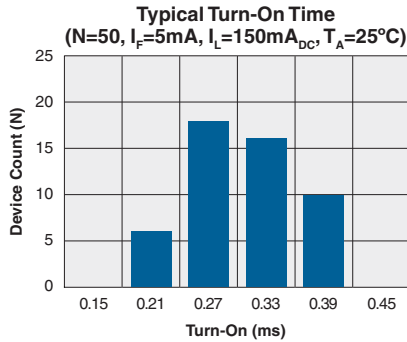
Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current *						
Continuous, AC/DC Configuration	-	I _L	-	-	150	mA
Peak	t=10ms	I _{LPK}	-	-	400	
On-Resistance, AC/DC Configuration	I _L =150mA	R _{ON}	-	15	22	Ω
Off-State Leakage Current	V _L =400V _P	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	t _{on}	-	-	1	ms
Turn-Off		t _{off}	-	-	0.25	
Output Capacitance	V _L =50V, f=1MHz	C _{OUT}	-	25	-	pF
Input Characteristics						
Input Control Current	I _L =150mA	I _F	-	-	5	mA
Input Dropout Current	-	I _F	0.4	0.7	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics						
Input to Output Capacitance	-	C _{IO}	-	3	-	pF

*NOTE: If both poles operate simultaneously, then load current must be derated so as not to exceed the package power dissipation value.

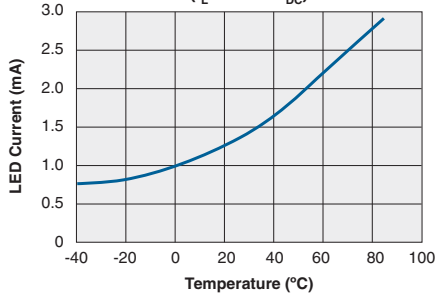
PERFORMANCE DATA *



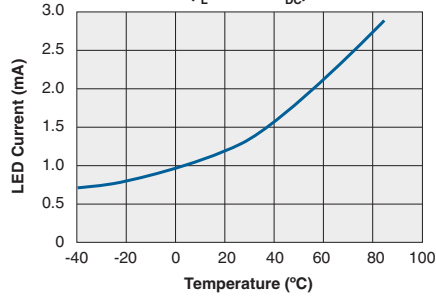
*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA *

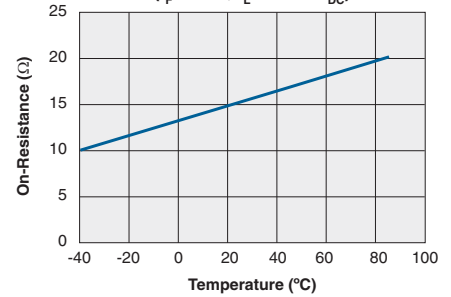
Typical I_F for Switch Operation vs. Temperature
($I_L=150\text{mA}_{DC}$)



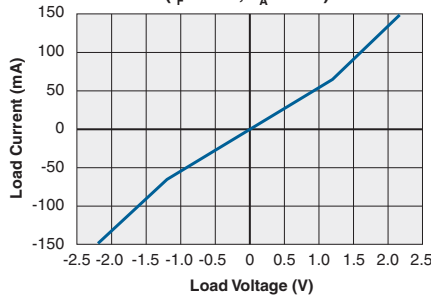
Typical I_F for Switch Dropout vs. Temperature
($I_L=150\text{mA}_{DC}$)



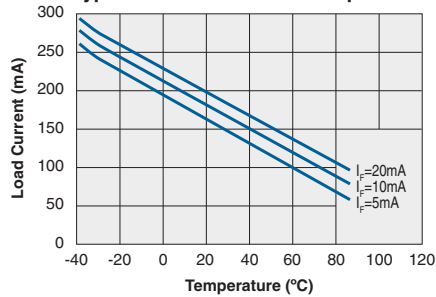
Typical On-Resistance vs. Temperature
($I_F=5\text{mA}$, $I_L=150\text{mA}_{DC}$)



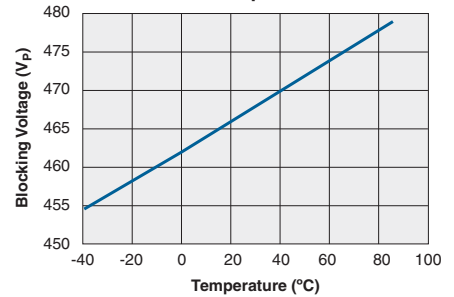
Typical Load Current vs. Load Voltage
($I_F=5\text{mA}$, $T_A=25^\circ\text{C}$)



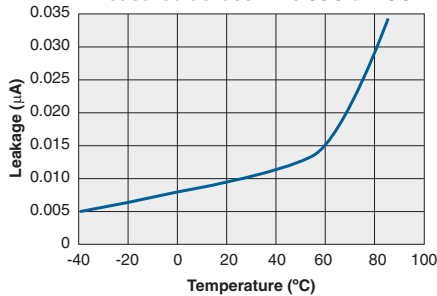
Typical Load Current vs. Temperature



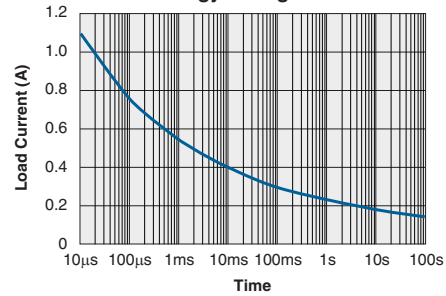
Typical Blocking Voltage vs. Temperature



Typical Leakage vs. Temperature
Measured across Pins 5&6 or 7&8



Energy Rating Curve



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. Clare classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
PAA110 / PAA110S / PAA110P	MSL 1

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
PAA110 / PAA110S	250°C for 30 seconds
PAA110P	260°C for 30 seconds

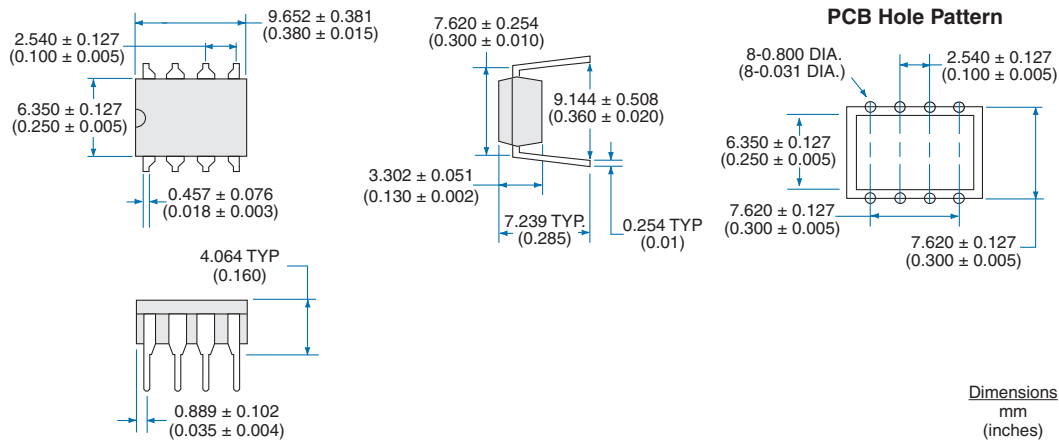
Board Wash

Clare recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since Clare employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine-based or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

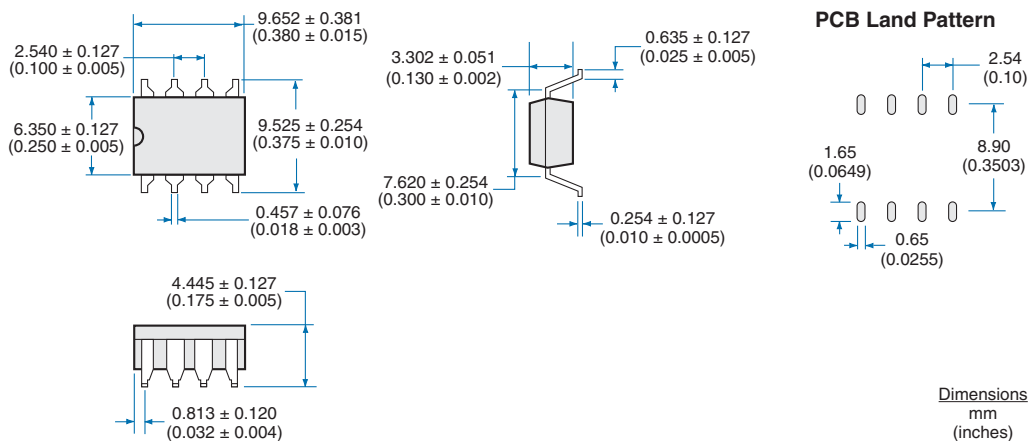


MECHANICAL DIMENSIONS

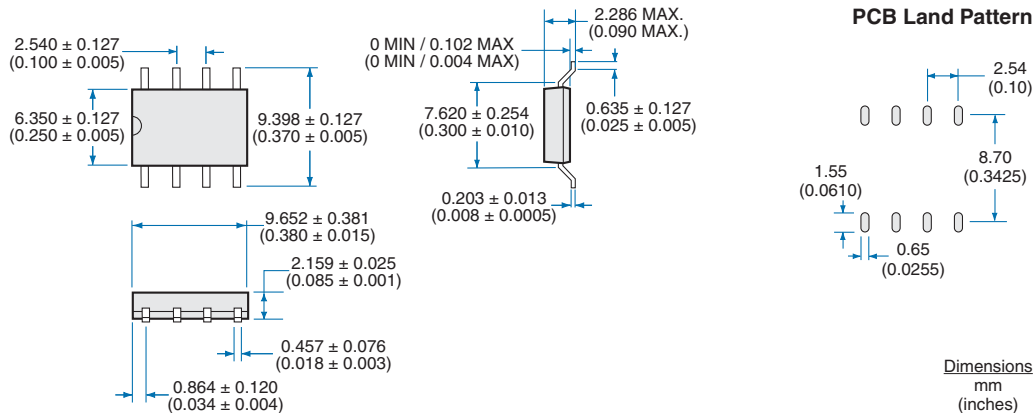
PAA110



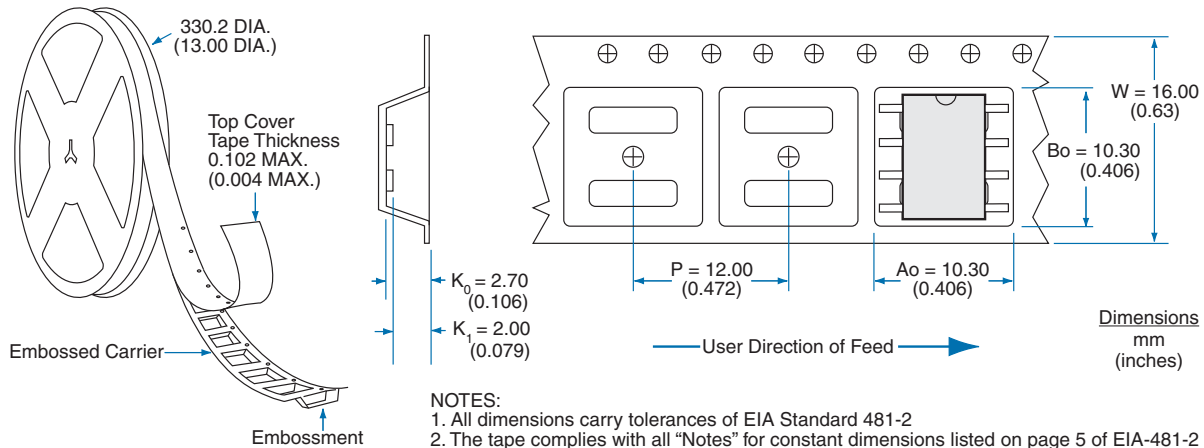
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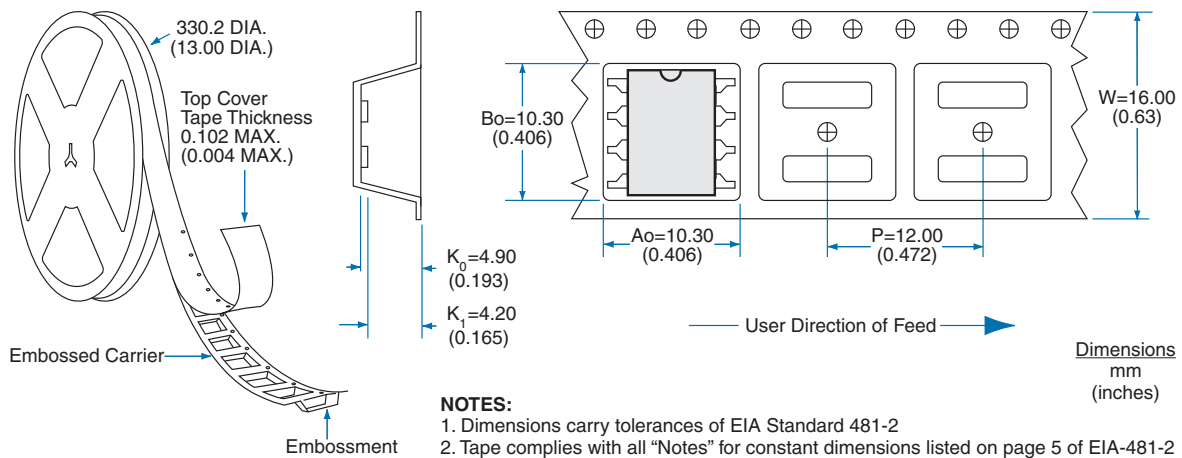
PAA110P



PAA110P Tape & Reel



PAA110S Tape & Reel



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