

New Product VSIB15A20 thru VSIB15A80

Vishay General Semiconductor

# Single-Phase Single In-Line Bridge Rectifiers



15 A

200 V to 800 V

200 A

10 µA

1.0 V

<b>FEATURES</b>	5
-----------------	---

- UL recognition file number E54214
- · Thin single in-line package
- Glass passivated chip junction
- High surge current capability
- High case dielectric strength of 2500 V<sub>BMS</sub>
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### **TYPICAL APPLICATIONS**

General purpose use in ac-to-dc bridge full wave rectification for switching power supply, home appliances, office equipment, industrial automation applications.

#### **MECHANICAL DATA**

Case: GSIB-5S

Epoxy meets UL 94 V-0 flammability rating

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

E3 suffix for consumer grade, meets JESD 201 class 1A whisker test

Polarity: As marked on body

Mounting Torque: 10 cm-kg (8.8 inches-lbs) max. Recommended Torque: 5.7 cm-kg (5 inches-lbs)

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	VSIB15A20	VSIB15A40	VSIB15A60	VSIB15A80	UNIT	
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	200	400	600	800	V	
Maximum RMS voltage	V <sub>RMS</sub>	140	280	420	560	V	
Maximum DC blocking voltage	V <sub>DC</sub>	200	400	600	800	V	
$ \begin{array}{ll} \mbox{Maximum average forward rectified} & T_{C} = 107 \ ^{\circ}C \ ^{(1)} \\ \mbox{output current at} & T_{A} = 25 \ ^{\circ}C \ ^{(2)} \end{array} $	I <sub>F(AV)</sub>	15 3.5				А	
Peak forward surge current single sine-wave superimposed on rated load	I <sub>FSM</sub>	200			А		
Rating for fusing (t < 8.3 ms)	l <sup>2</sup> t	166			A <sup>2</sup> s		
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	T <sub>STG</sub> - 55 to + 150				°C	

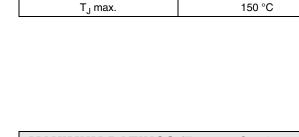
#### Notes:

(1) Unit case mounted on aluminum plate heatsink

(2) Units mounted on P.C.B. without heatsink

Document Number: 84652 Revision: 15-Dec-08

For technical questions within your region, please contact one of the following: PDD-Americas@vishay.com, PDD-Asia@vishay.com, PDD-Europe@vishay.com RoHS COMPLIANT



**PRIMARY CHARACTERISTICS** 

I<sub>F(AV)</sub>

V<sub>RRM</sub>

I<sub>FSM</sub>

 $I_{B}$ 

 $V_{F}$ 

# VSIB15A20 thru VSIB15A80



Vishay General Semiconductor

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS	SYMBOL	VSIB15A20	VSIB15A40	VSIB15A60	VSIB15A80	UNIT
Maximum instantaneous forward voltage drop per diode	7.5 A	V <sub>F</sub>	1.00			V	
Maximum DC reverse current at rated DC blocking voltage per diode	T <sub>A</sub> = 25 °C T <sub>A</sub> = 125 °C	I <sub>R</sub>	10 250			μΑ	

<b>THERMAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	VSIB15A20	VSIB15A40	VSIB15A60	VSIB15A80	UNIT
Typical thermal resistance	$R_{ extsf{ heta}JA}\ R_{ extsf{ heta}JC}$	22 <sup>(2)</sup> 1.5 <sup>(1)</sup>			°C/W	

#### Notes:

(1) Unit case mounted on aluminum plate heatsink

(2) Units mounted on P.C.B. without heatsink

(3) Recommended mounting position is to bolt down on heatsink with silicone thermal compound for maximum heat transfer with #6 screw

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
VSIB15A60-E3/45	7.0	45	20	Tube			

#### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

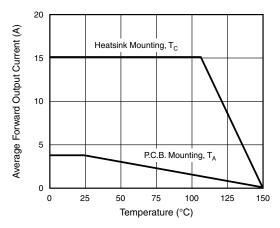
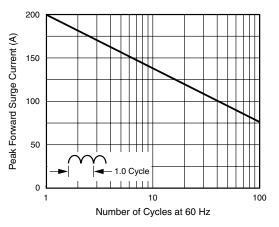
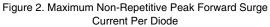


Figure 1. Derating Curve Output Rectified Current





For technical questions within your region, please contact one of the following: <u>PDD-Americas@vishay.com</u>, <u>PDD-Asia@vishay.com</u>, <u>PDD-Europe@vishay.com</u>



### **New Product** VSIB15A20 thru VSIB15A80

## Vishay General Semiconductor

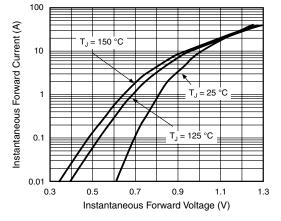


Figure 3. Typical Forward Characteristics Per Diode

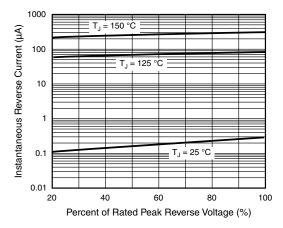
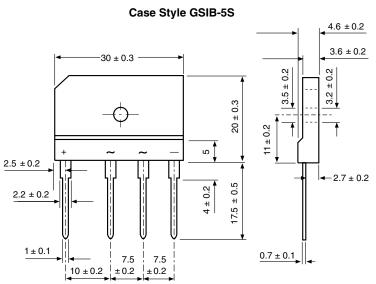


Figure 4. Typical Reverse Characteristics Per Diode





Document Number: 84652 Revision: 15-Dec-08

For technical questions within your region, please contact one of the following: PDD-Americas@vishay.com, PDD-Asia@vishay.com, PDD-Europe@vishay.com

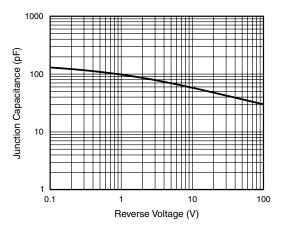


Figure 5. Typical Junction Capacitance Per Diode

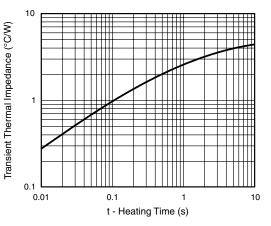


Figure 6. Typical Transient Thermal Impedance





Vishay

## Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.